OPERATION AND MAINTENANCE MANUAL

360

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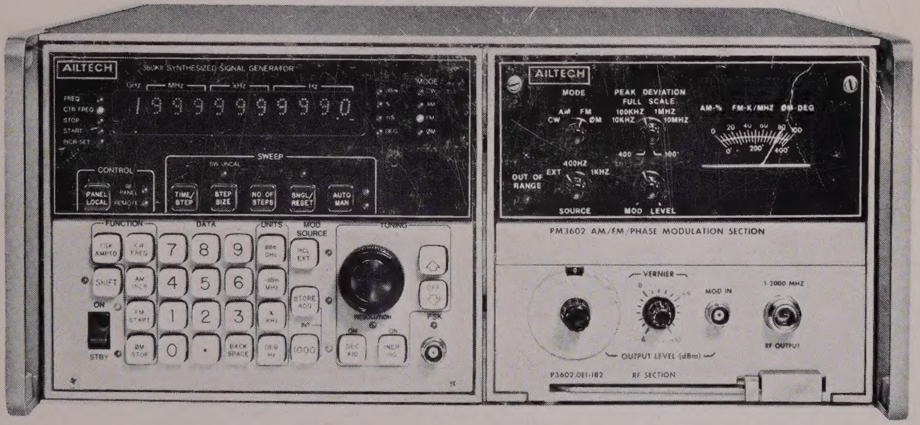
380 SYNTHESIZED SIGNAL GENERATOR

380K11 MAINFRAME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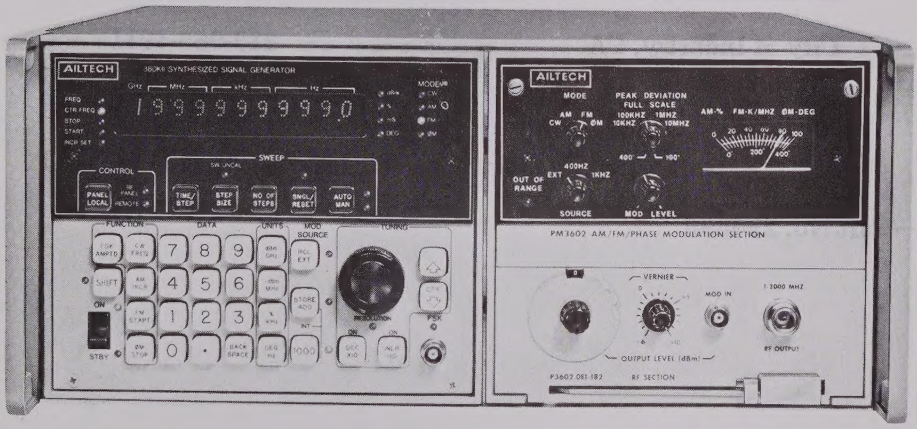
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CTR FREQ
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START
INCR SET
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MODE
CW
AM
FM
ØM
CONTROL
PANEL LOCAL
REMOTE
SWEEP
TIME/STEP
STEP SIZE
NO OF STEPS
SNGL/RESET
AUTO MAN
FUNCTION
DATA
UNITS
MOD SOURCE
TUNING
SHIFT
ON
STBY
BACK SPACE
STORE ADD
INT
RESOLUTION
PSK
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MODE
PEAK DEVIATION FULL SCALE
AM FM
CW ØM
100KHZ 1MHZ
10KHZ 10MHZ
AM-% FM-K/MHZ ØM-DEG
400° 100°
400HZ
EXT 1KHZ
OUT OF RANGE
SOURCE
MOD LEVEL
PM3602 AM/FM/PHASE MODULATION SECTION
VERNIER
MOD IN
1-2000 MHZ
RF OUTPUT
OUTPUT LEVEL (dBm)
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HANDBOOK — Part No. 296593

FOREWORD

The 380 Series Synthesized Signal Generator Manual comprises a complete handbook when used in conjunction with the proper plug-in and modulation manuals.

The 380K11 Mainframe and 3600E1 Frequency Extender Manual contains general information relating to the operational characteristics of the instrument and information required to service and maintain the instrument. Included are schematics, troubleshooting procedures, system calibration instructions and a replaceable parts list.

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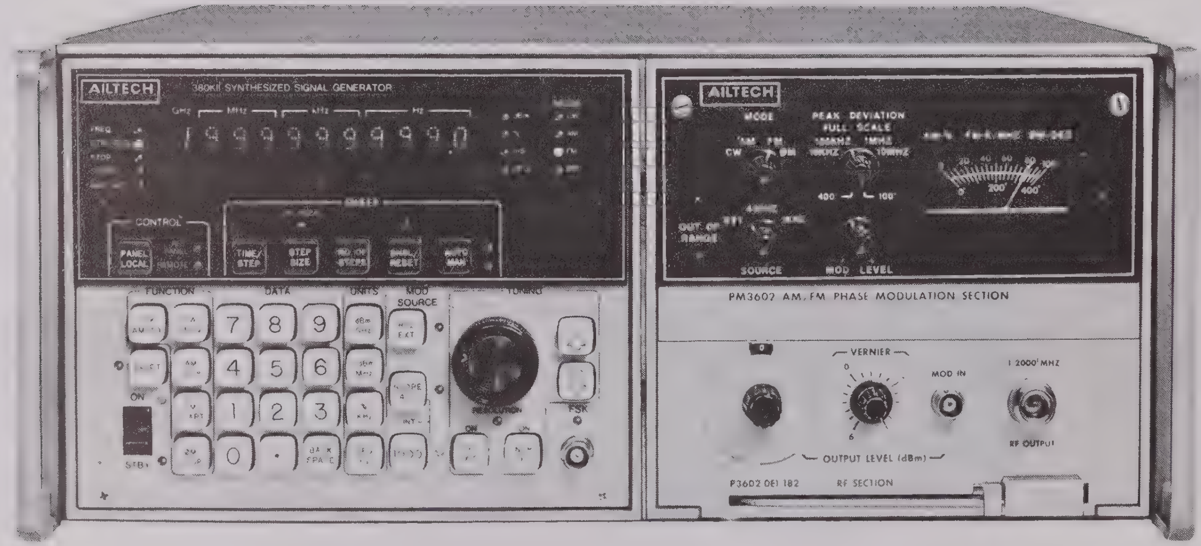

Figure 1-1. 380K11 Synthesized Signal Generator

CHAPTER 1

GENERAL INFORMATION

1-1. INTRODUCTION

1-2. The 380K11 Mainframe and 3600E1 Frequency Extender (Figure 1-1) are used with the P3600 Series RF Sections and PM3600 Series Modulation Sections. When fully assembled, these items make up a 380 Direct Synthesized Signal Generator whose frequency range depends upon the RF Section installed, with modulation capabilities determined by the specific Modulation Section in use. Refer to Table 1-1 for a complete listing of 380 Direct Synthesized Signal Generators and the options that are available to the user.

1-3. This manual contains the following information for the 380K11 Mainframe and the 3600E1 Frequency Extender and is divided into seven chapters as follows:

Chapter 1, GENERAL INFORMATION, provides brief physical and functional descriptions of the mainframe and frequency extender, 380 RF specifications, a list of component subassemblies, and a list of test equipment required for maintenance.

Chapter 2, INSTALLATION, provides information necessary for unpacking, initial turn-on, and initial checkout.

Chapter 3, OPERATION, provides information on how to operate the mainframe and frequency extender in conjunction with the RF section and modulation section to obtain a desired output.

Chapter 4, PERFORMANCE TESTS, provides equipment requirements and preliminary operations and performance tests.

Chapter 5, SYSTEM CALIBRATION AND ADJUSTMENTS, contains information to properly adjust and align the instrument.

Chapter 6, MAINTENANCE, provides information necessary to troubleshoot, repair, and adjust the mainframe and frequency extender.

Chapter 7, REPLACEABLE PARTS LIST, provides a list of parts contained in the mainframe and the frequency extender.

1-4. DESCRIPTION

1-5. **MAINFRAME.** The 380K11 Mainframe is 7 inches high, 16 3/4 inches wide, 25 1/2 inches deep and weighs 68 pounds. It produces frequencies in the range of 35 to 35.999999 MHz in steps as small as 0.1 Hz. The 3600E1 Frequency Extender adds 1 MHz increment to the 35 - 36 MHz output of the mainframe, covering 30 to 40 MHz. The 30 to 40 MHz output contains step sizes from 0.1 Hz (depending on the option) to 1 MHz. General specifications for the 380 Series are listed in Table 1-2 and the RF Specifications are listed in Table 1-3. Any of the RF Sections take this input, add the more significant steps and generate the RF frequency within its range.

TABLE 1-1. 380 SYNTHESIZER CONFIGURATIONS AND AVAILABLE OPTIONS

SYNTHESIZER CONFIGURATION	MODEL					
	380	380M	381	381M	382	382M
380K11 Mainframe	●	●	●	●	●	●
3600E1 Frequency Extender	●	●	●	●	●	●
P360180E1 180 MHz RF Section	●	●				
P3601.8E1 1800 MHz RF Section			●	●		
P3602.0E1 2000 MHz RF Section					●	●
PM3601 AM Modulation	●		●		●	
PM3602 AM/FM/ϕModulation		●		●		●
AVAILABLE SYNTHESIZER OPTIONS						
Option 001, Increased Resolution	●	●	●	●	●	●
Option 002, Frequency Standard 3 X 10^{-8}	●	●	●	●	●	●
Option 003, Frequency Standard 1 X 10^{-9}	●	●	●	●	●	●
Option 004, 50-400 Hz Operation	●	●	●	●	●	●
Option 006, Rack Mount Adapter	●	●	●	●	●	●
Option 007, Slide Adapter Kit	●	●	●	●	●	●
Option 181, 150 dB Attenuator	●	●				
Option 182, 130 dB Attenuator			●	●		
Option 183, 130 dB Attenuator					●	●
Service Accessory 299252	●	●	●	●	●	●

1-6. All frequencies are derived from a reference input. The reference can be an externally applied 5 or 10 MHz signal, or from an optional internal 10 MHz crystal controlled, oven-stabilized source. Two internal crystal options are available.

1-7. Frequency control of the RF output is either by front panel keyboard entry or via remote control inputs. The remote control can be either parallel BCD or IEEE 488 Bus inputs.

1-8. **FREQUENCY EXTENDER.** The 3600E1 Frequency Extender consists of five modules installed in the lower deck of the mainframe. The unit adds 1 MHz steps to the 35 - 36 MHz output of the mainframe, and covers 30 to 40 MHz. There are no operator controls or external connections on the frequency extender. The frequency extender and its location in relation to the mainframe is illustrated in Figure 1-2.

1-9. **OPTIONS.** The following options are available for the synthesized signal generator and are described below:

- Option 001 adds one additional decade to the mainframe, improving the basic mainframe/frequency extender resolution to 0.1 Hz. Actual output resolution of the complete synthesizer is dependent upon the RF section installed.
- Option 002 provides an internal 10-MHz reference signal, crystal-controlled and oven-stabilized with an aging rate of 3 X 10^{-8}/24 hr (after 72 hours warm-up).
- Option 003 provides an internal 10-MHz reference signal, crystal-controlled and oven-stabilized with an aging rate of 1 X 10^{-9}/24 hr (after 72 hours warm-up).
- Option 004 provides for operation from a 50- to 400-Hz ac line frequency.
- Option 006 is a rack mounting adapter kit which provides the means to mount the 380 in a standard 19-inch rack. (Refer to paragraph 2-14.) Slides are not provided.
- Option 007 is a rack mount slide adapter kit which provides the means for mounting the 380 in a rack with chassis slides. (Refer to paragraph 2-15.)
- Option 181 provides control of the output amplitude over the range of +13 dBm to -150 dBm from the front panel.
- Options, 182/183 provide control of the output amplitude over the range of +10 dBm to -136 dBm from the front panel.
- Service Accessory (Refer to paragraph 1-10.)

1-10. **ACCESSORIES.** A service kit part number 299252 is available for maintenance purposes, see Figure 1-3. Table 1-4 lists the items contained in the kit. The items marked with an asterisk are applicable to the mainframe/frequency extender combination.

TABLE 1-2. GENERAL SPECIFICATIONS

Frequency Selection	Keyboard control of frequency. Knob tuning or UP/DOWN keys for incrementing frequency in decade values or in selected increments.
Frequency Indication	Indicates to 0.1 Hz resolution on an 11 digit LED display. Resolution of output frequency dependent upon frequency range and option.
Reference Oscillator	Internal: Options 002 and 003. External: 5 or 10 MHz, 0 dBm, 50 ohms, BNC rear panel input.
Reference Output	Rear panel BNC connector provides 10 MHz reference output at +3 dBm nominal, 50 ohms.
Output Level Selection	Keyboard control, knob tuning or UP/DOWN keys for incrementing level in 0.5 dB steps with the 180 series attenuator options. The RF panel controls can be activated (disabling the keyboard) allowing calibrated control knob and vernier adjustment of level. For instruments without attenuator options, level is adjusted with a calibrated vernier control on the RF panel.
Output Level Indication	LED display indicates level in dBm with 0.5 dB increments when AMPTD key is depressed for units with 180 series attenuator options. When the RF panel is activated, level is controlled by the RF panel controls. Level is then indicated on the calibrated control knobs, and the LED display is disabled for level.
Modulation Selection	Instruments with PM3601 External AM Modulator provide AM and pulse modulation capability without controls or indication. Instruments with PM3602 AM/FM/ϕM Modulator (M Suffix) provide keyboard and programmed control of modulating mode, modulation source, and modulation level.

TABLE 1-2. GENERAL SPECIFICATIONS (Cont)

Modulation Selection (Cont)	Modulation level can be entered through the data keys, and changed in small increments by the tuning knob or the UP/DOWN keys. Increments are 0.5% steps for AM. For FM, increments are 50 Hz steps up to 10 kHz, 500 Hz steps from 10 kHz to 100 kHz, 5 kHz steps from 100 kHz to 1 MHz and 50 kHz from 1 MHz to 10 MHz. For phase modulation, increments are 0.5° up to 100° and 2.0° from 100° to 400°. Modulator panel controls can be enabled (disabling the keyboard) allowing continuous vernier adjustment of modulation level.
Modulation Indication	For instruments with M suffix, LED display indicates AM modulation in %, FM modulation in Hz, and Phase modulation in degrees, when the proper function key is depressed. Modulation meter on modulator panel also reads modulation level when entered from keyboard or modulation panel. LED modulation indication is disabled when the modulation panel control is enabled.
Operating Temperature Range	0° to +50°C
Power Requirements	115 or 230V ± 10%, 48 to 66 Hz 280 VA
Size	178 mm H x 426 mm W x 648 mm D (7" x 16 ¾" x 25 ½")
Weight	Net: 41 kg (91 lb.) Shipping: 52 kg (115 lb.)

TABLE 1-3. RF SPECIFICATIONS

FREQUENCY CHARACTERISTICS

<table>
<tr><th></th><th>380</th><th colspan="3">381</th><th>382</th></tr>
<tr><td>Frequency Range</td><td>.01 - 179.999999 MHz</td><td colspan="3">1.0 - 1799.99999 MHz</td><td>1.0 - 1999.99999 MHz</td></tr>
<tr><td>Frequency Resolution</td><td>1 Hz
0.1 Hz with Main Frame Option 001</td><td>Output Frequency
< 60 MHz
> 60 MHz</td><td colspan="2">Standard
1 Hz
10 Hz</td><td>Mainframe Option 001
0.1 Hz
1.0 Hz</td></tr>
<tr><td>Frequency Accuracy and Stability</td><td colspan="5">Same as reference oscillator (±3 x 10⁻⁸ /24 hours with mainframe option 002, ±1 x 10⁻⁹ /24 hours with mainframe option 003)</td></tr>
<tr><td>Harmonics</td><td>< −30 dBc, 1.0 to 180 MHz</td><td colspan="4">< −20 dBc, CF > 3 MHz(3)
< −15 dBc, CF < 3 MHz(3)</td></tr>
<tr><td>Sub Harmonics and Multiples (f/2, 3f/2, etc.)</td><td>N/A</td><td colspan="3">< 900 MHz, N/A
> 900 MHz, < −20 dBc</td><td>< 1000 MHz, N/A
> 1000 MHz, < −20 dBc</td></tr>
<tr><td>Signal to AM Noise (2)</td><td>> 84 dB</td><td colspan="4">> 70 dB</td></tr>
</table>

	Programming Mode	μP Time	Settling Time	Total Switching Time
Frequency Switching Time to be within 0.01 radians of final phase for 11-digits of frequency change	BCD Parallel	0	20μs	20μs (1)
	GPIB	19.9ms	20μs	20ms
	Fast Learn	84μs	20μs(1)	104μs

1. For 1800 MHz & 2000 MHz units, switching time is < 80 microseconds for a frequency step from above 10 MHz to below 10 MHz.
2. Measured in a ±15 KHz band, excluding a ±0.5 Hz band centered on the carrier.
3. For output levels below +5 dBm with 182/183 attenuator options.

SPECTRAL PURITY

Guaranteed SSB Phase Noise in a 1 Hz BW, including effects of internal standard; options 002 or 003.

OUTPUT FREQUENCY		SPURIOUS SIGNALS	PHASE NOISE AT OFFSET FREQUENCY		
380 MHz	381, 382 MHz	dBc	1 kHz dBc/Hz	10 KHz dBc/Hz	100 kHz dBc/Hz
.01 - 60	1 - 60	−100	−121	−126	−131
—	60 - 80	−98	−119	−124	−129
60 - 180	80 - 160	−95	−113	−118	−123
—	160 - 320	−86	−107	−112	−117
—	320 - 900	−80	−101	−106	−111
—	900 - 1800	−74	−95	−100	−105
—	1800 - 2000	−74	−95	−100	−105

TABLE 1-3. RF SPECIFICATIONS (Cont)

OUTPUT CHARACTERISTICS			
	380	381	382
Level Range	Std, +13 to 0 dBm 181 opt, +13 to −150 dBm	Std, +10 to 0 dBm 182 opt, +10 to −136 dBm	Std, +10 to 0 dBm 183 opt, +10 to −136 dBm
Level Accuracy	Std, ±1.0 dB 181 opt, ±1.5 dB	Std, ±2.0 dB 182 opt, ±3 dB, +10 to −50 dBm ±3.5 dB, −50 to −136 dBm	Std, ±2.0 dB 183 opt, ±3 dB, +10 to −50 dBm ±3.5 dB, −50 to −136 dBm
Leveling	Std, ±0.5 dB, f > 1 MHz ±1 dB, f < 1 MHz 181 opt, ±1.0 dB, f > 1 MHz ± 1.5 dB, f < 1 MHz	Std, ±1.5 dB 182 opt, ±2.5 dB	Std, ±1.5 dB 183 opt, ±2.5 dB
Programming Resolution	Std. N/A 181 opt, 0.5 dB	Std. N/A 182 opt, 0.5 dB	Std. N/A 183 opt, 0.5 dB
Impedance	50 Ω	50 Ω	50 Ω
VSWR	Std < 2.6 181 opt, < 2.6 above 0 dBm < 1.4 below 0 dBm	Std < 2.6 182 opt, < 2.6 above −6 dBm < 1.4 below −6 dBm	Std < 2.6 183 opt, < 2.6 above −6 dBm < 1.4 below −6 dBm

	Programming Mode	μP Time	Settling Time	Total Switching Time
Output Level Switching Time (180 series options only)	BCD Parallel	0	10ms	10ms
	GPIB	20ms	10ms	30ms

TABLE 1-3. RF SPECIFICATIONS (Cont)

AMPLITUDE MODULATION

PM3601 or PM3602 Modulators	380, 380M .01 - 180 MHz	381, 381M 1 - 1800 MHz	382, 382M 1 - 2000 MHz
Modulation Depth	0 - 95%(1)	0 - 90%(2)	
Indicated AM Accuracy at 400 and 1000 Hz Rates (M - suffix only)	±5% of Full Scale (100%)	±5% of Full Scale (100%)	
AM Distortion, THD. 400 and 1000 Hz Rates			
30% AM	1.0%	1%(3)	
90% AM	3.0%(1)	5%(3)	
AM 3 dB Bandwidth 0 - 70%	DC - 200 Hz, CF < 0.4 MHz DC - 10 kHz, 0.4 ≤ CF < 4 MHz DC - 100 kHz, CF > 4 MHz	DC - 10 kHz, 2 MHz < CF <10 MHz(4) DC - 80 kHz, CF ≥ 10 MHz	
0 - 90%	DC - 100 Hz, CF < 0.4 MHz DC - 5 kHz, 0.4 ≤ CF 4 MHz DC - 50 kHz, CF ≥ 4 MHz	DC - 5 kHz, 2 MHz < CF < 10 MHz(2) DC - 50 kHz, CF ≥ 10 MHz(2)	
Internal Oscillator (M - suffix only)	400 and 1000 Hz	400 and 1000 Hz	
Remote Programming (M - suffix only)	Fully programmable, external modulation input level is 1.0 VRMS for full scale.		
Programming Resolution (M - suffix only)	0.5% Increments		

1. For input levels below +9 dBm. With 181 ATTENUATOR OPTIONS, for levels below +7 dBm.
2. For output levels below +7 dBm. With 182/183 ATTENUATOR OPTIONS, for levels below +3 dBm.
3. For output levels between +0 and +7 dBm. With 182/183 OPTIONS, for Vernier level settings between —4 and +3 dBm.
4. For output levels between +2 and +7 dBm. With 182/183 OPTIONS, for levels between 0 and +5 dBm.

FREQUENCY MODULATION

PM 3602 Modulation Section	380M .01 - 180 MHz	381M 1 - 1800 MHz	382M 1 - 2000 MHz
Rate	20 Hz to 300 KHz		
FM Center Frequency Long Term Stability	Same as in CW Mode		
Maximum Peak Deviation	1 MHz	1 MHz, CF < 320 MHz 10 MHz, CF ≥ 320 MHz	1 MHz, CF < 400 MHz 10 MHz, CF ≥ 400 MHz
Incidential AM	< - 70 dB with 75 KHz peak deviation at a 1 KHz rate		

TABLE 1-3. RF SPECIFICATIONS (Cont)

FREQUENCY MODULATION (Cont)			
	380M .01 - 180 MHz	381M 1 - 1800 MHz	382M 1 - 2000 MHz
FM Distortion (Internal Oscillator)	< 2% up to 100 KHz dev.	< 2% up to 100 KHz dev. for CF < 320 MHz < 2% up to 1 MHz dev. for CF ≥ 320 MHz	
Internal Oscillator	400 and 1000 Hz		
Full Scale Ranges	0 - 10 KHz, 0 - 100 KHz, 0 - 1 MHz, 0 - 10 MHz		
Indicated FM Accuracy (up to 20 KHz rates)	±5% of Full Scale Range		
Remote Programming	Modes, range, and level. External modulation input level is 1.0 VRMS for full scale.		
Programming Resolution	0.5% of Selected Range		
PHASE MODULATION			
PM 3602 Modulation Section	380M .01 - 180 MHz	381M 1 - 1800 MHz	382M 1 - 2000 MHz
Rate	DC to 300 kHz		
Maximum Peak Deviation	100°, CF < 60 MHz 200°, CF ≥ 60 MHz	100°, CF < 80 MHz 200°, 80 to 160 MHz 400°, CF ≥ 160 MHz	100°, CF < 100 MHz 200°, 100 to 200 MHz 400°, CF > 200 MHz
ϕM Distortion (Internal Oscillator)	< 5%		
Internal Oscillator	400 and 1000 Hz		
Full Scale Ranges	0 - 100°, 0 - 400°		
Indicated Accuracy	± 5% of Full Scale Range		
Remote Programming	Mode, range and level. External modulation input level is 1.0 vrms for full scale.		
Programming Resolution	0.5% of Selected Range		
PULSE MODULATION			
PM 3601 or PM 3602	380, 380M .01 - 180 MHz	381, 381M 1 - 1800 MHz	382, 382M 1 - 2000 MHz
Rise/Fall Time	< 5 μs		
On/Off Ratio	> 70 dB	> 30 dB	
Input Level Required (Neg. voltage turns RF off)	0 to -5V		

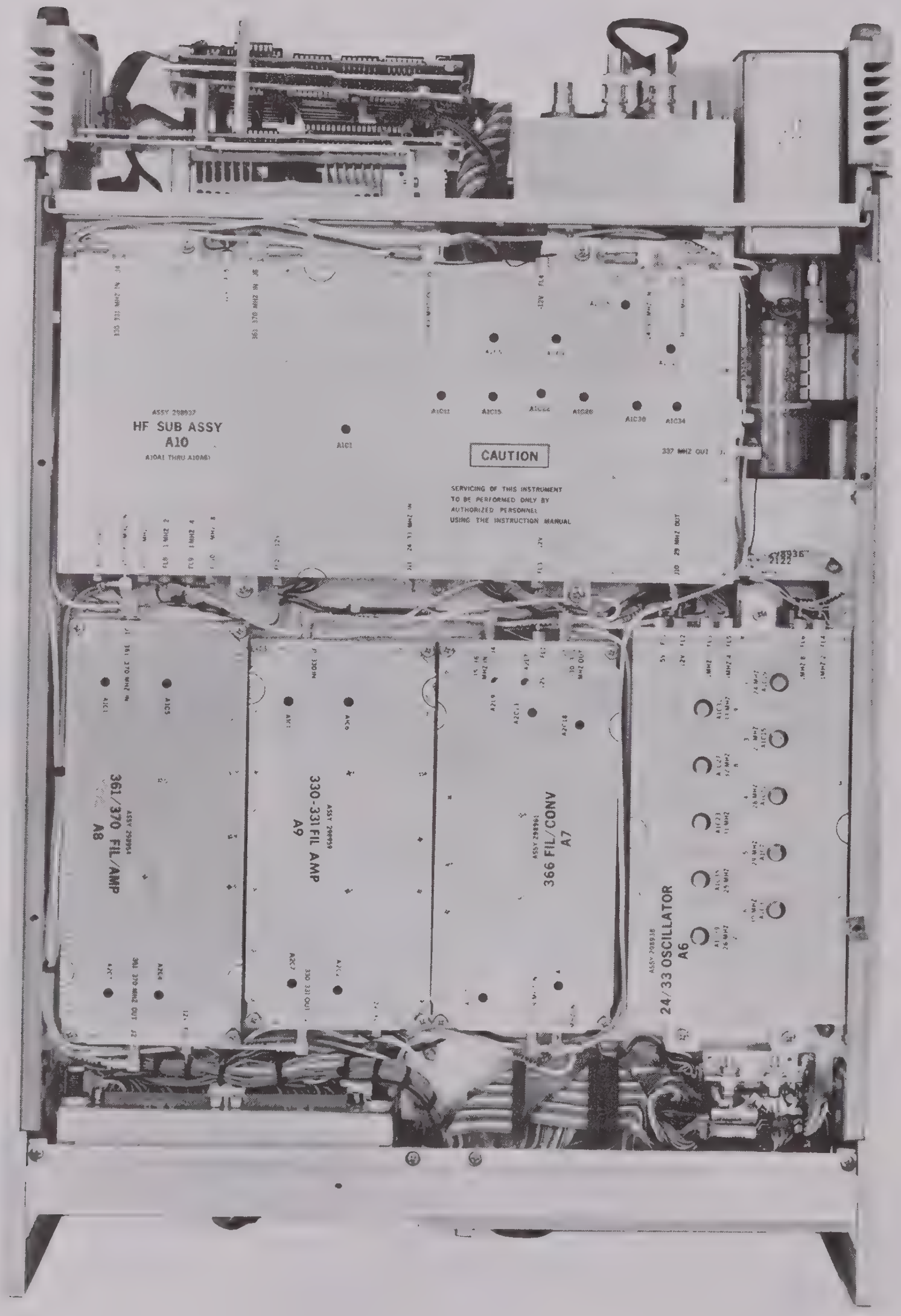
ASSY 298937
HF SUB ASSY
A10
A10A1 THRU A10A6
CAUTION
SERVICING OF THIS INSTRUMENT
TO BE PERFORMED ONLY BY
AUTHORIZED PERSONNEL
USING THE INSTRUCTION MANUAL
337 MHZ OUT
361/370 FIL/AMP
A8
330-331 FIL AMP
A9
366 FIL/CONV
A7
24/33 OSCILLATOR
A6

Figure 1-2. Frequency Extender Installed in Mainframe

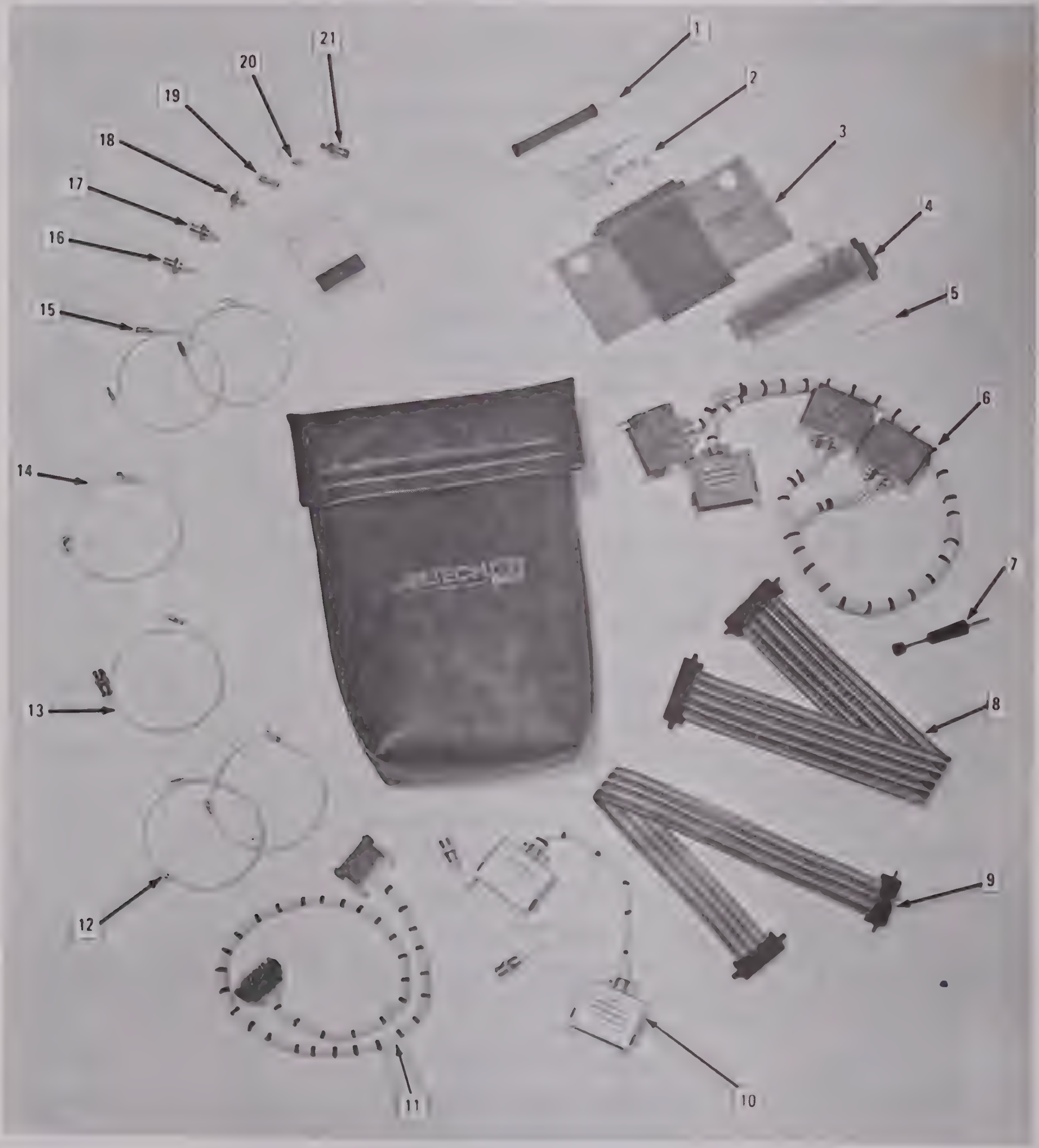
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Figure 1-3. Service Kit

TABLE 1-4. SERVICE KIT PART NO. 299252

Item	Description	Part Number
	Pouch Assembly	299252-4
1	*Adjustment Tool Non-Metallic	293198-1
2	*Card Jumper, BCD	299225
3	*Card Extender, BCD	299545
4	*Card Extender, Power Supply	299547
5	*Adjustment Tool	293198-2
	Pouch PI Extender Cable	299252-4
6	Extender Cable Assembly	299822
7	Extraction Tool M Block	294291
8	Extender Cable MPU	296551-1
9	Extender Cable MPU	296551-2
	Pouch Jumper Cable	299252-6
10	*Cable Assembly, Jumper Mainframe	299821
	Pouch Assembly Modulation Extender Cable	299252-7
11	Cable Assembly Extender Modulation	299820
	Pouch Assembly RF Cable	299252-8
12	*Cable Assembly, Coaxial, SMB Plug to SMB Plug Quantity 2	299543-1
13	*Cable Assembly, Coaxial, SMB Plug to SMB Plug Quantity 2	299543-2
14	*Cable Assembly, Coaxial, SMB Right Angle Plug to SMB Right Angle Plug	299543-3
15	*Cable Assembly, Coaxial SMB Jack to SMB Plug, Quantity 2	299543-4
	Case Assembly, RF Adapter	299252-9
16	*Adapter BNC Female, SMB Jack	294676
17	*Adapter BNC Female, SMB Plug	294677
18	*Adapter SMB Tee	294678
19	Adapter Straight Jack/Jack	294652
20	Adapter Straight Plug/Plug	294653
21	Adapter OSM Jack to Type BNC Jack	294658

*Specifically required for mainframe/frequency extender maintenance

1-11. TEST EQUIPMENT. Table 1-5 lists the test equipment recommended for use in testing, adjusting, and servicing the 380 Direct Synthesized Signal Generator.

1-12. MAINFRAME SUBASSEMBLIES. The mainframe consists of nine major subassemblies. Table 1-6 lists the reference designator, title and part number of each subassembly.

1-13. FREQUENCY EXTENDER SUBASSEMBLIES. The frequency extender consists of five major subassemblies. Table 1-7 lists the reference designator, title and part number of each subassembly.

TABLE 1-5. RECOMMENDED TEST EQUIPMENT

Item	Specification	Recommended Model
Coaxial Attenuator	10 dB	Narda
Digital Voltmeter		Systron Donner Model 7004
Frequency Counter	300 Hz to 2 GHz	Systron Donner Model 6244A
Frequency Counter	300 Hz to 180 MHz	Dana Model 7720
Function Generator		Wavetek Model 180
Modulation Meter	1 MHz to 2 GHz	Dana Model 9008
Power Meter, RF	200 kHz to 200 MHz ± 0.2 dB	Boonton Model 42B
Prescaler	600 MHz ÷ 10, ÷ 100	Weston 1259
Service Kit	See Table 1-4 and Figure 1-3	AILTECH 299252
Spectrum Analyzer	.001 to 12.4 kHz 100 dB display range with preselection and variable persistence	AILTECH 757

TABLE 1-6. MAINFRAME ASSEMBLIES AND SUBASSEMBLIES

Reference Designation	Title	Part Number
A1	Front Panel Assembly	296389-1
A1A1	Keyboard Assembly	296306-1
A1A2	Encoder Board Assembly	296308-1
A1A3	Optical Encoder Assembly	296424-1
A2	Lower Order BCD Assembly	298933-2
*A2A1	1 kHz BCD Decade Board	299204
*A2A2 - A2A4	0.1 Hz to 100 Hz BCD Decade Boards	299214
*A2A5	0.1 Hz BCD Decade Board	299214 (1)
*A2A6	BCD Interconnecting Board	299203
*A2A5	Jumper Board	299225 (2)
A4	Higher Order BCD Assembly	298934-1
A4A1	100 kHz BCD-1 Board	298904
A4A2	100 kHz BCD-2 Board	298905
A4A3	100 kHz BCD-4 Board	298906
A4A4	100 kHz BCD-8/1 Board	298907
A4A5	100 kHz BCD-8/2 Board	298908
A4A6	Buffer Amplifier Board	298909
A4A7-A10	64/65 MHz Switch Board	298910
A4A11	10 kHz BCD Board	299248
A5	Fixed Frequency Generator Assembly	298935-1
A5A1	Input Board	298921
A5A2	10 MHz Output Board	298918
A5A3	60 MHz Generator Board	298919
A5A4	65 MHz Generator Board	298917-2
A5A5	64 MHz Generator Board	298915
A5A6	35/35.5 MHz Board	298926
A11	Power Supply Assembly	—
A11A1	Filter Board	298997-1
A11A2	+5.2 Volt Regulator Board	298998-1
A11A3	–12 Volt Regulator Board	299200-1
A11A4	Power Supply Motherboard	298928-1

*A2A1 through A2A5 are installed in module A2 but are not part of module A2.

TABLE 1-6. MAINFRAME ASSEMBLIES AND SUBASSEMBLIES (Cont)

Reference Designation	Title	Part Number
A12	Rear Panel Assembly	296407-1
A13	Frequency Standard	298648 (3)
A14	Logic and Power Distribution Board	299201-1
A15	MPU Assembly	296387-1
A15A1	MPU PC Board	296351-1
A15A2	Latch PC Board	296353-1
A15A3	GPIB PC Board	296355-1

TABLE 1-7. FREQUENCY EXTENDER ASSEMBLIES AND SUBASSEMBLIES

Reference Designation	Title	Part Number
A6	24/33 MHz Oscillator Assembly	298938
A6A1	24/33 MHz Oscillator Board	298949
A6A2	Isolator Divider Board	298952
A6A3	Isolator Amplifier Board	298953
A7	366 MHz Filter Converter Assembly	298961
A7A1	366 MHz Filter Converter Input Board	298962
A7A2	366 MHz Filter Converter Output Board	298963
A8	361/370 MHz Filter Amplifier Assembly	298954
A8A1	361/370 MHz Filter Amplifier Input Board	298955
A8A2	361/370 MHz Filter Amplifier Output Board	298956
A8A3	361/370 MHz Capacitor Board	298957
A9	330/331 MHz Filter Amplifier Assembly	298959
A9A1	330/331 MHz Filter Amplifier Input Board	298965
A9A2	330/331 MHz Filter Amplifier Output Board	298966
A10	High Frequency Subassembly	298937
A10A1	337 MHz Oscillator Isolator Board	298995
A10A2	337 MHz Isolator Board	298996
A10A3	30/40 MHz Filter Amplifier Board	298901
A10A4	330 MHz Power Amplifier	298902
A10A5	1-5 MHz Comb Generator Board	298900
A10A6	29 MHz Filter Converter Board	298903

CHAPTER 2

INSTALLATION

2-1. INTRODUCTION

2-2. This chapter describes unpacking, preparation for use, initial turn-on and initial checkout of the 380K11 Mainframe with and without the 3600E1 Frequency Extender.

2-3. UNPACKING

2-4. No special instructions or precautions are necessary for unpacking the equipment except that reasonable care should be exercised to protect the instrument. When the instrument has been removed from the shipping container, remove the black plastic band securing the RF section in place.

2-5. PREPARATION FOR USE

2-6. If a complete synthesizer was ordered, all components have been assembled at the factory and all connections made prior to shipment. Paragraphs 2-7 through 2-12 describe installation procedures if various components are ordered separately.

2-7. **FREQUENCY EXTENDER INSTALLATION.** If the mainframe contains a frequency extender, all connections have been made at the factory. If a frequency extender is being added to a mainframe, refer to Figure 2-1 and proceed as follows. (This procedure requires a 5/16-inch spintight.)

a. Disconnect the ac line cord and turn the frequency synthesizer upside down.

b. Remove the bottom cover.

c. Disconnect two cables W23A10J5 and W10A7J4 from each other and remove the interconnecting adapter.

d. Install frequency extender and secure in place with eight nuts.

e. Make the following connections to the frequency extender:

Cable	Frequency Extender Connection
W18A7J2	A7J2
W24A10J10	A10J10
W23A10J5	A10J5
W14A10J7	A10J7
W10A7J4	A7J4

f. Replace the bottom cover.

g. Turn frequency synthesizer right side up.

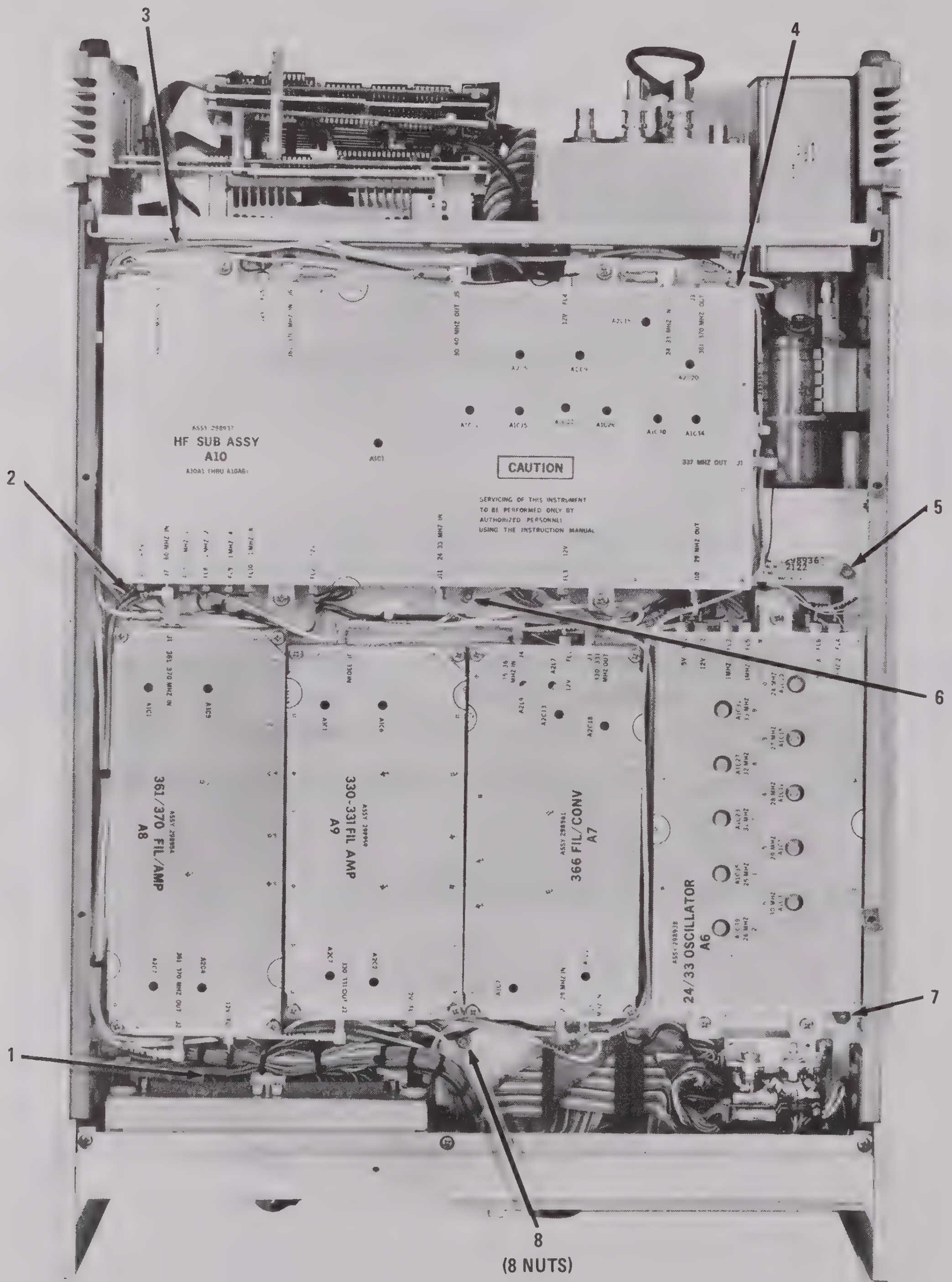

3
4
HF SUB ASSY
A10
CAUTION
SERVICING OF THIS INSTRUMENT TO BE PERFORMED ONLY BY AUTHORIZED PERSONNEL USING THE INSTRUCTION MANUAL
2
5
6
361/370 FIL/AMP
A8
330-331 FIL AMP
A9
366 FIL/CONV
A7
24/33 OSCILLATOR
A6
7
1
8
(8 NUTS)

Figure 2-1. Frequency Extender Installation

2-8. **FREQUENCY STANDARD INSTALLATION.** If the optional internal frequency standard (Mainframe Option 002 or 003) is ordered separately, it is installed as follows. (This procedure requires a Phillips head screwdriver and a soldering iron.)

a. Disconnect ac line cord.

b. Remove the rear cover assembly.

c. Remove the frequency standard cover plate at right near corner of the mainframe.

d. Secure cover plate to oven with two Phillips head screws provided. Make certain the frequency adjustment screw is accessible through the adjustment hole.

e. Withdraw coiled cable and leads from inside mainframe.

f. Connect coaxial cable to SMA output connector of frequency standard.

g. Solder purple-white striped wire to feedthrough filter capacitor.

h. Solder black wire to ground lug under feedthrough filter capacitor.

i. Slip screw heads on top surface of oven through slots in bracket at top of well.

j. Slide the unit into mainframe until cover plate bottoms against rear panel. Secure with screws provided.

k. Connect BNC jumper cable between rear panel connectors marked, 10 MHz INT STD OUT and 5/10 MHz REF IN.

2-9. **RF SECTION INSTALLATION/REMOVAL.**

2-10. Install the RF section by performing the following procedure:

CAUTION

Do not attempt to assemble the RF section to a mainframe if the connector alignment pins are bent, since this could cause extensive damage to the plug-in connectors.

a. Carefully slide unit above halfway into slot provided on right side of mainframe.

b. Make certain that the locking lever is at about a 45 degree angle with the front panel and fully seat the unit. The locking lever should pivot in towards panel.

c. Lock plug-in by depressing lever until latch engages.

d. Connect two ribbon cables from mainframe to RF section connectors J4 and J5. When removing the RF section proceed as follows:

CAUTION

Disconnect the two ribbon cables from connectors J4 and J5 before removing the RF section from the mainframe.

e. Removal of the RF section is the reverse of steps a through d.

2-11. **MODULATION SECTION INSTALLATION.** If the RF section was ordered and shipped with a modulation section, the two units have been assembled at the factory prior to shipment. From an external viewpoint, the modulation section makes up the upper half of the RF section front panel. To install the modulation section proceed as follows.

2-12. The modulation section is best installed with the RF section either completely removed from or partially inserted in the mainframe. (No tools are required for the following procedures.)

a. Insert modulation interface connector P3 through front panel opening and seat the connector in mating block J3 on RF section. Secure connector by finger-tightening the captive thumbscrews on P3.

b. Place lower edge of modulation section panel on upper edge of RF section panel. Make certain the metal tab on modulation section panel is behind the RF section panel.

c. Pivot panel back into recess provided. Secure unit by finger-tightening captive thumbscrews on modulation section front panel.

d. On PM3602 it is also necessary to secure rear support bracket to the RF section with a number six screw, nut, and lockwasher.

2-13. **RACK MOUNTING.**

2-14. A rack mounting adapter kit (Option 006) is required to secure the AILTECH 380 Synthesized Signal Generator in a standard 19-inch rack. The kit consists of two right-angle brackets which bolt to the side of the unit. (See Figure 2-2.) Complete assembly instructions are provided with the kit.

CAUTION

A fully assembled AILTECH 380 Synthesized Signal Generator weighs 91 pounds. Two persons are required to lift the unit, and it must be supported in a rack by some means other than the rack mounting kit. Full length shelf brackets or suitable rack-mount chassis slides (paragraph 2-15 are recommended for support in a rack.

2-15. Rack mount slide adapter kit (Option 007) provides a suitable means for mounting the AILTECH 380 Synthesized Signal Generator in a rack with chassis slides. (Slides are not provided with the kit.) The kit consists of two replacement side panels for the mainframe and instructions illustrating a typical rack mounting installation. The panels are designed to mate with the Jonathan Manufacturing Company 310-L-26 series slide.

2-16. INITIAL TURN-ON

2-17. The location and function of the controls, indicators and connectors on the mainframe are described in Chapter 3. Other controls, indicators, and connectors referenced in the following procedures are located on and described in the appropriate RF Section Instruction Manuals.

CAUTION

To prevent damage to the instrument, make line voltage selection before connecting line power. Also ensure that the line power cord is connected to a power circuit with a protective earth ground.

a. Set power PC card at rear of frequency synthesizer to 120 or 240 volts depending on power source being used. Use proper fuse as indicated.

b. Set ON/STBY switch on frequency synthesizer front panel to STBY.

c. Connect power cable at rear of frequency synthesizer to an uninterrupted source of 115/230-Vac, single-phase, 48- to 66-Hz power (50 to 400 Hz with Option 004). With the ON/STBY switch in STBY position the STBY LED will light. With the ON/STBY switch in ON position the LED will light and the display will indicate.

d. If the signal generator contains Option 002 or 003 (internal frequency standard), allow the unit to warmup for a minimum of 1 hour. (Refer to paragraph 3-5.)

e. With the ON/STBY switch to STBY position, all indicators except the STBY LED on the mainframe are blanked.

2-18. INITIAL CHECKOUT

2-19. After an initial warmup period of 1 hour, check that the signal generator is operating properly as follows:

a. If the unit is not equipped with an optional, internal frequency standard (Option 002 or 003), connect a stable 5 or 10 MHz signal at 0 dBm to the rear panel BNC connector marked 5/10 MHZ REF IN.

b. Connect frequency counter to RF OUTPUT connector on front panel of RF section.

c. Connect REF OUTPUT at rear of synthesizer mainframe to external 10 MHz input of counter.

d. Set ON/STBY switch to ON position and check that the frequency counter and the front panel indicates 100 MHz (± 1 count). The 380 will initialize to 100 MHz, –20 dBm, CW mode anytime the power is connected and the unit is turned on.

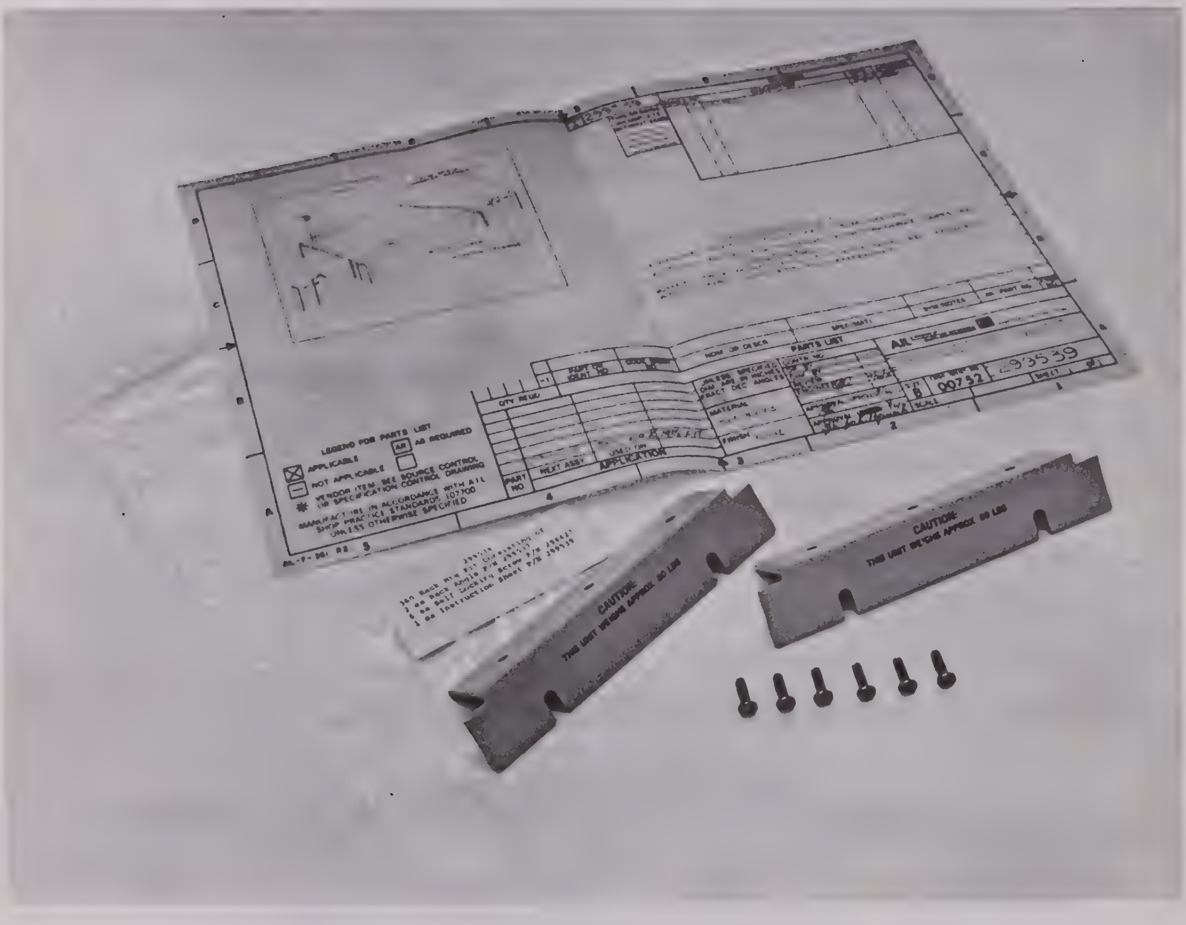

CAUTION:
THIS UNIT WEIGHS APPROX 80 LBS
CAUTION:
THIS UNIT WEIGHS APPROX 80 LBS
293539
00752

Figure 2-2. Rack Mounting Adapter Kit (Option 006)

CHAPTER 3

OPERATION

3-1. INTRODUCTION

3-2. This chapter illustrates and describes the controls, indicators, and connectors of the AILTECH 380K11 Mainframe. It also provides operating instructions and a simple operating check which may be performed should the unit appear to malfunction.

3-3. CONTROLS, INDICATORS AND CONNECTORS

3-4. Figures 3-1 and 3-2 show the controls, indicators, and connectors on the front and rear panels of the 380K11 Mainframe. Tables 3-1 and 3-2 list the exact name of the control, and function.

3-5. REFERENCE OSCILLATOR STABILIZATION

3-6. The 380K11 Mainframe and 3600E1 Frequency Extender are used in conjunction with an appropriate P360 RF Section as a precision signal source. The frequency accuracy and precision is dependent upon an externally applied 5 or 10 MHz reference signal, or an optional, internal, oven-stabilized, 10-MHz crystal controlled oscillator. A minimum period of one hour is required for the oven to reach full operating temperature. The specified stability is obtained after the oscillator is on for a minimum of 72 hours. For this reason, the signal generator is provided with an ON/STBY switch rather than a power on-off switch. It is suggested that the unit be connected to an uninterrupted source of ac power and remain in the standby mode when not being used. In this manner, the 10-MHz crystal oscillator is maintained at constant temperature, and the signal generator is ready for immediate use without a long warmup period.

3-7. Before attempting to operate the signal generator using the internal reference oscillator, make certain the BNC jumper cable between the 10 MHz INT STD OUT and the REF IN jacks at the rear of the mainframe is in place.

3-8. OPERATORS MAINTENANCE

3-9. If the signal generator appears to malfunction, perform the following checks:

a. If both the STBY and ON LED indicators are inoperative (no ac power), check that the primary power source is turned on.

b. Check that the power cable is securely connected to the unit.

c. Check fuse at rear of unit and replace blown fuse. If fuse constantly blows, the unit requires more extensive repair. (See Chapter 6.)

d. If ac line power is applied but no output is available, check that the RF section is properly seated and locked in place and also check that the voltage selector PC card is in the proper position.

e. Check that an appropriate internal or external reference source is connected to the mainframe REF IN connector.

f. Check that the output cable being used is not damaged or incorrectly connected. If an output is still not available, the unit requires more extensive repair. (See Chapter 6.)

g. Check two connectors J4 and J5 on rear of RF section.

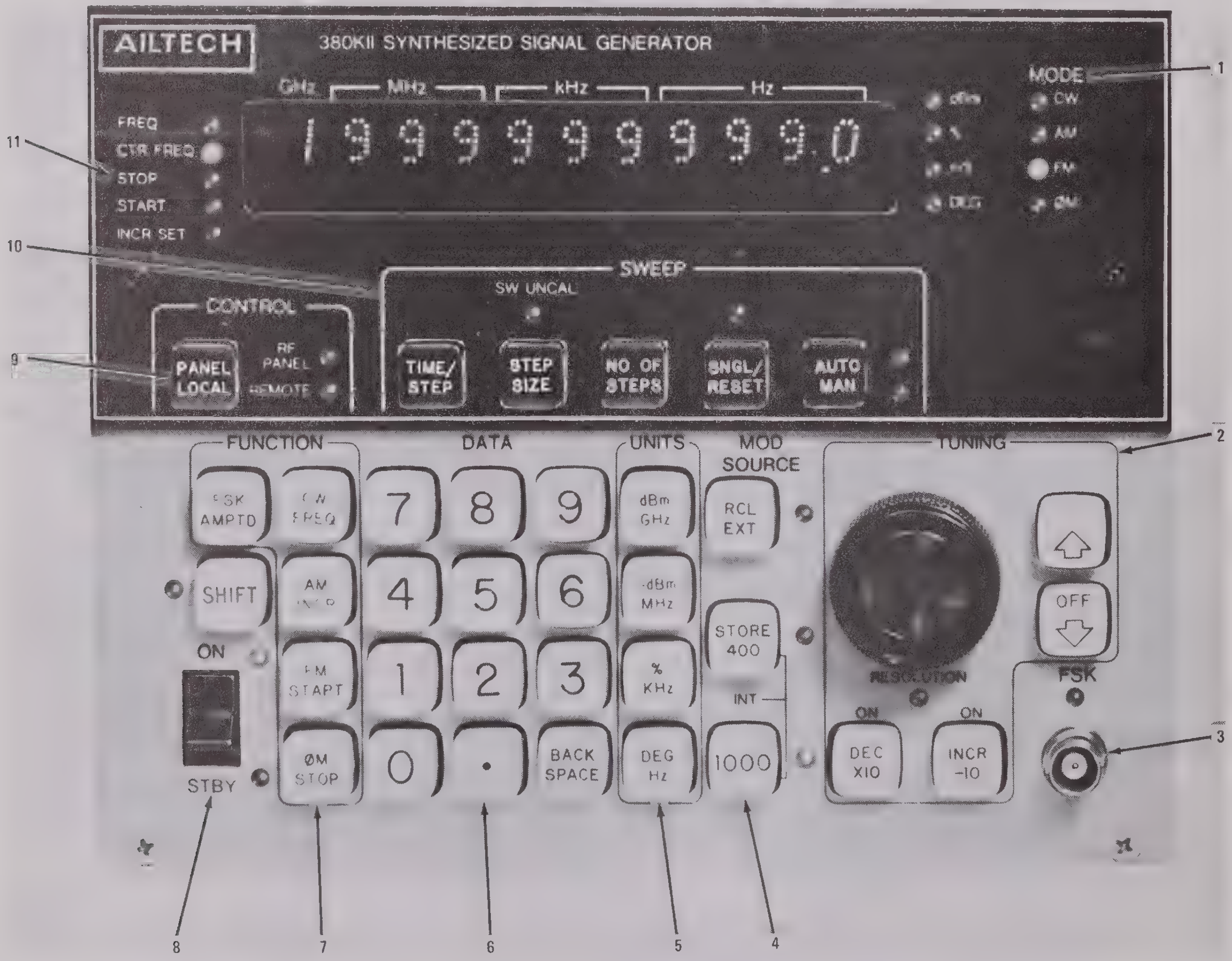

AILTECH
380KII SYNTHESIZED SIGNAL GENERATOR
GHz
MHz
kHz
Hz
1 9 9 9 9 9 9 9 9 9.0
FREQ
CTR FREQ
STOP
START
INCR SET
dBm
%
mV
DEG
MODE
CW
AM
FM
ØM
SWEEP
SW UNCAL
TIME/STEP
STEP SIZE
NO OF STEPS
SNGL/RESET
AUTO MAN
CONTROL
PANEL LOCAL
RF PANEL
REMOTE
FUNCTION
FSK AMPTD
CW FREQ
SHIFT
AM INCR
FM START
ØM STOP
ON
STBY
DATA
7
8
9
4
5
6
1
2
3
0
.
BACK SPACE
UNITS
dBm GHz
-dBm MHz
% KHz
DEG Hz
MOD SOURCE
RCL EXT
STORE 400
INT
1000
TUNING
OFF
RESOLUTION
ON
DEC X10
INCR ÷10
FSK
1
2
3
4
5
6
7
8
9
10
11

Figure 3-1. Front Panel Controls and Indicators

TABLE 3-1. FRONT PANEL CONTROLS AND INDICATORS

Figure 3-1 Index No.	Control or Indicator	Function
1	Mode Indicators	Four LED's which provides indication of the type of modulation present.
	CW	Modulation off
	AM	Amplitude Modulation
	FM	Frequency Modulation
	ØM	Phase Modulation
2	Tuning	This section allows any value selectable parameter to be changed.
	Tuning Knob	Stepping controls consist of a rotary control knob and stepping keys. Either control may be used to increment the value of selected functions.
	⇩	Increases the parameter being changed in steps.
	OFF ⇩	Decreases the parameter being changed in steps.
		When Tuning LED is illuminated, indicates that rotary control is activated.
		Shift OFF, turns off the control.
		Shift ON, turns on the control.
	INCR +10	Increases the stepping resolution by a factor of 10, once for each keystroke.
	DEC X10	Decreases the stepping resolution by a factor of 10, once for each keystroke.
3	Input connector BNC	TTL input for FSK data, or external sweep trigger.
4	MOD Source RCL EXT · STORE 400 · 1000	Three keys which select 400 or 1000 Hz internal, or external modulation.
	STORE/RCL	Keys which allow the instrument states to be stored and recalled in registers 1 through 9.
5	Units	Four keys which allow the entry to be terminated and entered.

TABLE 3-1. FRONT PANEL CONTROLS AND INDICATORS (Cont)

Figure 3-1 Index No.	Control or Indicator	Function
5 (Cont)		Selection of a function automatically selects either Shift or Unshift to properly terminate the function.
	dBm GHz	(SHIFT) Power related to 1 mw (>1 mw) Giga Hertz (X 10^9)
	-dBm MHz	(SHIFT) Power related to 1 mw (<1 mw) Mega Hertz (X 10^0) (Requires 180 series attenuator option)
	% KHz	(SHIFT) Percent Kilo Hertz (X 10^3)
	DEG Hz	(SHIFT) Degrees for phase modulation Hertz
6	DATA 7 8 9 4 5 6 1 2 3 0 . BACK SPACE	Standard keyboard which allows numerical data to be entered. BACK SPACE key clears the last digit or decimal point entered.
7	FUNCTION	Six keys select function to be entered.
	SHIFT	When the associated LED is illuminated the upper nomenclature of a two function key is selected.
	FSK AMPTD	(SHIFT) FSK (Frequency Shift Keying) Illuminates FSK LED and associates the front panel BNC connector with a TTL logic input for FSK Modulation. AMPTD causes the LED display to indicate output level in dBm, and allows a new power in dBm to be entered.
	CW FREQ	(SHIFT) CW turns off all Modulation. FREQ causes the LED display to indicate present frequency, and allows a new frequency to be entered.

TABLE 3-1. FRONT PANEL CONTROLS AND INDICATORS (Cont)

Figure 3-1 Index No.	Control or Indicator	Function
7 (Cont)	AM INCR	(SHIFT) AM causes the LED display to indicate present AM level, and allows a new percentage of modulation to be entered.
		INCR causes the LED display to indicate present frequency increment, and allows a new frequency increment or FSK deviation to be entered.
	FM START	(SHIFT) FM causes the LED display to indicate the present frequency deviation and allows a new deviation to be entered.
		START causes the LED display to indicate the present START frequency and allows a start frequency for a sweep to be entered.
	ØM STOP	(SHIFT) ØM causes the LED display to indicate the present degrees of phase modulation, and allows a new value to be entered.
		STOP causes the LED display to indicate the present STOP frequency and allows a new value to be entered.
8	ON/STBY switch	ON/STBY switch selects either STBY mode where DC power is applied to the microprocessor and the crystal oven only or ON mode where the instrument is fully functional. LED indicators indicate the status.
9	CONTROL	This section controls and indicates the following:
	REMOTE	Indicates the control status Local or Remote.
	RP PANEL	Indicates control status of RF and modulation panels, either by keyboard or panel control knobs and selector switches.

TABLE 3-1. FRONT PANEL CONTROLS AND INDICATORS (Cont)

Figure 3-1 Index No.	Control or Indicator	Function
9 (Cont)		(SHIFT) Panel key allows the RF front panel controls to be enabled or disabled.
		LOCAL allows the instrument to be returned to local control when under GPIB control. (Can be locked out by controller by sending the Local Lockout message.)
10	SWEEP	In conjunction with the START and STOP keys, allows sweep parameters to be entered.
		Each keystroke causes the time/step to change through 1, 10 and 100 ms/step and (O) external.
		Allows a step size to be entered via the front panel keyboard.
		The first keystroke causes the number of steps to be displayed on the LED indicator. Subsequent keystrokes allow selection of 100 or 1000 steps. In this case, a step size will be automatically calculated.
	SW UNCAL	This LED lights when 1000 steps have been exceeded, indicating that the sweep voltage staircase (BNC output rear panel) has limited at a maximum value of +2 volts.
		Depressing while in AUTO sweep instantaneously stops sweep, and returns generator to non-sweep (CW) mode. Subsequent keystrokes initiate a single-sweep.
		Allows manual sweeping in either direction using the TUNING knob from start to stop at the selected step size.
		(SHIFT) AUTO initializes a repetitive sweep from start to stop at the selected time/step and step size.

TABLE 3-1. FRONT PANEL CONTROLS AND INDICATORS (Cont)

Figure 3-1 Index No.	Control or Indicator	Function
11	Display Function	Five LEDs indicate the function being displayed.
	FREQ	The display indicates the instantaneous frequency in Hz.
	CTR FREQ	The display indicates the center frequency of a sweep.
	STOP	The display indicates the stop frequency of a sweep.
	START	The display indicates the start frequency of a sweep.
	INCR SET	The display indicates the stepping frequency increment.
	Display	An eleven digit LED seven segment display which displays the above functions in numerical form.
	Display Units	Provides the numerical indication of the functions. When none of the function LEDs are lit, the units displayed are in Hz.
	dBm	Power in dB referred to 1 mw (0 dBm).
	%	Percent
	mS	Milliseconds
	DEG	Degrees

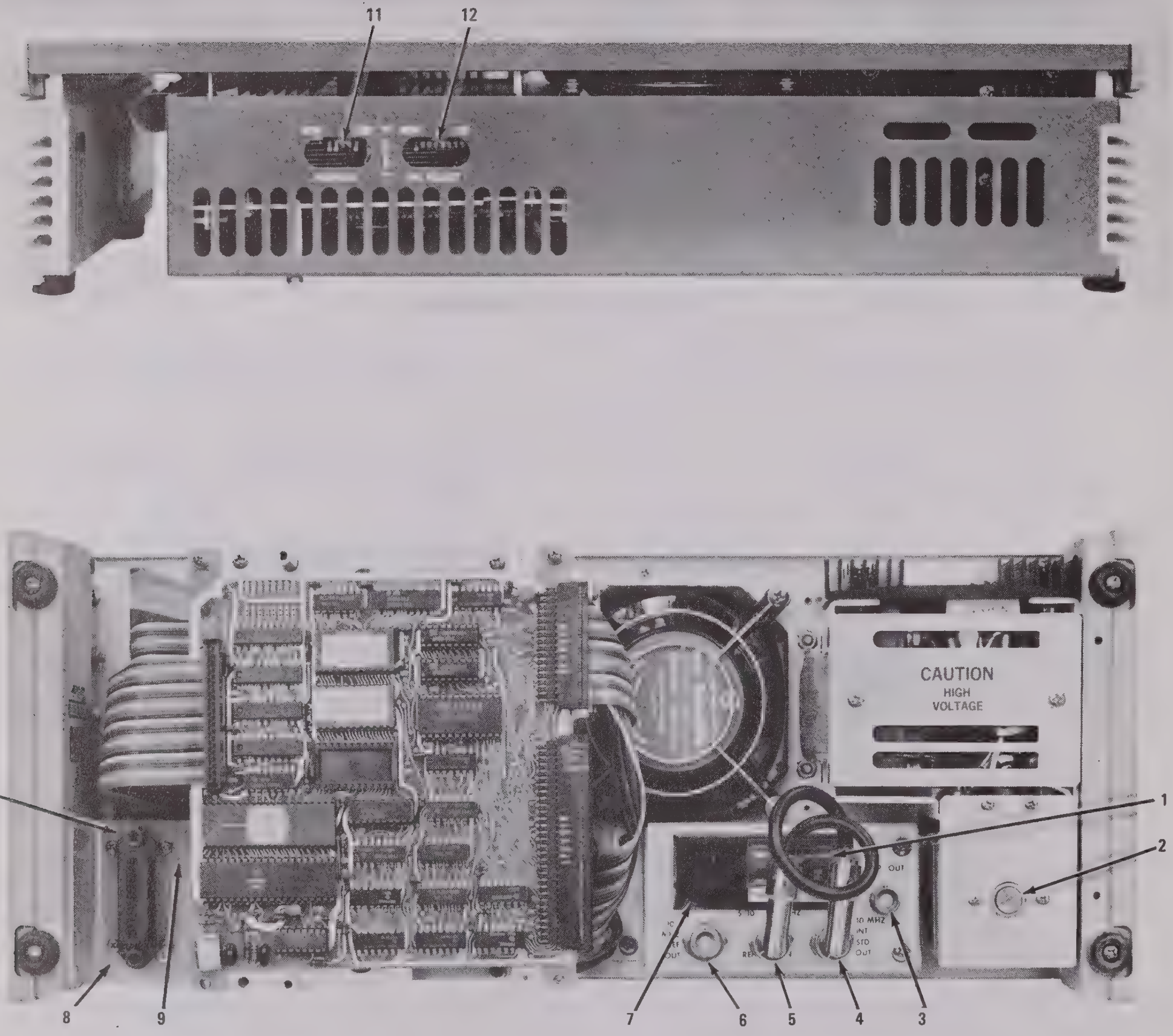

11
12
CAUTION
HIGH
VOLTAGE
10
1
2
OUT
10 MHZ
INT
STD
OUT
8
9
7
6
5
4
3

Figure 3-2. Rear Panel Controls and Connectors

TABLE 3-2. REAR PANEL CONTROLS AND CONNECTORS

Figures 3-2 Index No.	Control	Function
1	Line fuse and 115/230V switch	AC line fuse; 115/230V selectable PC switch that allows selection of primary power.
2	FREQ ADJ	±2 parts in 10^6 frequency adjustments for optional internal standard.
3	SWP OUT	BNC connector provides a 0 to 2V stepped DC ramp voltage proportional to the steps in RF frequency in sweep mode, 1000 steps maximum.
4	10 MHz INT STD OUT	BNC connector for internal frequency standard output. Used only with Options 002 and 003.
5	5/10 MHz REF IN	BNC connector for internal or external frequency standard input to the synthesizer.
6	10 MHZ REF OUT	BNC connector provides the buffered 10 MHz reference signal output.
7	AC Input	AC line connector, 115/230 volts ±10%, 48-66 Hz. (50 to 400 Hz with Option 004) Plug-in PC card changes AC voltage selected.
8	IEEE 4888 Input	Standard IEEE 488 input connector provides the interface with a 488 controller.
9	J5	50 pin connector provides a means for applying frequency control inputs, in parallel BCD. For keyboard control, the latch PC board connector P5 is attached to this input. In the BCD mode, the LED display is inactive.
10	J4	36 pin connector provides a means for applying level and modulation control input in parallel BCD. For keyboard control, the MPU PC board connector P4 is attached to this input.
11	MSB LSB "0" ↕ ADDRESS "1"	GPIB address switches. Address are 0 to 30.

TABLE 3-2. REAR PANEL CONTROLS AND CONNECTORS (Cont)

Figure 3-2 Index No.	Control	Function
12	“0” MSB LSB ↕ “1” PPL SELECT	Parallel Poll select switches. Settings are 1 through 8.

3-10. DETAILED OPERATING INSTRUCTIONS

3-11. CONTROLLED FUNCTIONS. All instrument functions are controlled by the keyboard and settings are displayed on an 11 digit LED readout. Annunciators indicate the displayed functions. The keystroke sequence is function, data and units, and proceeds from left to right. When line power is first applied, operation is initialized at 100 MHz, –20 dBm output, CW mode. In the standby mode, keep alive power allows previous settings and stored programs to be held in memory.

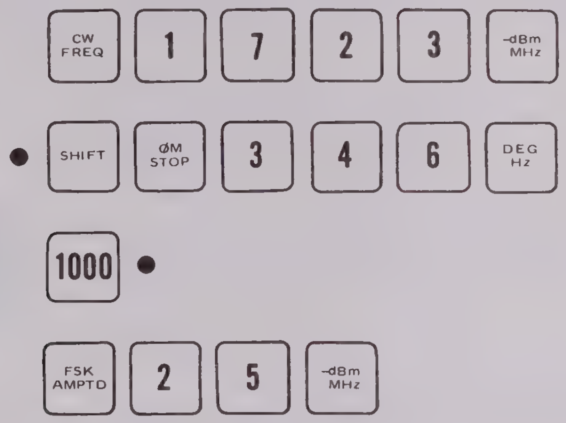
3-12. SHIFT KEY.

- SHIFT The 380 keyboard uses double function keys. For example, the key CW FREQ has two functions, the upper function is CW which is used to turn off all forms of modulation (CW carrier), and the lower function, FREQ, is used to allow entry of frequency data. The SHIFT key is used when the upper function of a two function key is to be chosen. The LED associated with the SHIFT key indicates when the SHIFT function is active. The SHIFT function latches automatically when a function is chosen which would otherwise require the SHIFT key to be stroked a second time for UNITS selection; thereby eliminating the need for another SHIFT keystroke per entry. The SHIFT automatically unlatches after the UNITS key is stroked, and the SHIFT LED is extinguished.

3-13. INITIALIZE. This function returns the operation of the synthesizer to 100 MHz — 20 dBm output, CW mode. Stored programs held in any of the nine memory registers are not affected by this function.

- SHIFT RCL EXT 0 •

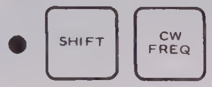
3-14. FREQUENCY, MODULATION AND LEVEL. A setup of 1723 MHz, phase modulated at 346°, at a level of -25 dBm, 1000 Hz internal modulation:


CW FREQ
1
7
2
3
-dBm MHz
●
SHIFT
ØM STOP
3
4
6
DEG Hz
1000
●
FSK AMPTD
2
5
-dBm MHz

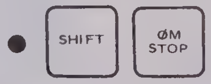
Note

Selection of ØM and AMPTD automatically selects SHIFT function (LED on) and units can be keyed directly without manually shifting. Keying units automatically returns SHIFT to off.

To turn modulation off:


●
SHIFT
CW FREQ

To return to the previous modulation setting:


●
SHIFT
ØM STOP

Note

In these keystrokes, since data and units are not entered, SHIFT LED will remain on. If next function does not require SHIFT, it must be keyed to turn OFF

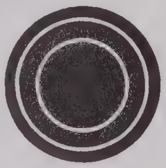
3-15. TUNING/INCREMENTING. Frequency, output level and modulation levels can be incremented using the up/down keys or the tuning knob. Frequency can be incremented in selectable decades or by entering an increment value.

3-16. FREQUENCY INCREMENTING WITHIN DECADES.

DEC X10 Display indicates a 1 in the decade column to be incremented — change the decade to be incremented by:

DEC X10 or INCR ÷ 10 then CW FREQ and ⇧ or OFF ⇩

To enable the tuning knob: ● SHIFT DEC X10 then



To disable the tuning knob: ● SHIFT OFF ⇩

3-17. FREQUENCY SET INCREMENTING.

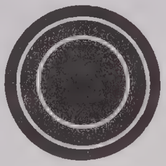
To increment in selectable step sizes, e.g., 25 kHz:

AM INCR 2 5 % KHz ● SHIFT INCR ÷ 10 CW FREQ

Then ⇧ or OFF ⇩ or

3-18. AMPLITUDE INCREMENTING IN FIXED 0.5 dB STEPS.

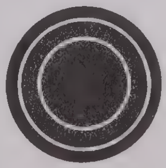
FSK AMPTD DEC X10 then ⇧ or OFF ⇩ or



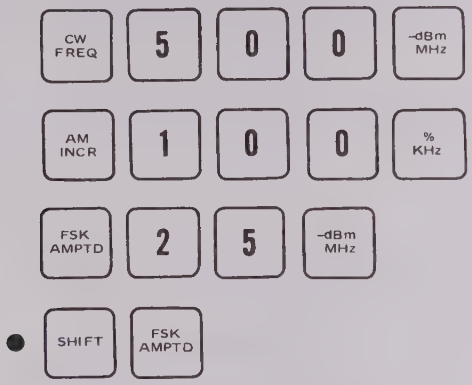
3-19. MODULATION INCREMENTING. Is in 0.5% steps for AM. For FM, increments are 50 Hz steps to 10 kHz, 500 Hz steps from 10 kHz to 100 kHz, 5 kHz steps from 100 kHz to 1 MHz, and 50 kHz steps from 1 MHz to 10 MHz. For phase modulation, increments are 0.5° up to 100° and 2° from 100° to 400°.

● SHIFT FM START then ⇧ or OFF ⇩

or ● SHIFT DEC X10 and

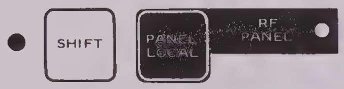


3-20. FREQUENCY SHIFT KEYING (FSK). The 380 can be used to generate FSK modulation at rates to 14k bit/sec e.g., 500 MHz carrier, 100 kHz frequency shift (deviation), –25 dBm output level.



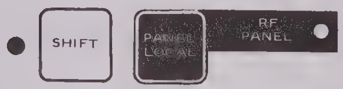
Then input the data stream at TTL levels into the FSK connector on the front panel. A logic 1 is f_c, a 0 is $f_c + f_{incr}$.

3-21. RF PANEL CONTROL. Normally, all modulator and RF panel controls are disabled and control of all functions is through the keyboard. The controls can be enabled allowing conventional operation of the modulation and RF output level:



Then operate panel controls.

To shift control back to the keyboard:

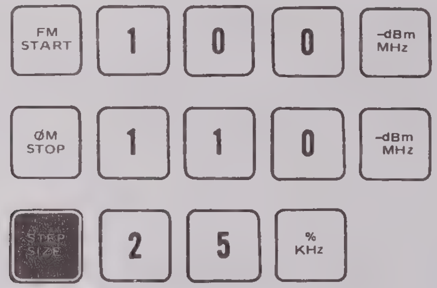


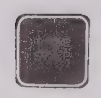
3-22. SYNTHESIZED DIGITAL SWEEP. To sweep the 380, the START and STOP frequencies are first entered. Sweep speed is selected using the TIME/STEP key, which allows a choice of 0, (External) 1, 10 or 100 msec per step. The current TIME/STEP is displayed after pressing the TIME/STEP key. Subsequent pressing of this key changes the TIME/STEP.

1. Next, either the STEP SIZE may be selected and the number of steps is automatically calculated, or alternatively, the number of steps; 100 or 1000, may be selected, and STEP SIZE will be automatically calculated. The SW UNCAL LED indicates when the number of steps is more than 1000, which is the maximum for the SWEEP OUTPUT VOLTAGE.
2. Sweep modes can be selected from a choice of SINGLE, AUTO, or MANUAL. When either the SINGLE or AUTO sweep is in process, the frequency display indicates the center frequency of the sweep. When MANUAL sweep is selected, the frequency display indicates the instantaneous frequency.

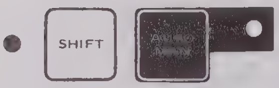
3. To stop the sweep when in AUTO, depress SNGL/RESET. Depressing START or STOP will position output at each end of sweep.
4. In order to enter new data while in AUTO sweep, the sweep must first be stopped by depressing SNGL/RESET.

Example 1: Auto Sweep between 100 MHz and 110 MHz in 25 kHz increments at 100 msec/step.

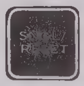

FM START
1
0
0
-dBm MHz
ØM STOP
1
1
0
-dBm MHz
STEP SIZE
2
5
% KHz



Each time depressed, rolls through 0, (Ext) 1, 10 or 100 msec as indicated on display

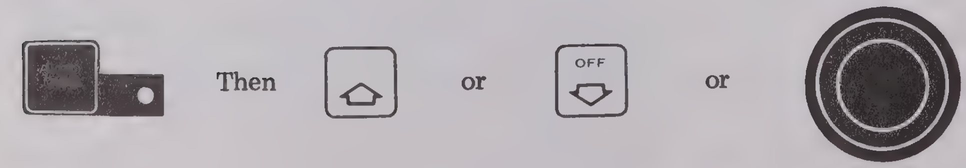


The sweep may be stopped instantaneously by pressing

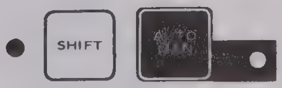


For a Single Sweep

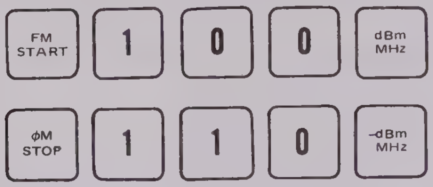
To sweep in MANUAL:

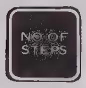

Then
or
OFF
or

To return to Auto Sweep:

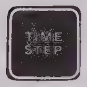


Example 2: Auto Sweep between 100 MHz and 110 MHz in 100 or 1000 steps using an external clock.

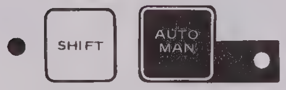




Changes selection from either 100 or 1000 steps each time pressed

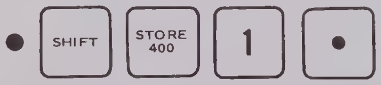


Choose 0 (Ext) from the 0, 1, 10, 100 msec menu, then connect TTL clock signal to FSK connector, maximum clock rate is 2 kHz. (500μs/step)

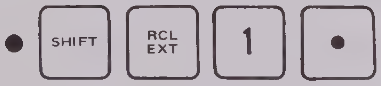


3-23. STORE/RECALL. Up to nine front panel set-ups can be stored in memory. Storage registers are one through nine. Sweep set-up can also be stored, however sweep modes are not stored, and must be selected after recall. First, enter the desired parameters, then:

To Store



To Recall



3-24. IEEE 488 PROGRAMMING

3-25. REMOTE GPIB OPERATION. The 380 can be operated through the General Purpose Interface Bus (GPIB). Bus compatibility, programming and data format are described in the following paragraphs. For more information about GPIB refer to IEEE Standard 488-1978. All front panel functions (except for that of the LINE switch), are programmable through the GPIB. The complete bus compatibility of the 380 (as defined by IEEE Standard 488-1978) is described at the end of Table 3-3.

3-26. GPIB MESSAGES. The programming capability of the 380 is further described by the eleven GPIB messages in Table 3-3. Foremost among these messages is the data message. Data messages contain the program codes that set the 380 output signal.

3-27. LOCAL-TO-REMOTE TRANSITION. The 380 communicates on the bus in the remote mode. In remote, its front panel controls are disabled (except for the LINE switch, and LOCAL key). The 380 will respond to the following messages: Data, Clear, Remote, Local, Local Lockout, Clear Lockout/Set Local and Abort. When addressed to talk, the 380 will automatically stop listening and send the status byte message, when being serial polled.

3-28. The 380 is enabled for remote operation upon receipt of the remote message. However, it remains in local mode (that is, the front panel is the control) until the 380 is first addressed to listen. Thereafter, it remains in remote until the front panel LOCAL key is pressed or it receives an appropriate message from the controller. The 380 output signal remains unchanged and all preselected front panel functions remain unchanged with the local-to-remote transition.

3-29. **REMOTE-TO-LOCAL TRANSITION.** The 380 returns to local control upon receipt of the Local or Clear Lockout/Set Local message. It can also be set to local mode by pressing the front panel LOCAL key (assuming that local lockout is not in effect). The 380 output signal remains unchanged and all preselected functions remain unchanged with the remote-to-local transition.

TABLE 3-3. MESSAGE REFERENCE TABLE

GPIB Messages	Response	Related Commands and Controls	Interface Functions
Data	All front panel functions, and remote only functions are programmable. The 380 can send Status Byte information.		T6 L4 AH1 SH1
Clear	Sets output to 100 MHz, -20 dBm with modulation and sweep off. Responds equally to DCL and SDC bus commands.	DCL SDC	DC1
Remote	The 380 remote mode is enabled when the REN bus line is true. However, it remains in local (i.e., the front panel enabled) until it is addressed to listen the first time. The output signal is unchanged. The front panel REMOTE indicator turns on for remote-enabled.	REN	RL1
Local	The 380 returns to front panel control. The output signal is unchanged. Responds equally to the GTL bus command and the front panel LOCAL key.	GTL	RL1
Local Lockout	LOCAL key is disabled. Only the controller can return the 380 to local.	LLO	RL1
Clear Lockout/ Set Local	The 380 returns to local when the REN bus line goes false.	REN	RL1

TABLE 3-3. MESSAGE REFERENCE TABLE (Cont)

GPIB Messages	Response	Related Commands and Controls	Interface Functions
Pass Control/ Take Control	The 380 has no controller capability		C
Require Service	The 380 sets the SRQ bus line true if one of the following conditions exist and it has been enabled to send the message for that condition. End of Single Sweep Power Restart.		SR1
Status Byte	The 380 responds to a Serial Poll Enable (SPE) bus command by sending an 8-bit byte. Bit 6 (RSV bit) is true if generator had sent the Require Service Message.	SPE SPD	T6
Status Bit (PPR Message)	The 380 ends the Status Bit message (PPR message), when parallel polled by the controller, by setting ATN and IDY true.	ATN, IDY	PP2
Abort	The 380 stops talking or listening.	IFC	T6 L4

*Commands control lines and interface functions are defined in IEEE Standard 488-1978. Knowledge of these might not be necessary if your controller's manual describes programming in terms of the eleven GPIB messages shown in the left column.

Complete GPIB capability as defined in the IEEE Standard 488-1978 is: SH1, AH1, T6, TEO, L4, LEO, SR1, RL1, PP2, DC1, DTO, CO.

3-30. LOCAL LOCKOUT. When a data transmission is interrupted, which can happen by returning the 380 to local with the LOCAL key, the data could be lost. This would leave the 380 in an unknown state. To prevent this, a local lockout is recommended. Local lockout disables the LOCAL key and allows return-to-local only under program control.

3-31. ADDRESSING THE 380. Set decades 1 to 5 of ADDRESS SWITCH (top of rear cover) to 1 or 0, in accordance with the binary digit corresponding to the selected decimal address between 0 and 30.

TABLE 3-4. IEEE NOTATIONS

<table>
<tr><th colspan="3">NOTATION CONVENTIONS</th></tr>
<tr><td colspan="2"><></td><td>Items enclosed within angular brackes are considered to be elements of the language being defined.</td></tr>
<tr><td colspan="2">:: =</td><td>“Is defined as” A :: = <B C> indicates that A can be replaced by the series of elements B C in any statement which A occurs.</td></tr>
<tr><td colspan="2">|</td><td>“or” indicates a choice of exactly one element from list (e.g., A|B indicated A or B but not both.</td></tr>
<tr><th colspan="3">DEFINITIONS</th></tr>
<tr><td colspan="2"><listen></td><td>:: = Controller addresses analyzer to listen</td></tr>
<tr><td colspan="2"><value></td><td>:: = 0 1 2 3 4 5 6 7 8 9 ASCII</td></tr>
<tr><td colspan="2"><value 2></td><td>:: = Packed BCD</td></tr>
<tr><th colspan="3">DATA ENTRY TO SYNTHESIZER</th></tr>
<tr><td colspan="2"><entry></td><td>:: = <value> <terminator></td></tr>
<tr><td colspan="2"><entry 2></td><td>:: = <value 2> <terminator></td></tr>
<tr><td colspan="2"><terminator></td><td>:: = <units code> <delimiter></td></tr>
<tr><td colspan="2"><terminator 2></td><td>:: = <dummy characters> <delimiter></td></tr>
<tr><td colspan="2"><dummy character></td><td>:: = <any ASCII characters except linefeed></td></tr>
<tr><td colspan="2"><terminator 3></td><td>:: = < • > <delimiter></td></tr>
<tr><td colspan="2">delimiter</td><td>:: = <LF> | <EOI></td></tr>
<tr><td colspan="2"><LF></td><td>:: = OA (ASCII Line Feed)</td></tr>
<tr><td colspan="2"><EOI></td><td>:: = 16 End or identify — issued on last character with ATN not active and EOI active.</td></tr>
<tr><td colspan="2"><Units Code></td><td>:: = +D | -D | PC | DG | HZ | KZ | MZ | GZ</td></tr>
<tr><td>Units of Power:</td><td>+D</td><td>:: = < dBm></td></tr>
<tr><td></td><td>-D</td><td>:: = <-dBm></td></tr>
<tr><td>Units of Phase:</td><td></td><td>:: = <Degrees></td></tr>
<tr><td>Units of Percentage</td><td>PC</td><td>:: = <Percentage %></td></tr>
<tr><td>Units of Frequency:</td><td>HZ</td><td>:: = <Hertz></td></tr>
<tr><td></td><td>KZ</td><td>:: = <Kilohertz></td></tr>
<tr><td></td><td>MZ</td><td>:: = <Megahertz></td></tr>
<tr><td></td><td>GZ</td><td>:: = <Gigahertz></td></tr>
</table>

TABLE 3-5. PROGRAMMING CODES

Code	Syntax	Function
AM	<Listen> AM <Entry>	Amplitude Modulation
AP	<Listen> AP <Entry>	Amplitude
AS	<Listen> AS <Entry>	Auto Sweep
+D	<Units Code>	dBm
–D	<Units Code>	–dBm
DG	<Units Code>	Degree
FA	<Listen> FA <Entry>	Start Frequency
FB	<Listen> FB <Entry>	Stop Frequency
FD	<Listen> FD	Frequency Shift Key Disable
FE	<Listen> FE	Frequency Shift Key Enable
FM	<Listen> FM <Entry>	Frequency Modulation
FR	<Listen> FR <Entry>	Frequency
GZ	<Units Code>	Gigahertz
HZ	<Units Code>	Hertz
IS	<Listen> IS <Entry>	Increment Set
KZ	<Units Code>	Kilohertz
L	<Listen> L <Entry 2>	Fast Learn
M0	<Listen> M0	Continuous Wave
M1	<Listen> M1	Internal 400 Hz
M2	<Listen> M2	Internal 1 kHz
M3	<Listen> M3	External
MZ	<Units Code>	Megahertz
N1	<Listen> N 1 | 2	Number of steps 1 — selects 10 2 — selects 100
PC	<Units Code>	Percent
PM	<Listen> PM <Entry>	Phase Modulation
RC	<Listen> RC 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 <Terminator 3>	Recall State
SD	<Listen> SD	Step Down
SS	<Listen> SS	Single Sweep
ST	<Listen> ST 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 <Terminator 3>	Save State
SU	<Listen> SU	Step Up
SZ	<Listen> SZ <Entry>	Step Size
T	<Listen> T | 0 | 1 | 2 | 3	Time/Step 0 — External 1 — 1 msec 2 — 10 msec 3 — 100 msec

3-32. PROGRAMMING EXAMPLES. The following examples indicate the 380 programming using either the Tektronix 4051 or the HP 9825.

INITIALIZE

Synthesizer initializes to 100 MHz, –20 dBm, CW mode.

TEK 4051	HP 9825
PRINT @25,32: “RC0.”	wrt 725, “RC0.”

FREQUENCY MODULATION AND LEVEL

A setup of 1723 MHz, phase modulated at 346°, at a level of –25 dBm, 1000 Hz internal modulation.

TEK 4051	HP 9825
PRINT @25,32:“FR1723MZ”	wrt 725, “FR1723MZ”
PRINT @25,32:“PM346DG”	wrt 725, “PM346DG”
PRINT @25,32:“M2”	wrt 725, “M2”
PRINT @25,32:“AP25-D”	wrt 725, “AP25-D”

INCREMENTING

Incrementing (Step Up/Step Down) Frequency in 25 kHz steps.

TEK 4051	HP 9825
PRINT @25,32:“IS25KZ”	wrt 725, “IS25KZ”
PRINT @25,32:“FR SU”	wrt 725, “FR SU”
PRINT @25,32:“SD”	wrt 725, “SD”

FREQUENCY SHIFT KEYING

Program a 500 MHz carrier, 100 kHz frequency shift at –25 dBm.

TEK 4051	HP 9825
PRINT @25,32:“FR500MZ”	wrt 725, “FR500MZ”
PRINT @25,32:“M0”	wrt 725, “M0”
PRINT @25,32:“IS100KZ”	wrt 725, “IS100KZ”
PRINT @25,32:“FE”	wrt 725, “FE”

Next connect the keying waveform to the front panel BNC connector marked FSK. If keying is desired over the GPIB, use the Step Up/Step Down function, instead of a keying generator.

PRINT @25,32:“FD”	wrt 725, “FD”
PRINT @25,32:“FR SU”	wrt 725, “FR SU”
PRINT @25,32:“SD”	wrt 725, “SD”

SYNTHESIZED DIGITAL SWEEP

Auto Sweep between 100 MHz and 110 MHz in 25 kHz increments at 100 msec/step.

TEK 4051	HP 9825
PRINT @25,32:"FA100MZ"	wrt 725, "FA100MZ"
PRINT @25,32:"FB110MZ"	wrt 725, "FB110MZ"
PRINT @25,32:"SZ25KZ"	wrt 725, "SZ25KZ"
PRINT @25,32:"T3"	wrt 725, "T3"
PRINT @25,32:"AS"	wrt 725, "AS"

To STOP the sweep:

TEK 4051	HP 9825
PRINT @25,32:"SS"	wrt 725, "SS"

To SINGLE SWEEP, again repeat message:

PRINT @25,32:"SS"	wrt 725, "SS"

STORE/RECALL

To store the above sweep parameters in register 1:

TEK 4051	HP 9825
PRINT @25,32:"ST1."	wrt 725, "ST1."

To recall the sweep:

TEK 4051	HP 9825
PRINT @25,32:"RC1.AS"	wrt 725, "RC1.AS"

Note sweep mode is non-storable and must be selected each time sweep is recalled.

LOCAL LOCKOUT

Disables local key. Only the controller can return the 380 to local.

TEK 4051	HP 9825
WBYTE @57, 17:	ll07

CLEAR LOCKOUT

Return 380 to local.

TEK 4051	HP 9825
WBYTE @57, 11:	lcl7/rem 7

SENDING THE REQUIRE SERVICE MESSAGE

The 380 sends a Require Service message if one of the following conditions exist:

- End of Sweep in a Single Sweep mode.
- Power Up: When 380 is manually switched from STBY to ON, (only if the 380 has been and is still under bus control).

The Require Service message is sent by setting the Service Request (SRQ) bus line true, and it is cleared after a serial poll has been executed by the controller.

3-33. **SENDING THE STATUS BYTE MESSAGE.** After receiving a Serial Poll Enable command (SPE) and when addressed to talk, the 380 sends a Status BYTE message. Status bit 1 is set true at End of Sweep in a Single Sweep mode. Status bit 2 is set true at Power Up. The Status bits are cleared after the 380 has been serial polled.

SYSTEM BYTE

BIT	8	7	6	5	4	3	2	1
Condition	0	Request Service	0	0	0	3	Power UP	End of Sweep

3-34. **PARALLEL POLL INTERFACE FUNCTION.** When several devices on the bus are capable of requesting service, the controller does not have to poll each device serially to determine which one it is. Another operation called a parallel poll is capable of polling up to eight devices at one time. Each device is assigned to one of the eight data lines on which to respond when a parallel poll is conducted. When the controller senses SRQ, it conducts a parallel poll by setting both ATN and EOI true at the same time. (Note: In the data mode the EOI line has the meaning of end-of-message. In the ATN true mode, it has the meaning of identify, in the sense of a parallel poll). All devices currently requesting service will then respond on their assigned data line. And by checking the bits in this poll byte, the controller can immediately determine which devices require service, without serially polling each one. If the device requesting service has more than one possible reason for asserting SRQ, the controller may also conduct a serial poll on that one device and its status byte could contain more detailed information about why SRQ was asserted.

3-35. The parallel poll interface function provides the 380 with the capability to present one bit of status to the controller without previously being addressed to talk.

3-36. The signal lines DI01 through DI08 are used to convey the device status bits during the parallel poll. This allows up to eight devices with a one-line-per-device assignment, although any number of devices can be handled through sharing DI0 lines.

3-37. The 380 employs the PP2 subset of the PP interface function. This subset omits the capability of being configured by the controller. Therefore, the PPR (parallel poll response) message and message sense (S) to be used during a parallel poll must be set on the PPL select switches numbered 1 through 8, located on the top rear cover.

3-38. A "1" is set when the switches are in the open position. The following Table 3-6 will aid in setting these switches. The PPR message specified by the switch settings will be set true during a parallel poll only when the 380 is sending the Require Service Message.

TABLE 3-6. PARALLEL POLL

PPR Message Specified	S = "1"	S = "0"
PPR1	Open only S1	Close only S1
PPR2	Open only S2	Close only S2
PPR3	Open only S3	Close only S3
PPR4	Open only S4	Close only S4
PPR5	Open only S5	Close only S5
PPR6	Open only S6	Close only S6
PPR7	Open only S7	Close only S7
PPR8	Open only S8	Close only S8

3-39. **FAST LEARN FUNCTION.** The Fast Learn Function is used when it is desired to load a frequency into the 380 in approximately 100 μs, over the interface bus. Since the frequency switching time for the 380 is less than 20 μs, total switching time using the Fast Learn Function is less than 104 μs, when using a controller capable of transferring data at a rate of 125k bytes. To use the Fast Learn Function, the 380 must first be addressed to listen and then the following syntax is used:

$$L\ \underbrace{F_{11}F_{12}F_{13}F_{14}F_{15}F_{16}S_{17}}_{\text{Frequency 1}}\qquad \underbrace{F_{21}F_{22}F_{23}F_{24}F_{25}F_{26}S_{27}}_{\text{Frequency 2}}\cdots\cdots$$

3-40. The ASCII character L must always precede the fast learn string. The next six bytes represent frequency coded as double packed BCD. Coding information is as follows Table 3-7.

TABLE 3-7. FAST LEARN CODING

DATA BYTE	DI0 DATA BITS	
	DI05-DI08	DI01-DI04
Fx1	BCD 0	BCD 1 GHz
Fx2	BCD 100 MHz	BCD 10 MHz
Fx3	BCD 1 MHz	BCD 100 KHz
Fx4	BCD 10 KHz	BCD 1 KHz
Fx5	BCD 100 Hz	BCD 10 Hz
Fx6	BCD 1.0 Hz	BCD 0.1 Hz

3-41. Leading zeros must not be suppressed and must be included with the data. Spaces and decimal points must not appear in the string.

3-42. The function of the S_{x7} byte is to strobe the frequency bits into the RF plug-in. The value of the S_{x7} byte depends upon where it appears in the string. If it is not the last character in the string, it may be a <CR> or an L. If it is the last character in the string, it must be a <CR> with EOI or a <LF>.

3-43. When the S_{x7} byte is a <CR> with EOI or <LF>, it will cause an MPU interrupt. The MPU then displays the last frequency on the LED display. The <CR> with EOI or <LF> terminates the fast learn mode. To re-enter this mode, the controller needs to output an L.

3-44. PROCEDURE FOR LOADING FAST LEARN.

1. Convert each frequency to 8 bit words as per Table 3-7.
2. Issue the words in the format shown.

Example:

Fast learn for 100 MHz and then 10 MHz.

$$\text{100 MHz}\quad \underbrace{\text{“01001100”}}_{\text{L}}\ \ \underbrace{\text{0000 0000}}_{\text{Word 1}}\ \ \underbrace{\text{0001 0000}}_{2}\ \ \underbrace{\text{0000 0000}}_{3}\ \ \underbrace{\text{“0”}}_{4,5,6}\ \ \underbrace{\text{0000 1101}}_{\text{CR}}$$

$$\text{10 MHz}\quad \underbrace{\text{“01001100}}_{\text{L}}\ \ \underbrace{\text{0000 0000}}_{\text{Word 1}}\ \ \underbrace{\text{0000 0000}}_{2}\ \ \underbrace{\text{0001 0000}}_{3}\ \ \underbrace{\text{“0”}}_{4,5,6}\ \ \underbrace{\text{0000 1101}}_{\text{CR}}$$

CHAPTER 4

PERFORMANCE TESTS

4-1. INTRODUCTION

4-2. This chapter contains the performance tests necessary to verify that the instrument is operating within its prescribed performance limits. Refer to Chapter 5 for System Calibration/Adjustments and Chapter 6 for Maintenance, if required.

4-3. The following tests and their paragraph numbers summarizes the tests to be performed to ensure that the AILTECH 380 Synthesized Signal Generator meets these specifications.

Title	Paragraph
Frequency Accuracy	4-6
Spectral Purity	4-7
Harmonics and Subharmonics	4-8
Spurious Responses	4-9
Signal-To-Phase Noise Ratio	4-10
Output Level Accuracy and Leveling	4-11
AM Modulation	4-12
FM Modulation	4-13
Phase Modulation	4-14

4-4. REQUIRED TEST EQUIPMENT

4-5. Table 4-1 summarizes the required test equipment to enable a technician to complete the performance tests. All applicable test equipment must bear evidence of current calibration.

TABLE 4-1. TEST EQUIPMENT REQUIRED

Equipment Name	Recommended Model
Counter	Systron Donner 6244A
Function Generator	Wavetek 180
Modulation Meter	Booton 82AD
Noise Analyzer	FEL 800B
Power Meter	Booton 42
Spectrum Analyzer	AILTECH 757
Variable Attenuator	HP 394
Variable Attenuator	HP 394A
Voltmeter RMS	HP 8405A
Wave Analyzer	HP 3590
X-Y Recorder	Jerrold AV-5

4-6. **FREQUENCY ACCURACY.** This test is divided into two frequency areas. The first test covers the keyboard frequency and the second covers the remote frequency.

Test Equipment Required

Counter	Systron Donner 6244A
÷ 10 Prescaler	

1. Connect the test equipment as shown in Figure 4-1.

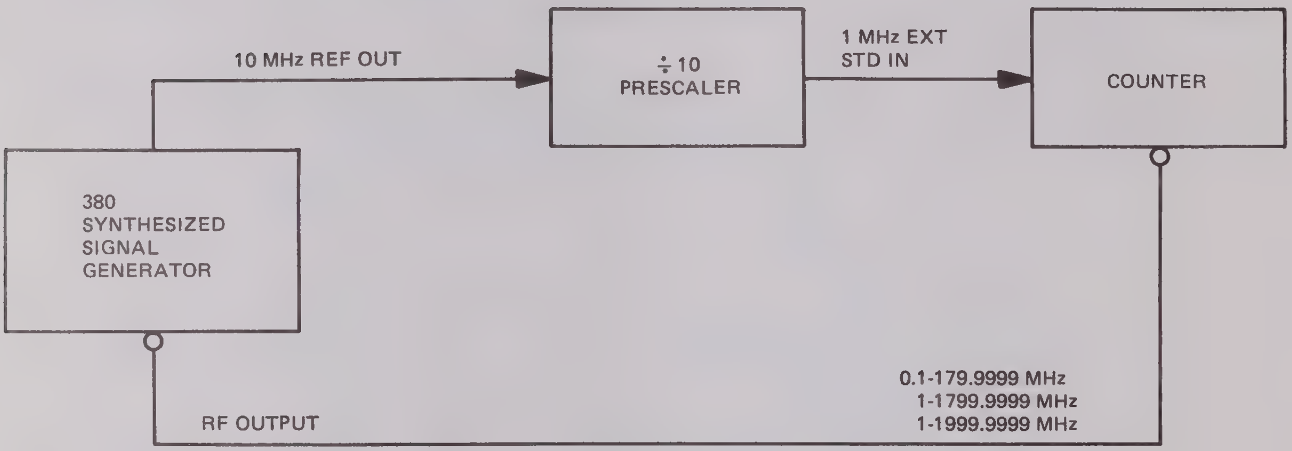

10 MHz REF OUT
÷ 10 PRESCALER
1 MHz EXT STD IN
COUNTER
380 SYNTHESIZED SIGNAL GENERATOR
RF OUTPUT
0.1-179.9999 MHz
1-1799.9999 MHz
1-1999.9999 MHz

Figure 4-1. Frequency Accuracy Test Setup

2. Set the following frequencies into the LED display using the 380 front panel keyboard.
3. Verify that output frequencies from the 380 are correct on the counter.
4. Record the output frequency readings from the counter in the column under the keyboard heading.

180 MHz Plug-In

	Keyboard	Remote
.5000000 MHz	______	______
1.1111110 MHz	______	______
6.2222220 MHz	______	______
7.3333330 MHz	______	______
9.4444440 MHz	______	______
15.5555500 MHz	______	______
26.6666660 MHz	______	______
37.7777770 MHz	______	______
48.8888880 MHz	______	______
59.9999990 MHz	______	______

180 MHz Plug-In (Cont)

	Keyboard	Remote
61.9000000 MHz	________	________
95.5555550 MHz	________	________
117.3333330 MHz	________	________
126.2222200 MHz	________	________
135.1111110 MHz	________	________
144.0000000 MHz	________	________
159.9999990 MHz	________	________
169.9999990 MHz	________	________
179.9999990 MHz	________	________

1800 and 2000 MHz Plug-Ins

	Keyboard	Remote
5.0000000 MHz	________	________
11.1111100 MHz	________	________
62.2222200 MHz	________	________
73.3333300 MHz	________	________
94.4444400 MHz	________	________
155.5555500 MHz	________	________
266.6666600 MHz	________	________
377.7777700 MHz	________	________
488.8888800 MHz	________	________
599.9999900 MHz	________	________
619.0000000 MHz	________	________
955.5555500 MHz	________	________
1173.3333300 MHz	________	________
1262.2222200 MHz	________	________
1351.1111100 MHz	________	________
1440.0000000 MHz	________	________
1599.9999900 MHz	________	________
1699.9999900 MHz	________	________
1799.9999900 MHz	________	________
1899.9999900 MHz	________	________
1999.9999900 MHz	________	________

5. Connect a bus controller to the GPIB connector J2 on the 380 rear panel.
6. Using the same test setup as in Figure 4-1, program the same frequencies as in Step 4 and check that the frequency outputs are correct on the counter.

7. Record the output frequency readings from the counter in the column under the Remote heading.

4-7. SPECTRAL PURITY

This test is provided in three phases, proceed as follows:

4-8. HARMONICS AND SUBHARMONICS.

Test Equipment Required

Attenuator	HP 394A
Noise Analyzer	FEL 800B
RMS Voltmeter	HP 8405A
Spectrum Analyzer	AILTECH 757
Wave Analyzer	HP 3590
X-Y Recorder	Jerrold AV-5

1. Connect a 757 Spectrum Analyzer to the RF OUTPUT connector on the front panel of the RF Section.
2. Set the spectrum analyzer for 40 dB RF attenuation and 0 dB IF attenuator.
3. Set scan width control for 200 MHz per division.
4. Using keyboard entry, set output level to +5 dBm, CW operation.
5. Set the 380 to sweep from 1 to 179.9 MHz (180 MHz RF Section), 1 to 1799.9 MHz (1800 MHz RF Section) or 1 to 1999.9 MHz (2000 MHz RF Section) in 10 MHz steps at 100 msec/step.
6. Observe the harmonics and subharmonics of the output signal of the 380 on the spectrum analyzer.

RF Section	.01 — 180 MHz	1 — 1800 MHz	1 — 2000 MHz
Harmonics	< -30 dBc 1.0 to 180 MHz	< -20 dBc	< -20 dBc
Subharmonics	None	< 900 MHz, None > 900 MHz < -20 dBc	< 1000 MHz, None > 1000 MHz, < -20 dBc

4-9. SPURIOUS RESPONSES.

Test Equipment Required

Spectrum Analyzer	AILTECH 757

1. Set an AILTECH 757 Spectrum Analyzer to 30 MHz/cm, and 10 dB/cm.
2. Connect the 380 to the analyzer input and set a level until the signal is at the top of display.

CAUTION

Be sure that the spectrum analyzer
is not being overdriven which will
cause false responses.

3. For the 180 MHz RF Section, carefully search through the frequency range 60 to 179.99 MHz, for any non-harmonic responses. The specification is –95 dBm.
4. For the 1800 MHz RF Section, carefully search through the frequency range 900 to 1799.99 MHz. The specification is –74 dBm.
5. For the 2000 MHz RF Section, carefully search the frequency range 1000 to 1999.99 MHz. The specification is –74 dBm.

4-10. SIGNAL-TO-PHASE NOISE RATIO.

Test Equipment Required

Noise Analyzer	FEL 800B
RMS Voltmeter	HP 8405A
Variable Attenuator	HP 394
Wave Analyzer	HP 3590
X-Y Recorder	Jerrold AV-5

1. Phase noise is measured at the frequencies indicated using the noise test setup as shown in Figure 4-2.

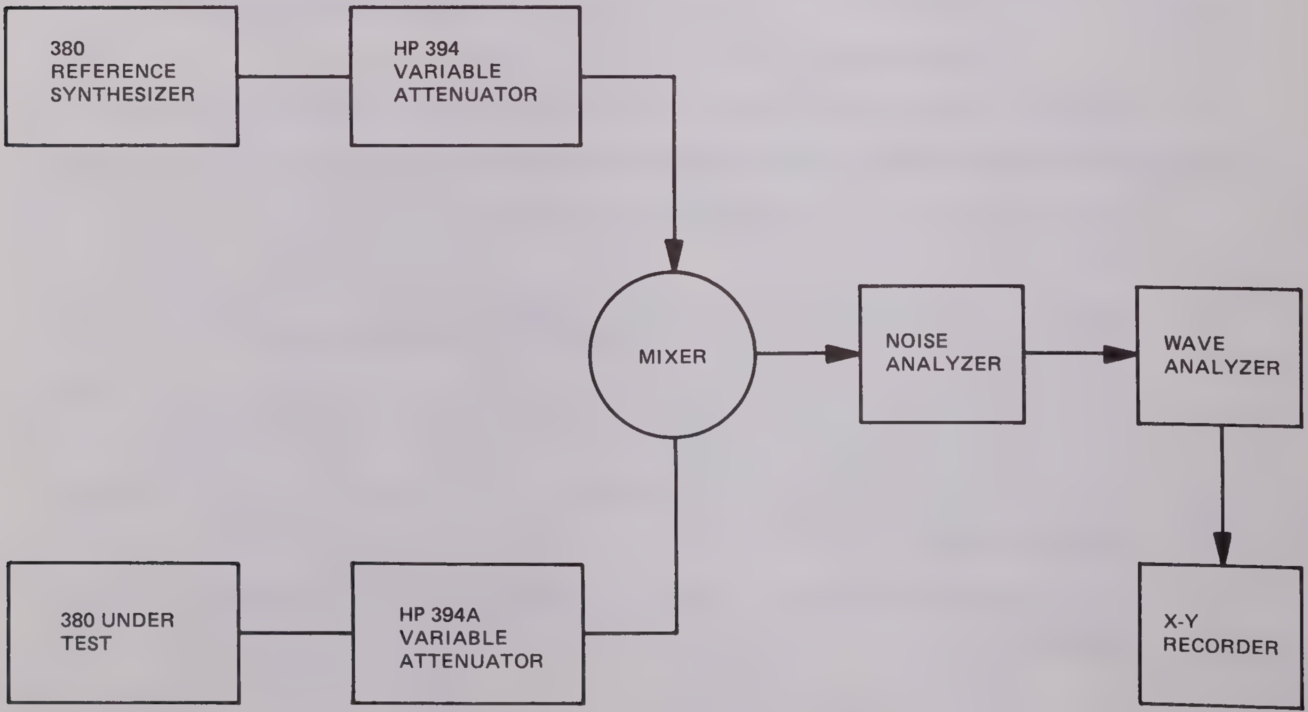

380 REFERENCE SYNTHESIZER
HP 394 VARIABLE ATTENUATOR
MIXER
NOISE ANALYZER
WAVE ANALYZER
X-Y RECORDER
380 UNDER TEST
HP 394A VARIABLE ATTENUATOR

Figure 4-2. Phase Noise Test Setup

2. The X-Y recorder is set up according to procedures in the HP 3590 handbook for a vertical scale of 20 dB/inch.

3. Set wave analyzer controls as follows:

Maximum Input Voltage	0.3V
Absolute/Relative	Absolute
Meter	Normal
Mode	BFO
Range	Auto
BW Response	Slow
Bandwidth	10 Hz
Frequency Range	64 kHz
Sweep	RESET and ON
Sweep Rate	1 Hz/sec
Frequency	10 kHz

4. Connect the BFO output to the step attenuator and connect the output of the attenuator to an RMS voltmeter. This is the CAL SIGNAL. Adjust the attenuator for a 1 mV output.

5. Connect the CAL SIGNAL to the wave analyzer input. Turn X-Y recorder On, Servo On, Pen Up, Chart on Hold. Adjust Y zero to locate pen indicator at the -93 dBc line.

6. Set noise analyzer controls as follows:

Loop BW	200 Hz
Video BW	One step clockwise pass 300 Hz
Lock Disable Switch	Lock

7. Set the 380 under test to any agreed upon frequency, i.e., 1970 MHz, at a level of 0 dBm.

8. Set the reference 380 to a frequency of 27.365 MHz higher or lower than the test frequency, at a power level of 0 dBm. Using the tuning knob, adjust the frequency in 100 Hz increments, for a good lock.

9. Adjust the variable attenuators for the lowest reading on the peak PHASE NOISE meter while maintaining adequate signal strength, as indicated on the SIG STR meter.

10. Connect the PM OUT connector of the phase noise analyzer to the RMS voltmeter. Adjust the CAL control for a reading of 7.07 Vrms.

11. Set the LOCK/DISABLE switch to LOCK position. If phase lock is not achieved, slightly readjust 380 frequency.

12. Connect the PM OUT connector of the phase noise analyzer to the input connector on the wave analyzer.

13. Set the wave analyzer frequency to 1 kHz. Move top recorder pin down to record noise level. If both 380's have the same noise characteristic (reference would have to be verified) the actual noise of the unit under test is 3 dB lower than the measurement.

14. Repeat the measurement at 10 kHz and 100 kHz. If a continuous plot is desired, use 5 cycle semi log graph paper. Use the 10 Hz wave analyzer bandwidth up to 200 Hz offset, 100 Hz bandwidth from 200 to 2000 Hz, and 1 kHz bandwidth above 2 kHz offset. If the analyzer MAX input switch is rotated one step CW each time the bandwidth is increased, calibration will be maintained.

380 Frequency ____________

Offset	Reading	Reading -3 dB	Check
1 kHz	______	____________	______
10 kHz	______	____________	______
100 kHz	______	____________	______

4-11. OUTPUT LEVEL ACCURACY AND LEVELING.

Test Equipment Required

Power Meter	Booton 42

1. Connect a power meter to the 380 RF OUTPUT connector through a calibrated 10 dB pad.

2. Set the output frequency to 1 MHz, and frequency increment to 10 MHz. Turn on the control knob.

3. Set the 380 output level to +5 dBm.

4. Slowly tune the 380 across its frequency range. Record the maximum and minimum power readings, and the frequency.

380 Setting, +5 dBm
Acceptable Readings, +2.0 to 8.0 dBm
Acceptable Leveling, $\frac{\text{Max} - \text{Min}}{2}$, ± 2.5 dBm

Maximum Level ____________ Frequency ______________
Minimum Level ____________ Frequency ______________
Check ________________

5. Repeat for a setting of 0 dBm.

Acceptable Readings, -3 to +3 dBm
Acceptable Leveling, ± 2.5 dBm

Maximum Level ____________ Frequency ______________
Minimum Level ____________ Frequency ______________
Check ________________

6. Repeat for a setting of –5 dBm.

Acceptable Readings, –8 to 2 dBm
Acceptable Leveling, ± 2.5 dBm

Maximum Level ____________ Frequency ______________
Minimum Level ____________ Frequency ______________
Check ______________

4-12. AM MODULATION.

Test Equipment Required

Function Generator	Wavetek 180
Modulation Meter	Booton 82AD

1. Set a function generator for 1.0 Vrms output at 4 kHz and connect it to the 380 RF Section AM MOD IN connector. (Figure 4-3)
2. Set the 380 for 100 MHz at +5 dBm.
3. Connect the RF OUTPUT to a modulation meter.
4. Set the 380 for 75% Modulation at EXT and record the reading. Repeat for 400 and 1000 Hz Internal.
5. Record the % AM. Acceptable readings are 70% to 80%.

EXT ____________
400 ____________
1000 ____________
Check ______________

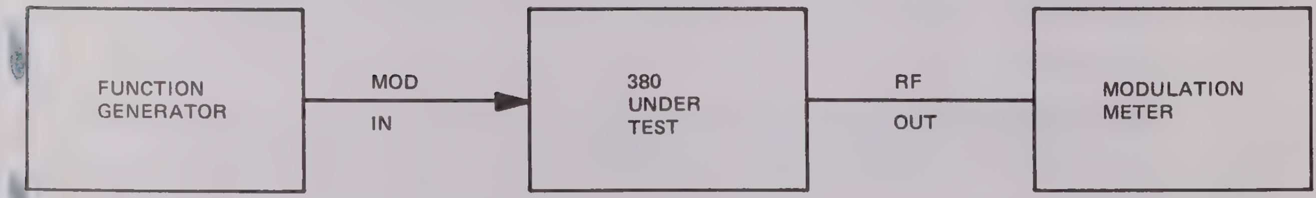


Figure 4-3. Modulation Test Setup

4-13. FM MODULATION.

Test Equipment Required

Function Generator	Wavetek 180
Modulation Meter	Booton 82AD

1. Set the function generator for a 1.0 Vrms output at 4 kHz and connect it to the 380 RF Section AM MOD IN connector.
2. Set the 380 for 100 MHz at +5 dBm.
3. Connect the RF OUTPUT to a modulation meter.
4. Set the 380 for the deviation shown at EXT Mod, FM positions.
5. Record the results.

		Accuracy
7.5 kHz	________	± 0.5 kHz
75 kHz	________	± 5.0 kHz

Check ____________

4-14. PHASE MODULATION.

Test Equipment Required

Counter	Systron Donner 6244A
Function Generator	Wavetek 180
Modulation Meter	Booton 82AD

1. Using the same test setup as indicated in Figure 4-3, set the 380 for phase modulation, EXT input.
2. Measure the frequency of the function generator on a counter and set it to 7.5 kHz ± 100 Hz.
3. Set the function generator for 1.0 Vrms and connect it to the 380 EXT MOD input connector.
4. Set the 380 for 57.5° (1 radian).
5. Record the results, readings should be within 7.5 kHz ± 0.5 kHz.

Reading ________ Check ____________

CHAPTER 5

SYSTEM CALIBRATION AND ADJUSTMENTS

5-1. INTRODUCTION

5-2. This chapter details the calibration procedures necessary to verify that the instrument is operating properly and within its prescribed operating limits. AILTECH recommends that these procedures be performed every six months or 1000 hours of operation.

5-3. This chapter covers calibration and adjustment procedures for the following:

a. Mainframe

b. RF Section

c. PM 3602 Modulator

5-4. Table 1-5 contains a tabular listing of test equipment and test accessories required for the calibration and adjustment procedures. In addition, the table contains the required minimum specifications and a suggested manufacturer's model number.

5-5. MAINFRAME

5-6. **POWER SUPPLY VOLTAGE.** This procedure provides the necessary information to adjust the three power supply voltages.

Test Equipment Required	
Digital Voltmeter	Systron Donner 7004

1. Allow the 380 to warmup in the ON mode for at least one hour.
2. Using a Phillips head screwdriver, remove top and rear covers.
3. Connect a digital voltmeter (DVM) between terminal A4FL2 and ground on the top rear of the A4 module (Figure 6-6).
4. Adjust potentiometer A11A3R9 on –12 volt regulator PC board (right hand board) for a reading of –12Vdc ± 1 mv (Figure 6-6).
5. Connect the digital voltmeter between terminal A4FL1 and ground.
6. Adjust potentiometer A11A2R10 on +5.2 volt regulator PC board (left hand board) for a reading of +5.3Vdc ± 1 mv (Figure 6-6).
7. Remove two Phillips head screws and protective cover, identified as CAUTION HIGH VOLTAGE from the rear power supply.

8. Connect a digital voltmeter between the rear terminal of the terminal strip and ground.
9. Adjust potentiometer R8 on the rear of the power supply for a reading of 5.0Vdc ± 10 mv.

5-7. **SWEEP OUTPUT VOLTAGE.** This procedure sets the sweep output voltage for a 0 volt offset.

Test Equipment Required

Digital Voltmeter	Systron Donner 7004

1. Connect a digital voltmeter to the SWP OUT BNC connector on the 380 rear panel.
2. Using the front panel keyboard, set in a START frequency of 1 MHz, a STOP frequency of 100 MHz, 100 NO. OF STEPS and MAN SWEEP.
3. Rotate the TUNING knob and set the frequency 1 MHz.
4. Adjust potentiometer A15A2R6 on the latch PC board (the middle board of three boards mounted on the rear of the 380) for a reading of 0 Vdc ± 10 mV.
5. Rotate TUNING knob and set frequency at 100 MHz. Digital voltmeter should read 2.0 Vdc ± 10 mV.

5-8. **24/33 MHz OSCILLATOR.** The following steps detail the procedure for setting the ten free running oscillators to specifications.

Test Equipment Required

Frequency Counter	Systron Donner Model 6244A

1. Turn the 380 on its left side and remove the bottom cover.
2. Remove the coaxial cable from connector A10J11 24/33 MHz IN from the HF SUB ASSY A10 module.
3. Connect a frequency counter to the disconnected coaxial cable using a suitable coaxial adapter.
4. Using the keyboard, set in a frequency of 30 MHz, and set in a tuning increment of 1 MHz.
5. Using the ⇧ or OFF ⇩ keys, step through the range of 30 to 39 MHz, adjusting the appropriate capacitor to the frequency in Table 5-1. Since the long term frequency drift is negative, set each oscillator approximately 10 kHz higher than nominal.

TABLE 5-1. NOMINAL OSCILLATOR FREQUENCIES

Frequency Selected (MHz)	Nominal Frequency	Adjustment Capacitor on A6A1
30.0000000	24	C19
31.0000000	25	C35
32.0000000	26	C39
33.0000000	27	C15
34.0000000	28	C11
35.0000000	29	C7
36.0000000	30	C3
37.0000000	31	C23
38.0000000	32	C27
39.0000000	33	C31

5-9. 10 MHz FREQUENCY STANDARD (OPTION 002 or 003). All frequencies generated by the 380 are derived from a 10 MHz oven controlled frequency standard. This adjustment sets the internal standard to within 1×10^{-9} of the house standard.

Test Equipment Required

Frequency Counter	Systron Donner Model 6244A

1. Allow the 380 to warmup for at least two hours.
2. Remove the rear cover to permit access to the internal oscillator.
3. Connect a frequency counter, referenced to the house standard, to the 380 RF section RF OUTPUT connector on the front panel.
4. Set the 380 for 100 MHz, CW at a suitable output level.
5. Remove the cap screw from the reference oscillator.
6. Set the frequency counter for ± 1 Hz resolution (1 second gate time) and adjust the reference oscillator for a counter reading of 100.000000 MHz (1×10^{-8} accuracy).
7. For the 180 MHz RF Section, set the counter for ± 0.1 Hz resolution (10 second gate time) and further adjust the reference oscillator for 100.0000000 MHz ± (1×10^{-9} accuracy).
8. For the 1.8 MHz and 2.0 GHz RF Sections, set the frequency to 1 GHz, the counter for ± 1 Hz resolution (1 second gate time) and adjust for 1000.000000 MHz +1.0 Hz (1×10^{-9} accuracy).

5-10. RF SECTIONS 180, 1800, and 2000 MHz ADJUSTMENTS.

5-11. **VHF OSCILLATOR A5.** This procedure is used to set the frequencies of the drift cancellation oscillator A5.

Test Equipment Required

Frequency Counter	Systron Donner Model 6244A

1. Remove the two cables from connectors J4 and J5 on the rear panel of the RF section.
2. Remove the RF section and reconnect to the mainframe using extender cables 298822, 296551-1 and 296551-2.
3. Connect a frequency counter to connector A5J1 (located on bottom of RF section) and set the counter resolution to 1 kHz.
4. Allow sufficient warmup time (1 hour).
5. Set the 380 to the frequencies listed in Table 5-2 for the appropriate RF section.
6. Adjust the oscillator frequencies approximately 500 kHz higher than the nominal frequency.
7. For the 1800 and 2000 MHz RF sections, the 305 MHz oscillator is adjusted 850 kHz higher than the nominal frequency.

TABLE 5-2. VHF OSCILLATOR FREQUENCIES

380 Output Frequency	VHF OSC Nominal Frequency	Adjustment Capacitor on A5A1
180 MHz RF SECTION		
160 MHz	305 MHz	C59
140 MHz	315 MHz	C60
120 MHz	325 MHz	C63
100 MHz	335 MHz	C62
80 MHz	345 MHz	C61
60 MHz	355 MHz	C68
20 MHz	365 MHz	C67
10 MHz	375 MHz	C66
1 MHz	385 MHz	C65
1800 MHz RF SECTION		
800 MHz	305 MHz*	C59
700 MHz	315 MHz	C60
600 MHz	325 MHz	C63
500 MHz	335 MHz	C62
400 MHz	345 MHz	C61

*Refer to paragraph 5-11, step 7.

TABLE 5-2. VHF OSCILLATOR FREQUENCIES (Cont)

380 Output Frequency	VHF OSC Nominal Frequency	Adjustment Capacitor on A5A1
1800 MHz RF SECTION (Cont)		
320 MHz	355 MHz	C67
20 MHz	365 MHz	C66
10 MHz	375 MHz	C65
1 MHz	385 MHz	C64
2000 MHz RF SECTION		
900 MHz	305 MHz*	C64
800 MHz	315 MHz	C59
700 MHz	325 MHz	C60
600 MHz	335 MHz	C63
500 MHz	345 MHz	C62
400 MHz	355 MHz	C61
30 MHz	365 MHz	C68
20 MHz	375 MHz	C67
10 MHz	385 MHz	C66
1 MHz	395 MHz	C65

*Refer to paragraph 5-11, step 7.

5-12. DOUBLER DRIVE LEVEL ADJUSTMENT, 1800 AND 2000 MHz RF SECTIONS. Adjusts the drive level for the 900 to 1800 MHz and 1000 to 2000 MHz ranges for the 1800 and 2000 MHz RF Sections. Normally performed after a repair or replacement of A8A1 frequency scaler.

Test Equipment Required

Power Meter	Dana Model 9008

1. Remove the two connectors J4 and J5 from the rear of the RF section, and remove the RF section from the mainframe.
2. Connect the RF section to the mainframe using extender cables 298822, 296551-1 and 296551-2.
3. Using proper adapters from the Service Kit 299252, disconnect the cable to connector A8J4, and connect a power meter to connector A8J4.
4. Set the 380 to the highest operating frequency, 1799 or 1999 MHz.

5. Set potentiometer A8A1R87 on the side cover of A8 output section fully clockwise.

6. Adjust potentiometer A8A1R87 in a counterclockwise direction until the power reading decreases by 0.5 dB, or reaches –10 dBm, whichever occurs first.

7. Reassemble the RF section.

5-13. OUTPUT LEVEL AND AMPLITUDE MODULATION ADJUSTMENTS

5-14. Output level and amplitude modulation adjustments are performed first using the RF front panel and modulation controls. Next, the adjustments for output level and amplitude modulation are performed using the keyboard.

Test Equipment Required

10 dB Coaxial Attenuator	Narda
Digital Voltmeter	Systron Donner Model 7004
Function Generator	Wavetek Model 180
Modulation Meter	Dana Model 9008
Power Meter	Boonton Model 42B
Spectrum Analyzer	AILTECH 757

5-15. OUTPUT LEVEL, RF FRONT PANEL CONTROL, 180 MHz RF SECTION.

1. Remove the right side panel and top cover from the 380.

2. Allow the instrument to warmup for at least one hour before making any adjustments.

3. Set the output frequency to 100 MHz.

4. Activate the RF PANEL controls using the SHIFT AND PANEL/LOCAL keys.

5. Set the modulator MODE switch to the CW position.

6. Connect the power meter and a calibrated 10 dB attenuator to the RF OUTPUT connector.

7. Connect a digital voltmeter to A8FL3 (second terminal from rear on top of A8) and ground, and set for DC volts.

8. Set the 10 dB step switch to +10 dBm (181 option only).

9. Set the OUTPUT LEVEL (vernier) control fully clockwise.

10. Record digital voltmeter reading. _______________ Vdc

11. Set the OUTPUT LEVEL (vernier) control fully counterclockwise. Adjust potentiometer A3R8 for a DVM reading which is 1/10 the voltage recorded in step 10.

12. Adjust linearity control potentiometer A8R73 (access through cover of A8) for a power output of –7 dBm.

13. Set the OUTPUT LEVEL (vernier) control fully clockwise, and readjust potentiometer A3R3, if necessary for a power output of + 13 dBm.
14. Repeat steps 12 and 13 until the levels of steps 12 and 13 are achieved.
15. Disconnect the DVM, and set the OUTPUT LEVEL (vernier) control fully counterclockwise. Adjust potentiometer A3R8 for a power output of 0 dBm.
16. Set OUTPUT LEVEL (vernier) fully clockwise. Power output should be +13 dBm, if not, readjust potentiometer A3R3.
17. Repeat steps 15 and 16 until no further adjustment is necessary.

5-16. AMPLITUDE MODULATION LINEARITY, 180 MHz RF SECTION.

1. Activate RF panel controls using the SHIFT and PANEL/LOCAL keys.
2. Set MODE switch to AM position.
3. Set the SOURCE switch to EXT position, MOD LEVEL control fully clockwise (PM3602 only).
4. Set the OUTPUT LEVEL (vernier) control fully clockwise. Set the OUTPUT LEVEL (10 dB Step) control to +10 dBm (181 option only).
5. Set the function generator for a 5 kHz sinewave at a level of about 1 Vrms and connect to the MOD IN front panel connector.
6. Set the 380 for 100 MHz output frequency.
7. Connect the modulation meter or spectrum analyzer to the RF OUTPUT connector on the front panel.
8. Adjust the modulation meter for AM, or adjust the spectrum analyzer to 100 MHz center frequency, 10 kHz division, 5 dB/division and a 1 kHz bandwidth.
9. With the OUTPUT LEVEL (vernier) control fully clockwise adjust the level of the function generator into the MOD IN connector for a 70% AM reading on the modulation meter, or if a spectrum analyzer is used, adjust for modulation sidebands 9 dB below the carrier (70% modulation).
10. Set the OUTPUT LEVEL (vernier) control fully counterclockwise. The modulation reading will probably change from 70% as read on the modulation meter or spectrum analyzer.
11. Adjust linearity control potentiometer A8R73 for 70% modulation.
12. Repeat steps 9, 10, and 11 until 70% modulation is obtained with the OUTPUT LEVEL (vernier) control at fully counterclockwise or fully clockwise positions.
13. Disconnect the function generator, the modulation meter or spectrum analyzer and connect the power meter (using a 10 dB pad).

14. Set the OUTPUT LEVEL (vernier) control fully clockwise. Power should read +13 dBm. If not, adjust potentiometer A3R3.
15. Set the OUTPUT LEVEL (vernier) control fully counterclockwise Power should read 0 dBm. If not, adjust potentiometer A3R8.
16. Connection function generator, modulation meter or spectrum analyzer, and recheck AM linearity and level, steps 9 through 16, until no further adjustment is required.

5-17. OUTPUT LEVEL, RF FRONT PANEL CONTROL, 1800 AND 2000 MHz RF SECTIONS.

1. Remove the right side panel and top cover from the 380.
2. Allow the instrument to warmup for a least one hour before making any adjustments.
3. Set the output frequency to 1.0 GHz.
4. Activate the RF PANEL controls using the SHIFT and PANEL/LOCAL keys.
5. Set the modulator MODE switch to the CW position.
6. Connect the power meter and a calibrated 10 dB attenuator to the RF OUTPUT connector.
7. Locate Low Level Set potentiometer A3R8 on the right side of the 380 and adjust potentiometer to maximum clockwise position.
8. Remove three hold down screws from the logic converter PC board A9 in the top of the RF section, and lift PC board to provide access to LIN ADJ potentiometer:

1800 MHz	A8A2R3
2000 MHz	A8A2R33

   Take care to ensure that all connectors on logic PC board remain connected.
9. Set the 10 dB step switch on RF section front panel to 0 dBm. (180 series option only).
10. Set OUTPUT LEVEL (vernier) to the +5 dBm position.
11. Adjust High Level Ref potentiometer A3R3 on right side of 380 for a +5 dBm output as read on power meter.
12. Rotate OUTPUT LEVEL (vernier) fully counterclockwise (minimum power).
13. Adjust LIN ADJ potentiometer A8A2R3, R33 (under logic converter PC board) for -6 dBm output. (0 dBm for instruments without 180 series attenuator).
14. Repeat steps 10 through 13 until 1 to 2 dB above correct reading of minimum output level.
15. Adjust Low Level Set potentiometer A3R8 on right side of instrument for correct power at minimum level.

5-18. AMPLITUDE MODULATION LINEARITY 1800 AND 2000 MHz RF SECTIONS.

1. Activate RF PANEL controls by using the SHIFT and PANEL/LOCAL keys (RF PANEL LED on).
2. Set MODE switch to AM position.
3. Set SOURCE switch to EXT position.
4. Set OUTPUT LEVEL (vernier) to the +5 dBm position.
5. Set the function generator for a 5 kHz sinewave at a level of about 1 Vrms and connect to the MOD IN front panel connector.
6. Set the 380 output frequency to 1.0 GHz.
7. Connect the modulation meter or the spectrum analyzer to the RF OUTPUT connector on the front panel.
8. Adjust the modulation meter for AM or adjust the spectrum analyzer to 1 GHz center frequency, 10 kHz/division, 5 dB/division and 1 kHz bandwidth.
9. Adjust the level of the function generator into the MOD IN connector for a 70% AM reading on the modulation meter, or if a spectrum analyzer is used, adjust for modulation sidebands 9 dB below the carrier (70% modulation).
10. Rotate the OUTPUT LEVEL (vernier) fully counterclockwise (minimum power). The modulation reading will probably change from 70% as read on the modulation meter, or spectrum analyzer.
11. Adjust LIN ADJ potentiometer A8A2R3 or A8A2R33, (located under the logic converter PC board A9) for 70% modulation.
12. Disconnect the function generator, and the modulation meter or spectrum analyzer.
13. Connect the power meter through a 10 dB pad to the RF OUTPUT connector.
14. Readjust Low Level Set potentiometer A3R8 on the right side of the instrument for a correct power reading with the OUTPUT LEVEL (vernier) control at minimum power (fully counterclockwise).
15. Set the OUTPUT LEVEL (vernier) to the +5 dBm position.
16. Adjust High Level Ref potentiometer A3R3 for a +5 dBm measured power.
17. Re-connect the function generator to the MOD IN connector.
18. Connect the modulation meter or spectrum analyzer to the RF OUTPUT connector.
19. Adjust the level of the function generator into the MOD IN connector for 70% modulation reading on the modulation meter or spectrum analyzer.

20. Repeat steps 10 through 19 until the modulation reading without further adjustment is within 68 to 72%.

5-19. AM METER CALIBRATION.

1. Set a function generator for a 5 kHz sinewave at a level of 1 Vrms (using the DVM) and connect to the MOD IN connector.
2. Activate RF panel controls by depressing SHIFT AND PANEL/LOCAL keys.
3. Set MODE switch to AM position.
4. Set SOURCE switch to EXT position.
5. Set MOD LEVEL control fully clockwise.
6. Remove top front bezel strip (3 Phillips head screws) and locate Meter Cal Adj potentiometer A4R30 on top of modulator. (Refer to PM3602 manual pages 4-28 and 4-29 for potentiometer A4R30 location).
7. Carefully adjust Meter Cal Adj potentiometer A4R30 for a full scale reading on the front panel. Tap the meter gently to obtain an accurate setting.
8. Set the OUTPUT LEVEL (vernier) to the -5 dBm position for 180 MHz/0 dBm for 1800, 2000 MHz, and the OUTPUT LEVEL (10 dB Step) control for highest power (fully clockwise).
9. Connect the modulation meter or spectrum analyzer to the RF OUTPUT connector.
10. Carefully adjust the function generator output level to 0.7 Vrms, using the DVM.
11. Adjust AM Set potentiometer A3R4 on the right side of the instrument for a 70% ± 3% reading on the modulation meter or spectrum analyzer, at OUTPUT LEVEL (vernier) settings +5, 0 and -5 dBm, for 1800/2000 MHz RF sections, and -10, -5, and 0 dBm, for 180 MHz RF sections.

5-20. KEYBOARD CALIBRATION OF OUTPUT LEVEL AND AM FOR 180, 1800 AND 2000 MHz RF SECTIONS. Calibrates output level and AM to the keyboard controls. These adjustments are performed after the adjustments in paragraphs 5-15, 5-18, and 5-19 have been completed.

1. Set the 380 using keyboard entry (RF Panel LED OFF) for a frequency of 100 MHz for 180 MHz RF Section and 1000 MHz for the 1800/2000 MHz RF Section. Set power to 0 dBm, 70% AM, and modulator SOURCE switch to EXT. Allow at least 1 hour warmup.
2. Set a function generator for a 5 kHz sinewave and set the level accurately to 1.0 Vrms with the digital voltmeter. Connect to the MOD IN connector.
3. Locate REM CAL ADJ potentiometer A4R35, and integrated circuit A4U3 pin 15, on top of modulator section. (Refer to PM3602 manual pages 4-28 and 4-29 for parts location).

4. Connect a digital voltmeter between integrated circuit A4U3 pin 15 and ground.
5. Adjust REM CAL ADJ potentiometer A4R35 for a 0.7 Vrms reading on the digital voltmeter.
6. Activate the RF panel controls for Level and Modulation. (Shift Panel/Local, LED ON).
7. Set the MODE switch to the CW position.
8. Connect the power meter to the RF OUTPUT connector and adjust the OUTPUT LEVEL (vernier) for a +5 dBm reading on the power meter for 180 MHz RF Sections, and 0 dBm for 1800/2000 MHz RF Sections.
9. Disconnect the power meter and connect the modulation meter or spectrum analyzer to the RF OUTPUT connector.
10. Set the MODE switch to the AM position and adjust the MOD LEVEL control for 70% ± 1% modulation as read on the external modulation meter or spectrum analyzer. (Sidebands 9 dB below carrier)
11. Locate terminal A8FL3, REF IN for 180 MHz RF Section, or A8FL7 for 1800/2000 MHz RF Sections. The terminals are located at the top right rear corner of the instrument adjacent to programming connectors J4 and J5. Refer to the appropriate RF Section manuals for location of the above terminal.
    - a Terminal A8FL3 REF IN, P360180E1 manual, pages 4-14 and 4-61.
    - b Terminal A8FL7 LEV REF, P3601.8E1 and P3602.0E1 manuals, pages 4-11 and 4-63.
12. Set a digital voltmeter for AC volts, and connect it to terminal A8FL3 and ground for 180 MHz RF Section or terminal A8FL7 and ground for 1800 or 2000 MHz RF Sections. Record the voltage to ±1 mV. __________VAC
13. Set 380 for keyboard entry.
14. Using the keyboard entry, enter power levels of +5 dBm for 180 MHz RF Section, or 0 dBm for 1800/2000 MHz RF Sections.
15. Set amplitude modulation level to 70%, modulation source to EXT, using the 380 keyboard controls.
16. Lift logic converter PC Board A9 to expose Remote High Level potentiometer A13R13 and Remote Low Level potentiometer A13R14.
17. Adjust Remote High Level potentiometer A13R13 (front potentiometer) for the same AC voltage recorded in step 12.
18. Disconnect the function generator input from the MOD IN connector, and disconnect modulation meter or spectrum analyzer from the RF OUTPUT connector.
19. Connect a power meter to the RF OUTPUT connector, through a 10 dB pad.

20. Adjust Remote Low Level potentiometer A13R14 (rear potentiometer) for +5 dBm for 180 MHz RF Section and 0 dBm for 1800/2000 MHz RF Sections, as measured on the power meter.

21. Using the keyboard, enter power levels of +10 dBm and 0 dBm for 180 MHz RF Sections, and +5 dBm, and -5 dBm for 1800/2000 MHz RF Sections. Record the dB error of both levels as read on the power meter. Split the difference and readjust Remote High Level potentiometer A13R13 to minimize error.

22. Disconnect the power meter from the RF OUTPUT connector and connect the modulation meter or spectrum analyzer to the RF OUTPUT connector.

23. Connect the function generator to the MOD IN connector, set at the previous frequency and level.

24. Using keyboard entry set for 70% EXT AM, and check modulation readings at output levels of 13, 5 and 0 dBm for 180 MHz RF Section and for 1800/2000 RF Sections +5 dBm and -5 dBm. Readings should be within 5%. If this cannot be obtained, slight readjust LIN ADJ potentiometer:

    a. 180 MHz Plug In, A8A2R73
    b. 1800 MHz Plug In, A8A2R3
    c. 2000 MHz Plug In, A8A2R33

25. If a modulation meter is used, set keyboard for 70% AM, Internal 400 or 1000 Hz Source. Set output level for 0 dBm. Modulation meter reading should be within 68 to 72%. If not, slightly readjust potentiometer A13R13.

26. Disconnect modulation meter or spectrum analyzer, and connect the power meter to the RF OUTPUT connector.

27. Using keyboard entry set in a power level +13 dBm for 180 MHz RF Sections and +3 dBm for 1800/2000 MHz RF sections. Set MODE switch to CW position. (Shift CW/FREQ.)

28. Set the output frequency to 1.0 MHz.

29. Using the increment set function, set in an increment of 1 MHz for 180 MHz, RF Section and 5 MHz for 1800/2000 MHz RF Sections.

30. Energize the TUNING knob.

31. Manually sweep the frequency through the full range starting at 1.0 MHz, in the preset increments.

32. Record the highest power level and frequency and the lowest power level and the frequency. The readings should be between 11.5 dBm and 14.5 dBm (13 dBm ± 1.5 dB) for 180 MHz RF Sections and 0 dBm and 6 dBm (3 dBm ± 3 dB), for 1800/2000 MHz RF Sections.

33. To equalize the variation in both directions, determine the amount and direction of the required change. Set the keyboard for 100 MHz for the 180 MHz RF Section and 1.0 GHz for 1800/2000 MHz RF Sections. Set output for +13 dBm for 180 MHz RF Section and +3 dBm for 1800/2000 MHz RF Sections.
34. Adjust the Remote High Level Set potentiometer A13R13 for the required change as read on the power meter. Record the power reading _____dBm.
35. Transfer control to the RF panel. (Shift PANEL/LOCAL, LED on)
36. Set MODE switch to the CW position.
37. Set the OUTPUT LEVEL (vernier) to +13 dBm for 180 MHz RF Sections, and +3 dBm for 1800/2000 MHz RF Sections.
38. Adjust High Level Ref potentiometer A3R3 on the right side of the instrument for the same power reading as in step 33.
39. Recheck steps 31 and 32.

5-21. PM3602 MODULATOR ADJUSTMENTS

5-22. DESCRIPTION. This procedure calibrates the 400 and 1000 Hz internal oscillator, adjusts the loop gain and center frequency of the FM oscillator, and calibrates the modulation meter and LED display for FM and ϕM.

5-23. 400 Hz and 1000 Hz INTERNAL OSCILLATOR.

Test Equipment Required

Cable Assembly	AILTECH 299543-3
Digital Voltmeter	Systron Donner 7004
Extender Cable	AILTECH 299820
Frequency Counter	Racal Dana 7720

1. Allow the 380 to warmup in the operate mode for a least one hour.
2. Remove the PM3602 Modulator from the 380 and reconnect it to the 380 using the extender cable.
3. Disconnect coaxial connectors and power connector A4P6 of phase lock loop A2 assembly.
4. Remove three screws securing A2 assembly and swing assembly aside.
5. Using keyboard entry, set for AM modulation, 70% Internal 400 Hz.
6. Connect a frequency counter to modulation oscillator connector A1A3J7 and adjust 400 Hz ADJ potentiometer A1A3R4 for a counter reading of 400 ± 4 Hz.
7. Using keyboard entry, set for 1 kHz internal modulation.

8. Adjust 1000 Hz ADJ potentiometer A1A3R5 for a counter reading of 1000 ± 10 Hz. Reconnect internal cable to connector A1A3J7.
9. Connect a digital voltmeter to pin 15 of A4U3. (See page 4-28 of PM3602 manual for location).
10. Set the digital voltmeter for AC volts, 1.0 Vrms scale.
11. Adjust LEVEL ADJ potentiometer A1A3R13 for a voltmeter reading of 0.70 Vrms ± 0.01V.
12. Check that the modulation oscillator shuts off when EXT SOURCE is entered. Also in CW MODE with either 400 or 1000 Hz Internal.
13. Reconnect the A2 assembly.

5-24. **MODULATION METER CALIBRATION.** Refer to paragraph 5-19, steps 1 through 7 and follow the procedure for AM meter calibration.

5-25. FM PHASE LOCK LOOP ADJUSTMENT.

Test Equipment Required

Adapter BNC Female to SMB Plug	294677
Adapter SMB Tee	294678
Cable Assembly	AILTECH 299453-3
Digital Voltmeter	Systron Donner 7004
Frequency Counter	Dana 7720
Insulated board — to protect modulator	

1. Remove top cover from 380.
2. Remove PM3602 Modulator, place on top of 380 using the insulated board, and reconnect modulation section connector. Do not use extender cable.
3. Transfer control to RF Panel (SHIFT PANEL/LOCAL, LED on).
4. Set modulator MODE switch to FM, SOURCE switch to EXT, PEAK DEVIATION to 10 KHz, and MOD LEVEL full counterclockwise.
5. Remove coax cables from A2J3 and A2J5 and connect cables together using the Tee adapter, AILTECH part number 294678.
6. Connect a frequency counter to connector A2J3.
7. Adjust 29 MHz VCO tuning capacitor A2A1C9 for a counter reading of 29 MHz ± 5 kHz.
8. Disconnect frequency counter from connector A2J3.

9. Connect 380 RF output to connector A2J5.
10. Set OUTPUT LEVEL for 0 dBm at 29.000 MHz.
11. Connect a digital voltmeter to test point A2A3TP1 and set the digital voltmeter for the 1.0 V dc range.
12. Record voltage to within 0.1 mV. (Voltage will be about 0 volts ± 50 mV).
13. Change frequency to 29.1 MHz and record new voltage at TP1. The difference between the voltage readings at 29.9 MHz and 29.1 MHz should be 12.5 mV ± 1.0 mV.
14. If the voltage difference is not within 12.5 ± 1.0 mV, adjust GAIN ADJ potentiometer A2A3R13 in small increments until the difference is within the tolerance.
15. Connect frequency counter to connector A2J3.
16. Set 380 for 28.0 MHz and slowly increment the frequency upward in 100 kHz steps until counter reading is the same as the 380 frequency, indicating phase lock.
17. Record this frequency. It should be less than 28.7 MHz.
18. Set 380 for 29.9 MHz and slowly increment the frequency lower in 100 kHz steps until the counter indicates phase lock, as above. This frequency should be greater than 29.3 MHz.
19. If the requirements of steps 15 through 18 are not achieved, repeat steps 6 through 14.
20. Reconnect all modulator cables.

5-26. FM CALIBRATION.

Test Equipment Required

Digital Voltmeter	Systron Donner 7004
Frequency Counter	Dana 7720
Function Generator	Wavetek 180
Spectrum Analyzer	AILTECH 757
Modulation Meter	Dana Model 9008

1. Activate the keyboard for control of output level and modulation (RF PANEL LED off).
2. Using keyboard entry, set the 380 frequency to 40 MHz, FM at a deviation of 70 kHz and EXT SOURCE. If a modulation meter is available, calibrate using either the 400 Hz or 1 kHz Internal Source. In this case, proceed to step 3.
3. Using a frequency counter and a digital voltmeter, adjust a sinewave generator (function generator) for an output frequency of 29.14 kHz, and a level of 1.0 Vrms and connect it to the MOD IN connector on the front panel.

4. Locate integrated circuit A4U11, FM STEREO SEPARATION ADJ potentiometer A4R38, and connector A4J3 on the logic control PC board A4 (front left side of the modulator).
5. Place 380 in STBY mode, and using an ohmmeter, connect the leads between A4U11 pin 10, and connector A4J3.
6. Adjust FM STEREO SEPARATION ADJ potentiometer A4R38 for a nominal 200 ohms.
7. Place 380 in ON mode, and connect the digital voltmeter, set for AC volts, between connector A4J3 and ground.
8. Locate BAND 1 CAL ADJ potentiometer A4R6 on the logic control PC board A4 and adjust for 0.07 Vrms. (This assumes that REM CAL ADJ potentiometer A4R35 has been adjusted as per the AM calibration paragraph 5-19).
9. Connect a modulation meter or a spectrum analyzer to the 380 RF OUTPUT connector and tune to 40 MHz. Set analyzer scan width to 30 kHz/div., and center signal on CRT display.
10. Loosen two thumbscrews on front of modulator and remove rear bracket screw. Slide forward from RF section exposing FM SENSITIVITY ADJ potentiometer A2A1R10 on bottom of modulator.
11. Adjust R10 for 70 kHz deviation as read on the modulation meter, or the first carrier null as observed on the spectrum analyzer.
12. Replace modulator in RF section.

5-27. **STEREO SEPARATION ADJUSTMENT.** This adjustment can be used to optimize stereo separation when the synthesizer is modulated with an FM Stereo Multiplex signal. The FM STEREO SEPARATION ADJ potentiometer A4R38 is located on logic control PC board A4. (For location refer to PM3602 manual page 4-28). The control is set at the factory for a nominal resistance of 200 ohms.

1. Unfasten the PM3602 Modulator front panel screws, and the modulator rear bracket screw, and slide the modulator forward slightly to allow access to potentiometer A4R38.
2. Connect an FM stereo multiplex signal to the MOD IN connector.
3. Using keyboard entry, set for FM mode, 70 kHz deviation, EXT SOURCE.
4. Connect 380 to an FM deviation meter with a stereo decoder. Follow normal procedure for measuring separation. If separation is less than 40 dB, adjust R38 for best separation, 40 to 48 dB.
5. Adjustment of R38 will somewhat affect the FM calibration. If necessary, repeat paragraph 5-26, however, adjust BAND 1 CAL ADJ A4R6 instead of R10 in step 11, for correct calibration.
6. Adjust BAND 1 CAL ADJ potentiometer A4R6 located on the top PC board, for the first carrier null.

5-28. PHASE MODULATION CALIBRATION.

Test Equipment Required

Digital Voltmeter	Systron Donner 7004
Function Generator	Wavetek 180
Extender Cable	AILTECH 299820
Spectrum Analyzer	AILTECH 757

1. Using a Phillips head screwdriver, remove two black flathead screws securing modulation front panel.
2. Carefully pull front panel clear of modulator, being careful not to put excessive strain on connecting wiring. Rest panel on a suitable support.
3. Activate keyboard for control of RF output, level and modulation (RF PANEL LED off).
4. Set the 380 to 100 MHz, phase modulation, 137.5 degrees, EXT SOURCE.
5. Connect a digital voltmeter, set for DC volts, between test point A1A2TP1, located next to A2R9 and ground.
6. Adjust VARACTOR BIAS potentiometer A1A2R18 for a reading of approximately -3Vdc.
7. Set the function generator to approximately 20 kHz, and connect it to the front panel MOD IN connector.
8. Connect the spectrum analyzer to the 380 front panel RF OUTPUT connector, tune for 100 MHz, and set the analyzer for a scan width of 30 kHz/div.
9. Starting with a low function generator level, increase the function generator output until the first carrier null is observed on the analyzer.
10. Adjust the function generator DC offset in conjunction with output level, to obtain the lowest carrier null.
11. Switch off the DC offset on the function generator. Preferably use a DC block, as any small offset will cause a calibration error.
12. Readjust VARACTOR BIAS ADJ potentiometer A1A2R18 for the lowest null depth observed in step 10.
13. Adjust linearity potentiometers A1A2R3 and R9 to increase the depth of the null.
14. Accurately set the function generator level into the front panel MOD IN connector to 1.0 Vrms. Use a DC block to eliminate any residual DC voltage which will cause an error in calibration.

15. Locate the ϕMOD SENS ADJ potentiometer A1A2R22, on the top right hand side of the modulator housing.
16. With the 380 still set for 137.5 degrees phase modulation, set ϕMOD SENS ADJ potentiometer R22 for the first carrier null.
17. Reassembly the front panel to the modulator using a Phillips head screwdriver and two black flathead screws.

CHAPTER 6

MAINTENANCE

6-1. INTRODUCTION

6-2. This chapter contains maintenance information for the 380K11 Mainframe and the 3600E1 Frequency Extender. The maintenance philosophy used provides defined troubleshooting procedures to isolate a malfunction to a card or subassembly, then provides sufficient test data on each card or subassembly to allow a technician to further troubleshoot to a faulty component. To achieve these goals, this chapter contains the principles of operation of the signal generator and frequency extender to familiarize the technician with the operation of the units; a troubleshooting chart to isolate a malfunction to a card or subassembly; complete photographs of the units so that the technician can identify a faulty card or subassembly; principles of operation of each card and subassembly along with schematic diagrams, critical test data, service block diagrams, and parts location diagrams to enable the technician to isolate a malfunction to a particular component. In addition, this chapter contains several critical performance tests and adjustments to enable the technician to maintain the signal generator and frequency extender in optimum operating condition.

6-3. PRINCIPLES OF OPERATION

6-4. **SYNTHESIZED SIGNAL GENERATOR MAINFRAME.** (Figure 6-1.) The 380K11 Synthesized Signal Generator Mainframe produces an output between 35 and 36 MHz selectable in 1.0-Hz increments (0.1 Hz with option -001). The basis for the extremely high accuracy of the 380K11 Synthesized Signal Generator is 10-MHz standard oscillator A13 (-002 or -003 option). This oscillator is a highly stable, crystal-controlled oscillator in a proportionally controlled oven. If the -002 or -003 option is not included, an external 5-MHz or 10-MHz reference source may be used. The accuracy of the frequency synthesizer depends on the accuracy of the reference source used.

6-5. The reference output is applied to fixed frequency generator input board A5A1, where the 10-MHz signal is amplified and filtered before being distributed to various circuits in the mainframe. If a 5-MHz input is used, A5A1 acts as a frequency doubler producing the 10-MHz required by the rest of the unit. The output of A5A1 is applied to all the other printed circuit boards in A5 (A5A2-A5A6). In A5A2, the 10-MHz signal is supplied to two amplifier and filter channels to provide two 10-MHz outputs. One output is applied to the RF plug-in section being used. The second output is supplied to an output connector at the rear of the frequency synthesizer to serve as a highly accurate 10-MHz reference signal. Board A5A3 is a 60-MHz generator. In A5A3, the 10-MHz signal is multiplied by 6, filtered, and the resultant 60-MHz output is supplied to 1 to 5 MHz comb generator A10A5 in the frequency extender.

6-6. The 65-MHz generator (A5A4) and 64-MHz generator (A5A5) generate the two comb line frequencies used in the mainframe. The 65-MHz generator multiplies the 10-MHz input by 13 and divides by 2 to produce the 65-MHz output. The 64-MHz generator multiplies the 10-MHz input by 8, divides the result by 5, and multiplies the 16-MHz result by 4 to produce the 64-MHz output. The 64- and 65-MHz output of A5A4 and A5A5, respectively, are both applied through A4 to all BCD sections.

6-7. The 10-MHz also is applied to 35/35.5 MHz board A5A6. In A5A6, the 10-MHz signal is multiplied by 7 and divided by 2, and the resultant 35-MHz signal is applied to the board output as well as to a divide-by-10 circuit within the board. The 64-MHz input to A5A6 (from A5A5) is mixed with the second harmonic of the divide-by-10 circuit (7 MHz) and bandpass filtered to produce a 71-MHz signal. This signal is divided by 2 to produce the 35.5-MHz output signal. A logic circuit on the card, which is controlled by the plug-in logic, determines whether the 35-MHz or 35.5-MHz signal will be available at the A5A6 output. This output provides the starting frequency for the BCD synthesis section.

6-8. The 35/35.5 MHz output of A5A6 is applied to lower order BCD board A2A5. With the -001 option, the A2A5 board produces the tenths of a Hertz output. When this option is not used, the A2A5 board is replaced by a jumper board and the tenths of a Hertz digit is not produced. The 35/35.5 MHz is applied through the jumper board to A2A4. The jumper board also transfers control of the 35/35.5 MHz logic to the 0.1 Hz logic line instead of the 0.05 Hz logic as it would be controlled with option -001. The 64 and 65 MHz comb line frequencies are also applied to the lower order BCD boards from A4A6 after being buffered on 10 kHz BCD board A4A11. These buffered comb line frequencies are applied directly to A2A1 and are further buffered and distributed from there to A2A2, A2A3, A2A4, and A2A5.

6-9. Selection of a frequency to be produced by the signal generator is set into the keyboard front panel A1A1. The frequency information is routed through the MPU assembly and into the plug-in section which contains a logic converter. The logic converter is in the plug-in section because each plug-in unit requires different logic signals depending upon the frequency range of the particular unit. The logic signals are returned to the frequency synthesizer mainframe through the plug-in module interface, J1 and J2, and are distributed by logic and power distribution board A14 to all BCD boards. Table 6-1 lists the signal name at each pin of the interface connector.

6-10. The logic signals representing the selected frequency output cause the lower order BCD boards to generate the lower order numbers. Board A2A5 generates the tenth of a Hertz digit (if -001 option is used); A2A4 generates the 1-Hz digit; A2A3 generates the 10-Hz digit, A2A2 generates the 100-Hz digit; and A2A1 generates the 1-kHz digit. Each digit is generated and added to the output of the BCD stage which feeds that particular stage thus forming a chain. (See paragraph 6-66 for a detailed description of how each BCD stage operates to produce its applicable digit). The output of A2A1 is applied to the higher order BCD assembly.

6-11. The higher order BCD assembly selects the 10-kHz and 100-kHz digits in the frequency synthesizer output. A4A11 generates the 10-kHz steps and A4A1-A4A10 produce the 100-kHz step. A4A1 through A4A4 are driven by 64/65 MHz switches A4A7 through A4A10, respectively. The BCD-8 #2 board (A4A5) functions as a final divider, filter, and amplifier for the selected frequency synthesizer mainframe output.

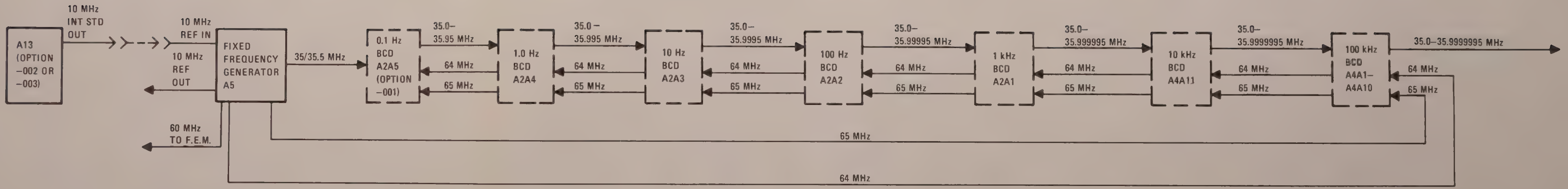

A13
(OPTION
–002 OR
–003)
10 MHz
INT STD
OUT
10 MHz
REF IN
10 MHz
REF
OUT
60 MHz
TO F.E.M.
FIXED
FREQUENCY
GENERATOR
A5
35/35.5 MHz
0.1 Hz
BCD
A2A5
(OPTION
–001)
35.0–
35.95 MHz
1.0 Hz
BCD
A2A4
35.0 –
35.995 MHz
10 Hz
BCD
A2A3
35.0–
35.9995 MHz
100 Hz
BCD
A2A2
35.0–
35.99995 MHz
1 kHz
BCD
A2A1
35.0–
35.999995 MHz
10 kHz
BCD
A4A11
35.0–
35.9999995 MHz
100 kHz
BCD
A4A1–
A4A10
35.0–35.9999995 MHz
64 MHz
65 MHz

Figure 6-1. Synthesized Signal Generator Mainframe, Functional Block Diagram

TABLE 6-1. PLUG-IN/MAINFRAME INTERFACE (PMI) WIRE LIST

From	To	Signal Name
J1-A	A14 J6-16	BCD 1 kHz-1
J1-B	A1/A1 J1-13	Loc 100 kHz-2
J1-C	A5 J2	10 MHz (Coax)
J1-D	A1/A1 J1-26	Loc 100 kHz-8
J1-E	A14 J6-5	BCD 1 kHz-8
J1-F	A1/A1 J1-12	Loc 100 kHz-4
J1-H	A14 J6-18	BCD 1 kHz-2
J1-J	A1/A1 J1-25	Loc 100 kHz-1
J1-K	A14 J6-15	BCD 1 kHz-4
J1-L	A1/A1 J1-11	Loc 1 MHz-4
J1-M	A14 J6-1	BCD 100 Hz-2
J1-N	A1/A1 J1-24	Loc 1 MHz-2
J1-P	A14 J6-3	BCD 100 Hz-4
J1-R	A1/A1 J1-10	Loc 1 MHz-8
J1-S	A14 J6-9	BCD 1.0 Hz-1
J1-T	A1/A1 J1-23	Loc 1 MHz-1
J1-U	A14 J6-11	BCD 1.0 Hz-8
J1-V	A1/A1 J1-9	Loc 10 MHz-4
J1-W	A14 J6-10	BCD 1.0 Hz-2
J1-X	A1/A1 J1-22	Loc 10 MHz-2
J1-Y	A14 J6-12	BCD 1.0 Hz-4
J1-Z	A1/A1 J1-8	Loc 10 MHz-8
J1-AA	A1/A1 J1-15	Loc 100 Hz-4
J1-BB	A14 J6-22	FEM 1 MHz-2
J1-CC	A1/A1 J1-2	Loc 100 Hz-1
J1-DD	A14 J6-19	FEM 1 MHz-8
J1-EE	A1/A1 J1-14	Loc 100 Hz-2
J1-FF	A14 J6-23	FEM 1 MHz-1
J1-HH	A1/A1 J1-1	Loc 100 Hz-8
J1-a	A14 J6-24	BCD 0.1 Hz-8
J1-b	A1/A1 J1-21	Loc 10 MHz-1
J1-c	A14 J6-25	BCD 0.1 Hz-4
J1-d	A1/A1 J1-7	Loc 1 kHz-4

TABLE 6-1. PLUG-IN/MAINFRAME INTERFACE (PMI) WIRE LIST (Cont)

From	To	Signal Name
J1-e	A14 J6-5	BCD 10 Hz-1
J1-f	A1/A1 J1-20	Loc 1 kHz-2
J1-h	A14 J6-26	BCD 0.1 Hz-2
J1-j	A1/A1 J1-6	Loc 1 kHz-1
J1-k	A14 J6-20	BCD 10 Hz-8
J1-m	A1/A1 J1-19	Loc 1 kHz-8
J1-n	A14 J6-13	BCD 0.1 Hz-1
J1-p	A1/A1 J1-5	Loc 1.0 Hz-4
J1-r	A14 J6-7	BCD 10-Hz-4
J1-s	A1/A1 J1-18	Loc 1.0 Hz-2
J1-t	A14 J6-6	BCD 10 Hz-2
J1-u	A1/A1 J1-4	Loc 1.0 Hz-8
J1-v	A14 J6-14	BCD 100 Hz-1
J1-w	A1/A1 J1-17	Loc 1.0 Hz-1
J1-x	A14 J6-2	BCD 100 Hz-8
J1-y	A14 J6-8	0.05 Hz Log
J1-z	A14 J6-21	FEM 1 MHz-4
J2-A	Chassis Gnd	Gnd
J2-B	A14 J3-6	5.2V
J2-C	A10 J5	30/40 MHz (coax)
J2-D	A14 J3-7	5.2V
J2-E	A14 J3-5	5.2V
J2-F	A1/A1 J2-13	Loc 0.1 Hz-8
J2-H	A10 J10	29 MHz (coax)
J2-J	A1/A1 J2-26	Loc 10 Hz-4
J2-K	A14 J5-1	BCD 10 kHz-8
J2-L	A1/A1 J2-12	Loc 0.1 Hz-4
J2-M	A7 J2	29 MHz (coax)
J2-N	A1/A1 J2-25	Loc 10 Hz-8
J2-P	A14 J5-6	BCD 10 kHz-1
J2-R	A1/A1 J2-11	Loc 0.1 Hz-2
J2-S	A14 J5-3	BCD 10 kHz-2
J2-T	A1/A1 J2-24	Loc 10 Hz-2
J2-U	A14 J5-2	BCD 10 kHz-4

TABLE 6-1. PLUG-IN/MAINFRAME INTERFACE (PMI) WIRE LIST (Cont)

From	To	Signal Name
J2-V	A1/A1 J2-10	Loc 0.1 Hz-1
J2-W	Not Used	
J2-X	A1/A1 J2-23	Loc 10 Hz-1
J2-Y	A14 J5-4	RESLT2
J2-Z	A1/A1 J2-9	Loc 100 MHz-4
J2-AA	A1/A1-J2-17	Loc 10 kHz-8
J2-BB	A14 J3-1	-12 VOP
J2-CC	A1/A1 J2-4	Loc 10 kHz-2
J2-DD	A14 J3-9	-12 VOP
J2-EE	A14 J3-4	5.2V
J2-FF	A14 J3-2	-12 VOP
J2-HH	A14 J5-24	Loc/Rem
J2-a	Not Used	
J2-b	A1/A1 J2-22	Loc 100 MHz-2
J2-c	A14 J5-5	RESLT1
J2-d	A14 J5-11	Loc/Rem
J2-e	A14 J5-10	BCD 100 kHz-1
J2-f	A1/A1 J2-21	Loc 100 MHz-8
J2-h	A14 J5-7	BCD 100 kHz-8
J2-j	A1/A1 J2-8	Loc 100 MHz-1
J2-k	A14 J5-9	BCD 100 kHz-2
J2-m	A1/A1 J2-20	Loc 1 GHz-4
J2-n	A14 J5-8	BCD 100 kHz-4
J2-p	A1/A1 J2-7	Loc 1 GHz-2
J2-r	Not Used	
J2-s	A1/A1 J2-19	Loc 1 GHz-8
J2-t	Not Used	
J2-u	A1/A1 J2-6	Loc 1 GHz-1
J2-v	Not Used	
J2-w	A1/A1 J2-18	Loc 10 kHz-1
J2-x	Not Used	
J2-y	A1/A1 J2-5	Loc 10 kHz-4
J2-z	Not Used	

6-12. FREQUENCY EXTENDER. (Figure 6-2)

6-13. The purpose of the frequency extender is to add 1-MHz steps to the 35- to 36-MHz output of the frequency synthesizer mainframe, to extend the output to cover a 30- to 40-MHz output for use by certain plug-in units. The principle of drift cancellation is used to accomplish this extension.

6-14. Logic inputs to 24 to 33 MHz oscillator A6 control the frequency generated by A6. One output of A6 is applied to 29 MHz converter A10A6. The same logic which selects the A6 output also is applied to 1 to 5 MHz comb generator A10A5. This logic selects a comb line frequency between 1 and 5 MHz, which when mixed with the A6 output, results in a constant 29-MHz output from 29-MHz converter A10A6. The 1 to 5 MHz comb lines are developed from a 60-MHz input to A10A5 using a programmable counter technique. The 60-MHz input comes from 60-MHz generator A5A3 in the mainframe.

6-15. Another output of A6 is mixed in A10A2 with a 337-MHz signal from 337 MHz oscillator/ isolator A10A1 to produce a 361- to 370-MHz output. This output is amplified and band-pass filtered in 361 to 370 MHz filter amplifier A8 and applied to a frequency converter on A10A4.

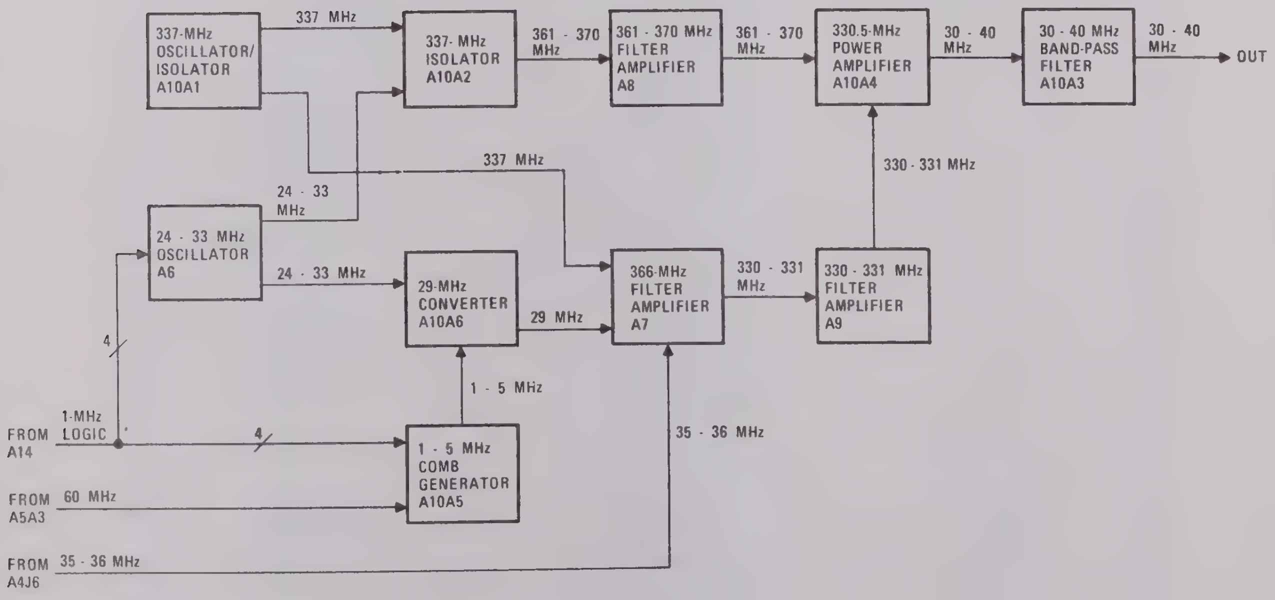

337-MHz OSCILLATOR/ ISOLATOR A10A1
337 MHz
337- MHz ISOLATOR A10A2
361 - 370 MHz
361 - 370 MHz FILTER AMPLIFIER A8
361 - 370 MHz
330.5-MHz POWER AMPLIFIER A10A4
30 - 40 MHz
30 - 40 MHz BAND-PASS FILTER A10A3
30 - 40 MHz
OUT
337 MHz
330 - 331 MHz
24 - 33 MHz
24 - 33 MHz OSCILLATOR A6
24 - 33 MHz
29-MHz CONVERTER A10A6
29 MHz
366-MHz FILTER AMPLIFIER A7
330 - 331 MHz
330 - 331 MHz FILTER AMPLIFIER A9
4
1 - 5 MHz
FROM A14
1-MHz LOGIC
4
1 - 5 MHz COMB GENERATOR A10A5
35 - 36 MHz
FROM A5A3
60 MHz
FROM A4J6
35 - 36 MHz

Figure 6-2. Frequency Extender, Functional Block Diagram

6-16. A second 337-MHz output of A10A1 is applied to 366 MHz filter amplifier A7 and mixed with the 29-MHz input from A10A6 to produce a 366-MHz signal. After band-pass filtering, the 366-MHz signal is mixed with the 35- to 36-MHz signal from the mainframe to produce a signal between 330 and 331 MHz. This signal is band-pass filtered in 330 to 331 MHz filter amplifier A9 and applied to 330.5 MHz power amplifier A10A4.

6-17. In 330.5 MHz power amplifier A10A4, the 330- to 331-MHz output of A9 is amplified and mixed with the 361 to 370 MHz output of A8 to produce a frequency between 30 and 40 MHz. This 30- to 40-MHz output is band-pass filtered in 30 to 40 MHz band-pass filter A10A3 before being applied to the plug-in unit being used.

6-18. The Drift Cancellation principle can be illustrated by observing what happens if the 24-33 MHz oscillator is deviated from its nominal value by a frequency error Δ f. For the case where 30.000000 MHz is selected, a logic 0 is programmed to the frequency extender module. The 24-33 MHz oscillator should be at 24 MHz and the 1-5 MHz comb generator is at 5 MHz. The frequency in the 361-370 MHz filter will then be Δf higher than 361 MHz. At the same time, however, the 29 MHz signal in A10A6 will be Δf high causing the 366 MHz signal in A7 to high by Δf also. This causes the 330 MHz signal to be high by Δf, but this signal is subtracted from the 361 x Δf MHz signal in A10A4. The net result is $(361 + \Delta F) - (330 + \Delta f) = 30$ MHz which is exactly what was selected. Therefore any drift of the 24-33 MHz oscillator is cancelled by this technique.

6-19. TROUBLESHOOTING

6-20. **SYNTHESIZED SIGNAL GENERATOR MAINFRAME.** Figure 6-3 is a troubleshooting chart for the signal generator mainframe. Through use of the chart and the servicing block diagram (Figure 6-4) a malfunction can be isolated to a board or subassembly. Figure 6-5 provides an interconnection diagram for the digital subsystem. Figure 6-6 shows the location of all boards and subassemblies in the mainframe. To further isolate a malfunction to a defective component on a board or subassembly see the applicable paragraph on repair of the board or subassembly.

6-21. **FREQUENCY EXTENDER.** The troubleshooting procedure philosophy for the frequency extender is the same as that for the mainframe as described in paragraph 6-20. Figure 6-7 is the troubleshooting chart, figure 6-8 is the servicing block diagram, and figure 6-9 shows the location of boards and subassemblies in the frequency extender.

6-22. REPAIR

6-23. The following paragraphs provide repair data for boards and subassemblies of the frequency synthesizer mainframe. (Repair data for the frequency extender is given in paragraphs 6-207 through 6-244.) The data is presented in the form of functional description (with significant circuit parameters), schematic diagrams, parts location diagrams, and references to the servicing block diagrams, replaceable parts list, and recommended test equipment to be used (Table 1-5). With all of the data presented, an experienced technician should be able to isolate a malfunction to a component on a board or subassembly with the board or subassembly either in the frequency synthesizer (normal system signals being used) or on a bench (simulated signals being used). To further aid in understanding the functional descriptions presented in the following paragraphs, figure 6-10 is provided to show the internal structure of all integrated circuits used in the signal generator.

6-24. Repair data for each printed circuit board can be found on the servicing block diagram, the referenced schematic diagrams, and in the functional descriptions. To facilitate identification of parts, component location diagrams are included for each printed circuit card. A description of each component is given in Chapter 7.

6-25. FRONT PANEL ASSEMBLY A1. (Figure 6-11)

6-26. The front panel assembly contains the keyboard assembly A1A1, encoder board assembly A1A2 and optical encoder A1A3.

6-27. **KEYBOARD ASSEMBLY A1A1.** (Figure 6-12 through 6-15). The keyboard contains 35 key switches, 11 numeric displays, 29 LEDs, 4 octal latches and 2 resistor networks. This board serves as the human interface to the 380 Synthesized Signal Generator.

6-28. **Keyswitch Arrays.** The 35 keyswitches are organized into two arrays, A and B. All switches are single-pole, single-throw, momentary action mechanical switches.

6-29. **Numeric Indicators (U1-U11).** The numeric indicators have on-board decoder/driver and memory within each display package.

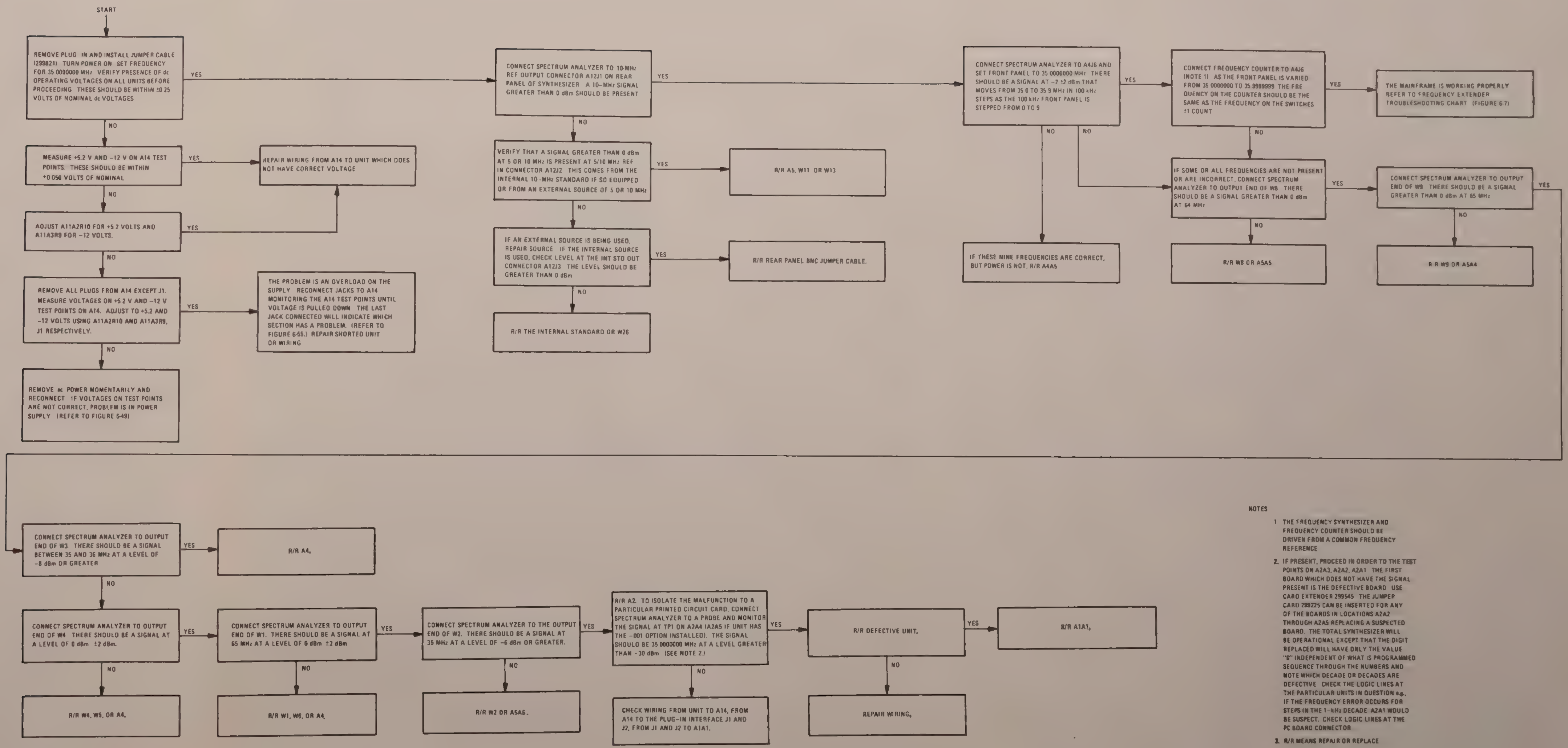
6-30. The numeric indicator decodes positive 8421 BCD logic inputs into characters 0-9, a minus sign, a test pattern, and four blanks in the invalid BCD states. The unit employs a left-hand decimal point which is turned on by a V_{IL} when the indicator is enabled.

6-31. The indicators are wired in a bit parallel, character serial configuration. The five data inputs of each display are parallel wired to the buffered data bus lines (DX0 through DX4). The latch enable input of each display is wired to an output (DSP-00 through DSP-10) on one of the two address decoders located on the encoder board. When the latch enable input is driven low, the numeric indicator will display the decoded BCD inputs and decimal point if enabled. When the latch enable returns to a logic high, the input data will become latched and displayed continuously until changed again.

6-32. **LED Latches.** The four octal latches U15, 16, 17, 12 store the required states of the various LEDs. These latches have their data inputs parallel wired to the DX0 through DX7 signals and the clock input of each latch is wired to one of the address decoder outputs (DSP-11, DSP-13, DSP-14 or DSP-15).

6-33. The output control input of each latch is wired to ground for continuous operation. On the rising edge of the clock input, the logic levels at the data inputs get latched at the output and drive the appropriate LEDs. The outputs of each latch are connected to the cathodes of the LED indicators. These outputs are capable of sinking 25 mA, which is sufficient to drive the LED without additional drivers.

6-34. The anodes of each LED are wired to a 330 ohm resistor. These resistors are in resistor network packages (U13 and U14). The other end of each resistor is internally tied to a common pin, and externally tied to VCC. This provides the proper current limiting for each LED.



REMOVE PLUG IN AND INSTALL JUMPER CABLE (299821) TURN POWER ON SET FREQUENCY FOR 35 0000000 MHz VERIFY PRESENCE OF dc OPERATING VOLTAGES ON ALL UNITS BEFORE PROCEEDING THESE SHOULD BE WITHIN ±0 25 VOLTS OF NOMINAL dc VOLTAGES

MEASURE +5.2 V AND −12 V ON A14 TEST POINTS. THESE SHOULD BE WITHIN +0 050 VOLTS OF NOMINAL

REPAIR WIRING FROM A14 TO UNIT WHICH DOES NOT HAVE CORRECT VOLTAGE

ADJUST A11A2R10 FOR +5 2 VOLTS AND A11A3R9 FOR −12 VOLTS.

REMOVE ALL PLUGS FROM A14 EXCEPT J1. MEASURE VOLTAGES ON +5.2 V AND −12 V TEST POINTS ON A14. ADJUST TO +5.2 AND −12 VOLTS USING A11A2R10 AND A11A3R9, J1 RESPECTIVELY.

THE PROBLEM IS AN OVERLOAD ON THE SUPPLY RECONNECT JACKS TO A14 MONITORING THE A14 TEST POINTS UNTIL VOLTAGE IS PULLED DOWN THE LAST JACK CONNECTED WILL INDICATE WHICH SECTION HAS A PROBLEM. (REFER TO FIGURE 6-55.) REPAIR SHORTED UNIT OR WIRING

REMOVE dc POWER MOMENTARILY AND RECONNECT IF VOLTAGES ON TEST POINTS ARE NOT CORRECT, PROBLEM IS IN POWER SUPPLY (REFER TO FIGURE 6-49)

CONNECT SPECTRUM ANALYZER TO 10-MHz REF OUTPUT CONNECTOR A12J1 ON REAR PANEL OF SYNTHESIZER A 10–MHz SIGNAL GREATER THAN 0 dBm SHOULD BE PRESENT

VERIFY THAT A SIGNAL GREATER THAN 0 dBm AT 5 OR 10 MHz IS PRESENT AT 5/10 MHz REF IN CONNECTOR A12J2 THIS COMES FROM THE INTERNAL 10-MHz STANDARD IF SO EQUIPPED OR FROM AN EXTERNAL SOURCE OF 5 OR 10 MHz

R/R A5, W11 OR W13

IF AN EXTERNAL SOURCE IS BEING USED, REPAIR SOURCE IF THE INTERNAL SOURCE IS USED, CHECK LEVEL AT THE INT STD OUT CONNECTOR A12J3 THE LEVEL SHOULD BE GREATER THAN 0 dBm

R/R REAR PANEL BNC JUMPER CABLE.

R/R THE INTERNAL STANDARD OR W26

CONNECT SPECTRUM ANALYZER TO A4J6 AND SET FRONT PANEL TO 35 0000000 MHz THERE SHOULD BE A SIGNAL AT −2 ±2 dBm THAT MOVES FROM 35 0 TO 35 9 MHz IN 100 kHz STEPS AS THE 100 kHz FRONT PANEL IS STEPPED FROM 0 TO 9

IF THESE NINE FREQUENCIES ARE CORRECT, BUT POWER IS NOT, R/R A4A5

CONNECT FREQUENCY COUNTER TO A4J6 (NOTE 1) AS THE FRONT PANEL IS VARIED FROM 35 0000000 TO 35 9999999 THE FREQUENCY ON THE COUNTER SHOULD BE THE SAME AS THE FREQUENCY ON THE SWITCHES ±1 COUNT

THE MAINFRAME IS WORKING PROPERLY REFER TO FREQUENCY EXTENDER TROUBLESHOOTING CHART (FIGURE 6-7)

IF SOME OR ALL FREQUENCIES ARE NOT PRESENT OR ARE INCORRECT, CONNECT SPECTRUM ANALYZER TO OUTPUT END OF W8 THERE SHOULD BE A SIGNAL GREATER THAN 0 dBm AT 64 MHz

R/R W8 OR A5A5

CONNECT SPECTRUM ANALYZER TO OUTPUT END OF W9 THERE SHOULD BE A SIGNAL GREATER THAN 0 dBm AT 65 MHz

R/R W9 OR A5A4

CONNECT SPECTRUM ANALYZER TO OUTPUT END OF W3. THERE SHOULD BE A SIGNAL BETWEEN 35 AND 36 MHz AT A LEVEL OF −8 dBm OR GREATER

R/R A4.

CONNECT SPECTRUM ANALYZER TO OUTPUT END OF W4 THERE SHOULD BE A SIGNAL AT A LEVEL OF 0 dBm ±2 dBm.

R/R W4, W5, OR A4.

CONNECT SPECTRUM ANALYZER TO OUTPUT END OF W1. THERE SHOULD BE A SIGNAL AT 65 MHz AT A LEVEL OF 0 dBm ±2 dBm

R/R W1, W6, OR A4.

CONNECT SPECTRUM ANALYZER TO THE OUTPUT END OF W2. THERE SHOULD BE A SIGNAL AT 35 MHz AT A LEVEL OF −6 dBm OR GREATER.

R/R W2 OR A5A6.

R/R A2. TO ISOLATE THE MALFUNCTION TO A PARTICULAR PRINTED CIRCUIT CARD, CONNECT SPECTRUM ANALYZER TO A PROBE AND MONITOR THE SIGNAL AT TP1 ON A2A4 (A2A5 IF UNIT HAS THE −001 OPTION INSTALLED). THE SIGNAL SHOULD BE 35 0000000 MHz AT A LEVEL GREATER THAN −30 dBm (SEE NOTE 2.)

CHECK WIRING FROM UNIT TO A14, FROM A14 TO THE PLUG-IN INTERFACE J1 AND J2, FROM J1 AND J2 TO A1A1.

R/R DEFECTIVE UNIT.

REPAIR WIRING.

R/R A1A1.

NOTES

1. THE FREQUENCY SYNTHESIZER AND FREQUENCY COUNTER SHOULD BE DRIVEN FROM A COMMON FREQUENCY REFERENCE
2. IF PRESENT, PROCEED IN ORDER TO THE TEST POINTS ON A2A3, A2A2, A2A1 THE FIRST BOARD WHICH DOES NOT HAVE THE SIGNAL PRESENT IS THE DEFECTIVE BOARD USE CARD EXTENDER 299545 THE JUMPER CARD 299225 CAN BE INSERTED FOR ANY OF THE BOARDS IN LOCATIONS A2A2 THROUGH A2A5 REPLACING A SUSPECTED BOARD. THE TOTAL SYNTHESIZER WILL BE OPERATIONAL EXCEPT THAT THE DIGIT REPLACED WILL HAVE ONLY THE VALUE "0" INDEPENDENT OF WHAT IS PROGRAMMED SEQUENCE THROUGH THE NUMBERS AND NOTE WHICH DECADE OR DECADES ARE DEFECTIVE CHECK THE LOGIC LINES AT THE PARTICULAR UNITS IN QUESTION e.g., IF THE FREQUENCY ERROR OCCURS FOR STEPS IN THE 1-kHz DECADE A2A1 WOULD BE SUSPECT. CHECK LOGIC LINES AT THE PC BOARD CONNECTOR
3. R/R MEANS REPAIR OR REPLACE

Figure 6-3. Frequency Synthesizer Mainframe, Troubleshooting Chart

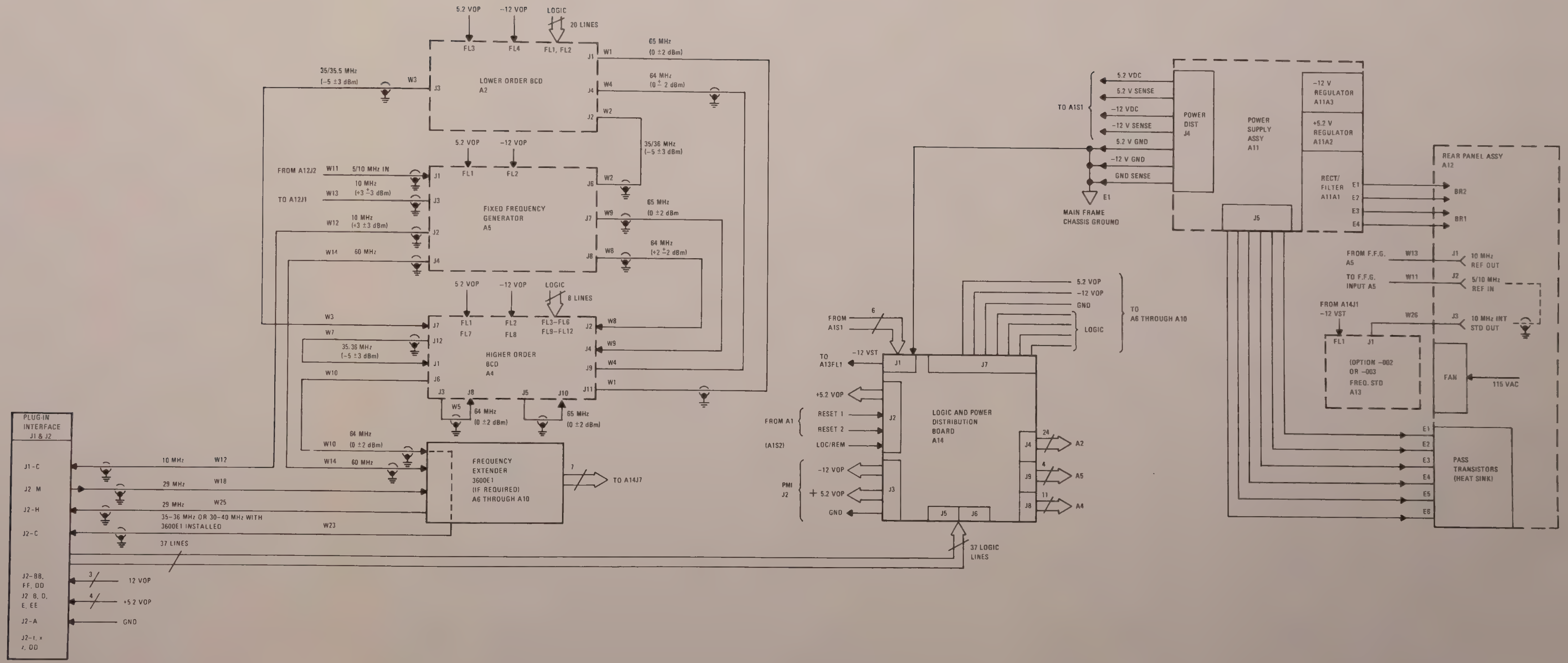

5.2 VOP
-12 VOP
LOGIC
20 LINES
FL3
FL4
FL1, FL2
LOWER ORDER BCD
A2
J1
W1
65 MHz
(0 ±2 dBm)
J4
W4
64 MHz
(0 ± 2 dBm)
J2
W2
35/35.5 MHz
(-5 ±3 dBm)
W3
J3
35/36 MHz
(-5 ±3 dBm)
FROM A12J2
W11
5/10 MHz IN
TO A12J1
W13
10 MHz
(+3 ±3 dBm)
W12
10 MHz
(+3 ±3 dBm)
W14
60 MHz
FL1
FL2
FIXED FREQUENCY
GENERATOR
A5
J6
J7
W9
65 MHz
(0 ±2 dBm
J8
W8
64 MHz
(+2 ±2 dBm)
8 LINES
FL1
FL7
FL2
FL8
FL3-FL6
FL9-FL12
W7
J12
35/36 MHz
(-5 ±3 dBm)
W10
HIGHER ORDER
BCD
A4
J9
J11
J5
J10
W5
64 MHz
(0 ±2 dBm)
65 MHz
(0 ±2 dBm)
PLUG-IN
INTERFACE
J1 & J2
J1-C
J2 M
J2-H
J2-C
J2-BB,
FF, DD
J2 B, D,
E, EE
J2-A
J2-t, x
z, DD
10 MHz
29 MHz
W18
W25
35-36 MHz OR 30-40 MHz WITH
3600E1 INSTALLED
W23
37 LINES
3
12 VOP
4
+5.2 VOP
GND
64 MHz
(0 ±2 dBm)
FREQUENCY
EXTENDER
3600E1
(IF REQUIRED)
A6 THROUGH A10
7
TO A14J7
FROM
A1S1
6
TO
A13FL1
-12 VST
+5.2 VOP
FROM A1
RESET 1
RESET 2
(A1S2)
LOC/REM
PMI
J2
-12 VOP
+ 5.2 VOP
GND
LOGIC AND POWER
DISTRIBUTION
BOARD
A14
J5
J6
37 LOGIC
LINES
24
A2
4
A5
11
A4
5.2 VOP
-12 VOP
GND
LOGIC
TO
A6 THROUGH A10
TO A1S1
5.2 VDC
5.2 V SENSE
-12 VDC
-12 V SENSE
5.2 V GND
-12 V GND
GND SENSE
E1
MAIN FRAME
CHASSIS GROUND
POWER
DIST
J4
POWER
SUPPLY
ASSY
A11
-12 V
REGULATOR
A11A3
+5.2 V
REGULATOR
A11A2
RECT/
FILTER
A11A1
E1
E2
E3
E4
REAR PANEL ASSY
A12
BR2
BR1
FROM F.F.G.
A5
W13
J1
10 MHz
REF OUT
TO F.F.G.
INPUT A5
W11
J2
5/10 MHz
REF IN
FROM A14J1
-12 VST
W26
J3
10 MHz INT
STD OUT
FL1
J1
(OPTION -002
OR -003
FREQ. STD
A13
FAN
115 VAC
E1
E2
E3
E4
E5
E6
PASS
TRANSISTORS
(HEAT SINK)

Figure 6-4. Frequency Synthesizer Mainframe, Servicing Block Diagram

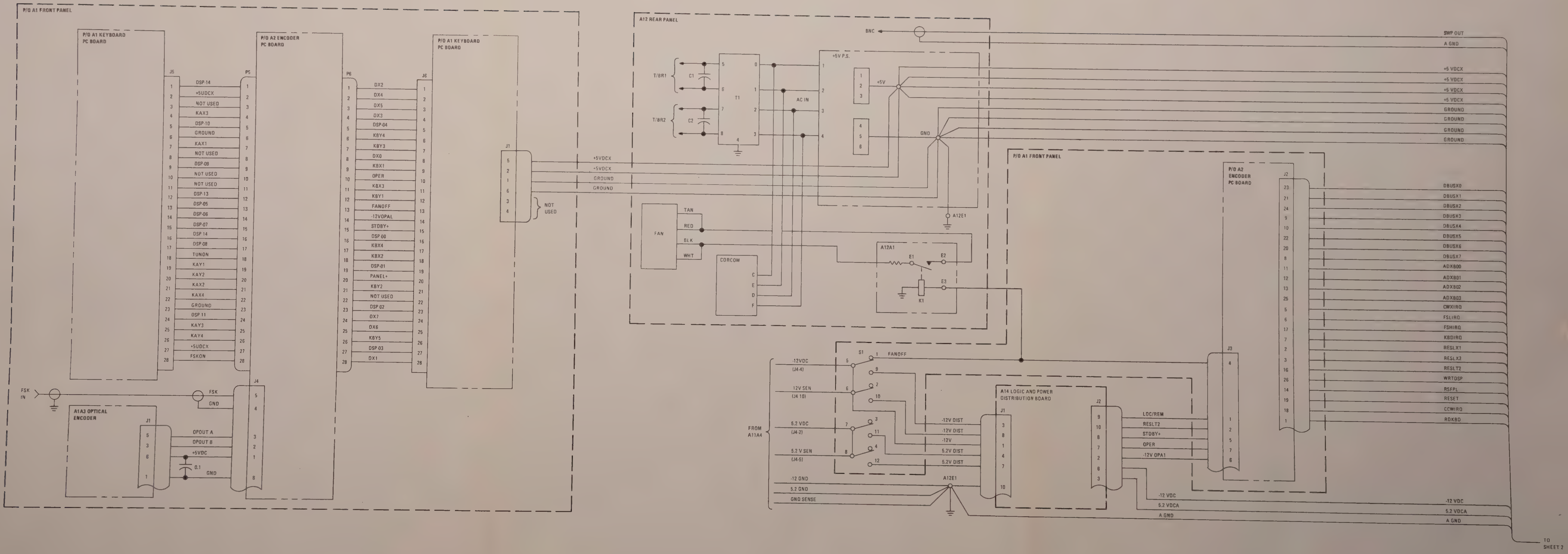

P/O A1 FRONT PANEL
P/O A1 KEYBOARD PC BOARD
P/O A2 ENCODER PC BOARD
P/O A1 KEYBOARD PC BOARD
J5
P5
P6
J6
J1
J4
DSP-14
+5UDCX
NOT USED
KAX3
DSP-10
GROUND
KAX1
NOT USED
DSP-09
NOT USED
NOT USED
DSP-13
DSP-05
DSP-06
DSP-07
DSP-14
DSP-08
TUNON
KAY1
KAY2
KAX2
KAX4
GROUND
DSP 11
KAY3
KAY4
+5UDCX
FSKON
DX2
DX4
DX5
DX3
DSP-04
KBY4
KBY3
DX0
KBX1
OPER
KBX3
KBY1
FANOFF
-12VOPAL
STDBY+
DSP 00
KBX4
KBX2
DSP-01
PANEL+
KBY2
NOT USED
DSP 02
DX7
DX6
KBY5
DSP 03
DX1
NOT USED
FSK IN
FSK
GND
A1A3 OPTICAL ENCODER
OPOUT A
OPOUT B
+5VDC
0.1
GND
+5VDCX
+5VDCX
GROUND
GROUND
A12 REAR PANEL
BNC
T/BR1
T/BR2
C1
C2
T1
AC IN
+5V P.S.
+5V
GND
A12E1
FAN
TAN
RED
BLK
WHT
CORCOM
A12A1
E1
E2
E3
K1
SWP OUT
A GND
+5 VDCX
+5 VDCX
+5 VDCX
+5 VDCX
GROUND
GROUND
GROUND
GROUND
P/O A1 FRONT PANEL
P/O A2 ENCODER PC BOARD
J2
J3
DBUSX0
DBUSX1
DBUSX2
DBUSX3
DBUSX4
DBUSX5
DBUSX6
DBUSX7
ADXBD0
ADXBD1
ADXBD2
ADXBD3
CWXIRQ
FSLIRQ
FSHIRQ
KBDIRQ
RESLX1
RESLX2
RESLT2
WRTDSP
RSFPL
RESET
CCWIRQ
RDXBD
S1
FANOFF
-12VDC (J4-4)
-12V SEN (J4 10)
5.2 VDC (J4-2)
5.2 V SEN (J4-5)
FROM A11A4
-12 GND
5.2 GND
GND SENSE
A12E1
-12V DIST
-12V DIST
-12V
5.2V DIST
5.2V DIST
A14 LOGIC AND POWER DISTRIBUTION BOARD
J1
J2
LOC/REM
RESLT2
STDBY+
OPER
-12V OPA1
-12 VDC
5.2 VDCA
A GND
-12 VDC
5.2 VDCA
A GND
TO SHEET 2

Figure 6-5. Digital Subsystem Interconnection Diagram (Sheet 1)

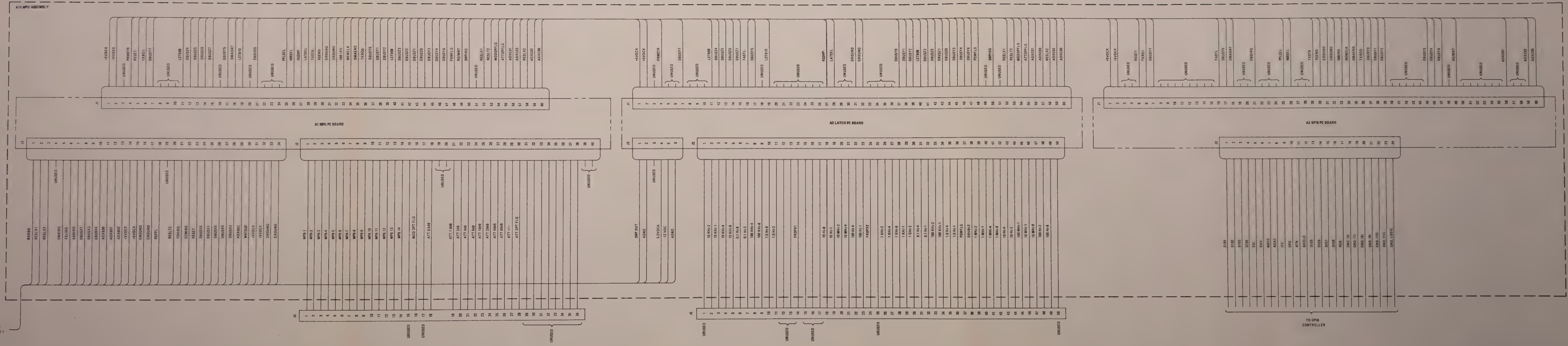

A15 MPU ASSEMBLY
A1 MPU PC BOARD
A2 LATCH PC BOARD
A3 GPIB PC BOARD
J1
J2
J3
J4
J5
UNUSED
FROM SHEET 1
TO GPIB CONTROLLER

Figure 6-5. Digital Subsystem Interconnection Diagram (Sheet 2)

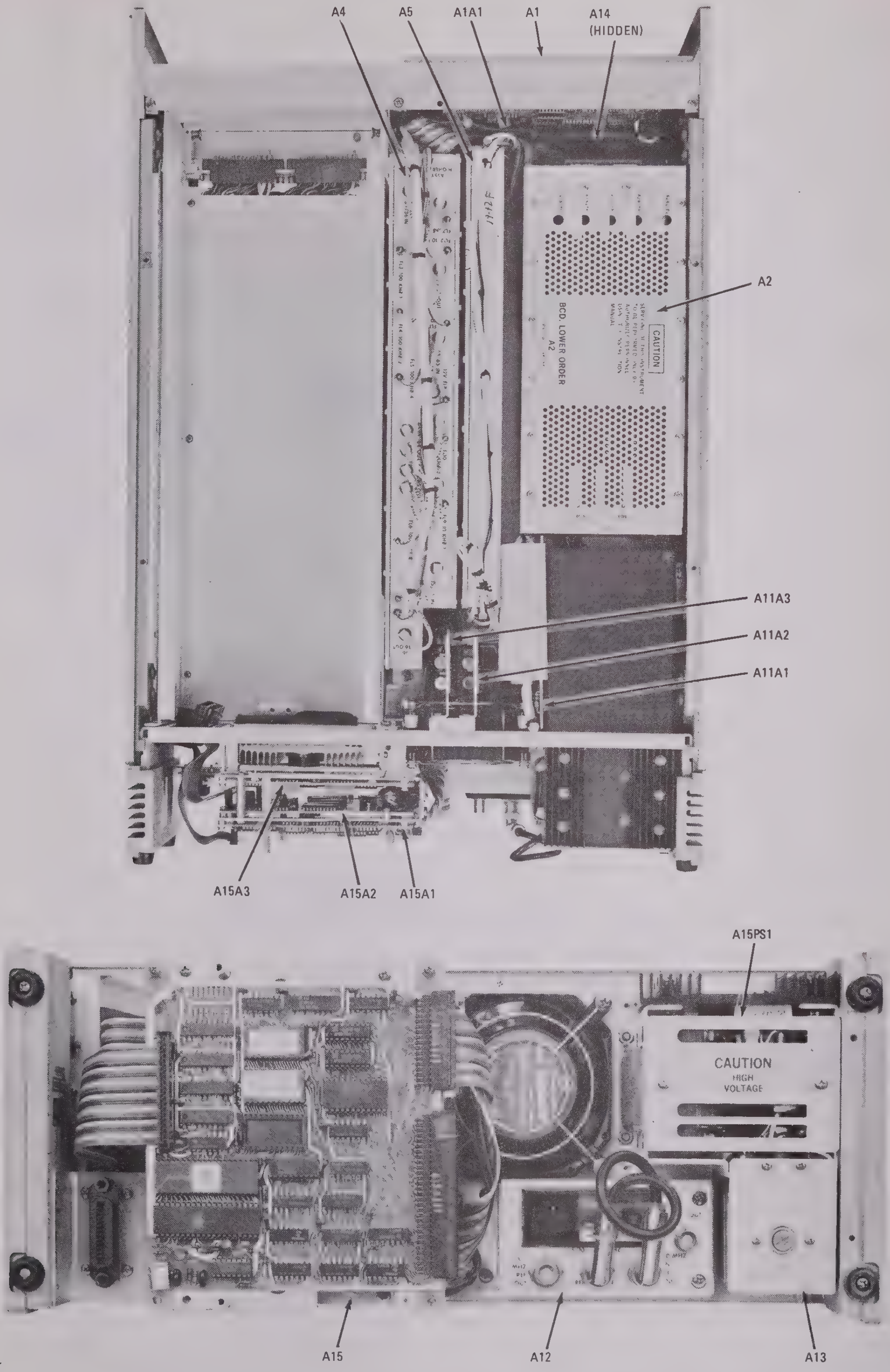
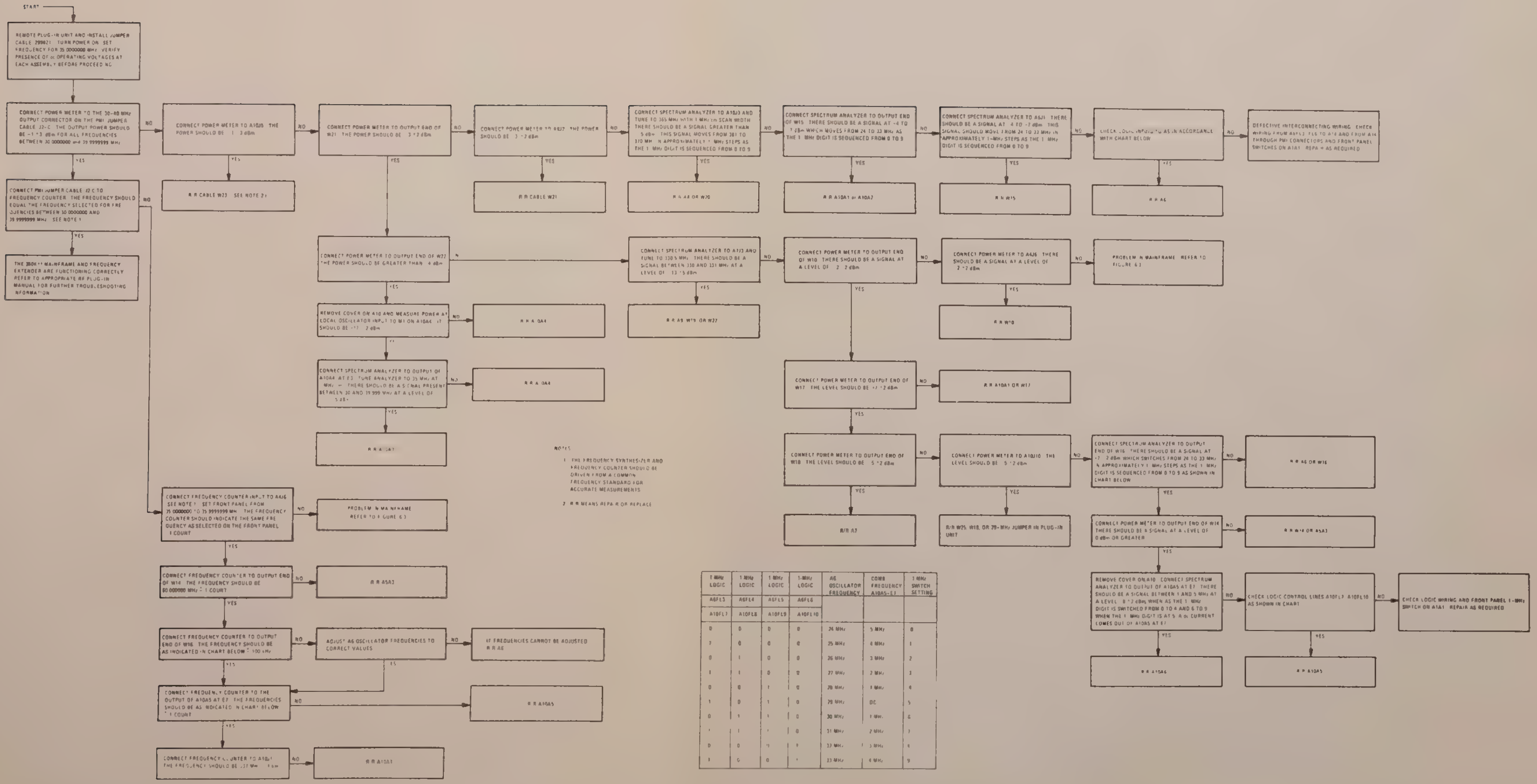

A4
A5
A1A1
A1
A14 (HIDDEN)
A2
CAUTION
BCD LOWER ORDER A2
A11A3
A11A2
A11A1
A15A3
A15A2
A15A1
A15PS1
CAUTION HIGH VOLTAGE
A15
A12
A13

Figure 6-6. Frequency Synthesizer, Mainframe, Subassembly Locations


START
REMOTE PLUG-IN UNIT AND INSTALL JUMPER CABLE 299021. TURN POWER ON. SET FREQUENCY FOR 35.0000000 MHz. VERIFY PRESENCE OF dc OPERATING VOLTAGES AT EACH ASSEMBLY BEFORE PROCEEDING
CONNECT POWER METER TO THE 30–40 MHz OUTPUT CONNECTOR ON THE PMI JUMPER CABLE J2-C. THE OUTPUT POWER SHOULD BE −1 ±3 dBm FOR ALL FREQUENCIES BETWEEN 30.0000000 and 39.9999999 MHz
CONNECT PMI JUMPER CABLE J2-C TO FREQUENCY COUNTER. THE FREQUENCY SHOULD EQUAL THE FREQUENCY SELECTED FOR FREQUENCIES BETWEEN 30.0000000 AND 39.9999999 MHz. SEE NOTE 1
THE 380K MAINFRAME AND FREQUENCY EXTENDER ARE FUNCTIONING CORRECTLY. REFER TO APPROPRIATE RF PLUG-IN MANUAL FOR FURTHER TROUBLESHOOTING INFORMATION
CONNECT POWER METER TO A10J5. THE POWER SHOULD BE 1 3 dBm
R/R CABLE W23. SEE NOTE 2
CONNECT POWER METER TO OUTPUT END OF W21. THE POWER SHOULD BE 3 ±2 dBm
CONNECT POWER METER TO A8J7. THE POWER SHOULD BE 3 ±2 dBm
R/R CABLE W21
CONNECT SPECTRUM ANALYZER TO A10J3 AND TUNE TO 365 MHz WITH 1 MHz/cm SCAN WIDTH. THERE SHOULD BE A SIGNAL GREATER THAN 5 dBm. THIS SIGNAL MOVES FROM 361 TO 370 MHz IN APPROXIMATELY 1 MHz STEPS AS THE 1 MHz DIGIT IS SEQUENCED FROM 0 TO 9
R/R A8 OR W20
CONNECT SPECTRUM ANALYZER TO OUTPUT END OF W15. THERE SHOULD BE A SIGNAL AT −4 TO 7 dBm WHICH MOVES FROM 24 TO 33 MHz AS THE 1 MHz DIGIT IS SEQUENCED FROM 0 TO 9
R/R A10A1 or A10A2
CONNECT SPECTRUM ANALYZER TO A6J1. THERE SHOULD BE A SIGNAL AT 4 TO −7 dBm. THIS SIGNAL SHOULD MOVE FROM 24 TO 33 MHz IN APPROXIMATELY 1-MHz STEPS AS THE 1 MHz DIGIT IS SEQUENCED FROM 0 TO 9
R/R W15
CHECK LOGIC INPUTS TO A6 IN ACCORDANCE WITH CHART BELOW
R/R A6
DEFECTIVE INTERCONNECTING WIRING. CHECK WIRING FROM A6FL3–FL6 TO A14 AND FROM A14 THROUGH PMI CONNECTORS AND FRONT PANEL SWITCHES ON A1A1. REPAIR AS REQUIRED
CONNECT POWER METER TO OUTPUT END OF W22. THE POWER SHOULD BE GREATER THAN 4 dBm
REMOVE COVER ON A10 AND MEASURE POWER AT LOCAL OSCILLATOR INPUT TO M1 ON A10A4. IT SHOULD BE −17 2 dBm
R/R A10A4
CONNECT SPECTRUM ANALYZER TO OUTPUT OF A10A4 AT E3. TUNE ANALYZER TO 35 MHz AT MHz. THERE SHOULD BE A SIGNAL PRESENT BETWEEN 30 AND 39.999 MHz AT A LEVEL OF 5 dBm
R/R A10A4
R/R A10A2
CONNECT SPECTRUM ANALYZER TO A7J3 AND TUNE TO 330.5 MHz. THERE SHOULD BE A SIGNAL BETWEEN 330 AND 331 MHz AT A LEVEL OF 13 ±5 dBm
R/R A9, W19 OR W27
CONNECT POWER METER TO OUTPUT END OF W10. THERE SHOULD BE A SIGNAL AT A LEVEL OF 2 2 dBm
CONNECT POWER METER TO A4J6. THERE SHOULD BE A SIGNAL AT A LEVEL OF 2 ±2 dBm
PROBLEM IN MAINFRAME. REFER TO FIGURE 6-3
R/R W10
CONNECT POWER METER TO OUTPUT END OF W17. THE LEVEL SHOULD BE +7 ±2 dBm
R/R A10A1 OR W17
CONNECT POWER METER TO OUTPUT END OF W18. THE LEVEL SHOULD BE 5 ±2 dBm
R/R A7
CONNECT POWER METER TO A10J10. THE LEVEL SHOULD BE 5 ±2 dBm
R/R W25, W18, OR 29-MHz JUMPER IN PLUG-IN UNIT
CONNECT SPECTRUM ANALYZER TO OUTPUT END OF W16. THERE SHOULD BE A SIGNAL AT +7 2 dBm WHICH SWITCHES FROM 24 TO 33 MHz IN APPROXIMATELY 1 MHz STEPS AS THE 1 MHz DIGIT IS SEQUENCED FROM 0 TO 9 AS SHOWN IN CHART BELOW
R/R A6 OR W16
CONNECT POWER METER TO OUTPUT END OF W14. THERE SHOULD BE A SIGNAL AT A LEVEL OF 0 dBm OR GREATER
R/R W14 OR A5A3
REMOVE COVER ON A10. CONNECT SPECTRUM ANALYZER TO OUTPUT OF A10A5 AT E7. THERE SHOULD BE A SIGNAL BETWEEN 1 AND 5 MHz AT A LEVEL 0 ±2 dBm WHEN AS THE 1 MHz DIGIT IS SWITCHED FROM 0 TO 4 AND 6 TO 9. WHEN THE 1 MHz DIGIT IS AT 5 A dc CURRENT COMES OUT OF A10A5 AT E7
R/R A10A6
CHECK LOGIC CONTROL LINES A10FL7–A10FL10 AS SHOWN IN CHART
R/R A10A5
CHECK LOGIC WIRING AND FRONT PANEL 1-MHz SWITCH ON A1A1. REPAIR AS REQUIRED
CONNECT FREQUENCY COUNTER INPUT TO A4J6. SEE NOTE 1. SET FRONT PANEL FROM 35.0000000 TO 35.9999999 MHz. THE FREQUENCY COUNTER SHOULD INDICATE THE SAME FREQUENCY AS SELECTED ON THE FRONT PANEL ±1 COUNT
PROBLEM IN MAINFRAME. REFER TO FIGURE 6-3
CONNECT FREQUENCY COUNTER TO OUTPUT END OF W14. THE FREQUENCY SHOULD BE 60.000000 MHz ± 1 COUNT
R/R A5A3
CONNECT FREQUENCY COUNTER TO OUTPUT END OF W16. THE FREQUENCY SHOULD BE AS INDICATED IN CHART BELOW ± 100 kHz
ADJUST A6 OSCILLATOR FREQUENCIES TO CORRECT VALUES
IF FREQUENCIES CANNOT BE ADJUSTED R/R A6
CONNECT FREQUENCY COUNTER TO THE OUTPUT OF A10A5 AT E7. THE FREQUENCIES SHOULD BE AS INDICATED IN CHART BELOW ±1 COUNT
R/R A10A5
CONNECT FREQUENCY COUNTER TO A10J1. THE FREQUENCY SHOULD BE 331 MHz
R/R A10A1
YES
NO

1 MHz LOGIC	1 MHz LOGIC	1 MHz LOGIC	1 MHz LOGIC	A6 OSCILLATOR FREQUENCY	COMB FREQUENCY A10A5-E7	1 MHz SWITCH SETTING
A6FL3	A6FL4	A6FL5	A6FL6			
A10FL7	A10FL8	A10FL9	A10FL10			
0	0	0	0	24 MHz	5 MHz	0
1	0	0	0	25 MHz	4 MHz	1
0	1	0	0	26 MHz	3 MHz	2
1	1	0	0	27 MHz	2 MHz	3
0	0	1	0	28 MHz	1 MHz	4
1	0	1	0	29 MHz	DC	5
0	1	1	0	30 MHz	1 MHz	6
1	1	1	0	31 MHz	2 MHz	7
0	0	0	1	32 MHz	3 MHz	8
1	0	0	1	33 MHz	4 MHz	9

NOTES

1. THE FREQUENCY SYNTHESIZER AND FREQUENCY COUNTER SHOULD BE DRIVEN FROM A COMMON FREQUENCY STANDARD FOR ACCURATE MEASUREMENTS

2. R/R MEANS REPAIR OR REPLACE

Figure 6-7. Frequency Extender, Troubleshooting Chart

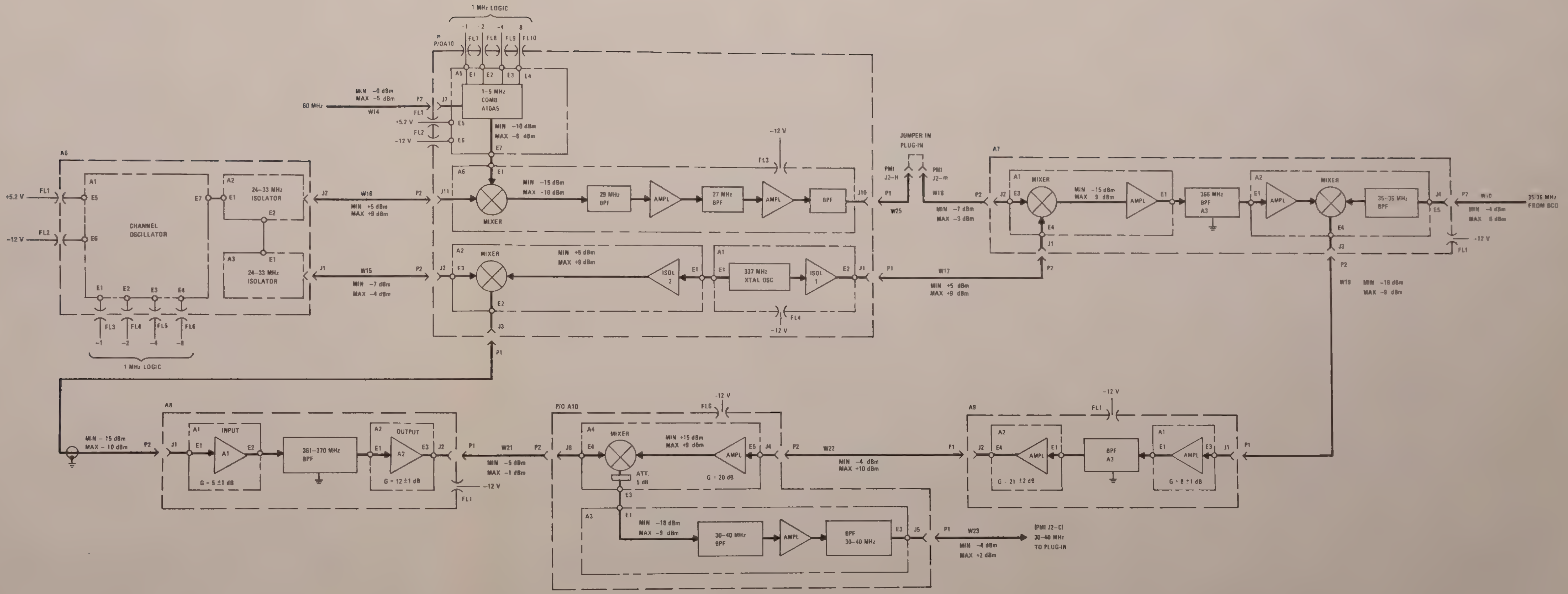

1 MHz LOGIC
-1
-2
-4
8
FL7
FL8
FL9
FL10
P/OA10
A5
E1
E2
E3
E4
1-5 MHz
COMB
A10A5
60 MHz
MIN -0 dBm
MAX -5 dBm
W14
P2
J7
FL1
+5.2 V
E5
FL2
-12 V
E6
MIN -10 dBm
MAX -6 dBm
E7
A6
A1
+5.2 V
FL1
E5
-12 V
FL2
E6
CHANNEL
OSCILLATOR
E7
A2
E1
24-33 MHz
ISOLATOR
E2
A3
E1
24-33 MHz
ISOLATOR
E1
E2
E3
E4
FL3
FL4
FL5
FL6
-1
-2
-4
-8
1 MHz LOGIC
J2
W16
P2
MIN +5 dBm
MAX +9 dBm
J1
W15
P2
MIN -7 dBm
MAX -4 dBm
J11
A6
E1
MIXER
MIN -15 dBm
MAX -10 dBm
29 MHz
BPF
AMPL
27 MHz
BPF
AMPL
BPF
J10
-12 V
FL3
A2
MIXER
J2
E3
E2
J3
P1
MIN +5 dBm
MAX +9 dBm
ISOL
2
E1
A1
E1
337 MHz
XTAL OSC
ISOL
1
E2
J1
FL4
-12 V
JUMPER IN
PLUG-IN
PMI
J2-H
PMI
J2-m
P1
W25
W18
MIN -7 dBm
MAX -3 dBm
P2
P1
W17
MIN +5 dBm
MAX +9 dBm
P2
A7
A1
MIXER
J2
E3
MIN -15 dBm
MAX 9 dBm
AMPL
E1
E4
J1
366 MHz
BPF
A3
A2
E1
AMPL
MIXER
35-36 MHz
BPF
E4
J3
J4
E5
P2
W20
35/36 MHz
FROM BCD
MIN -4 dBm
MAX 0 dBm
-12 V
FL1
P2
W19
MIN -18 dBm
MAX -9 dBm
MIN - 15 dBm
MAX - 10 dBm
P2
A8
J1
A1
INPUT
E1
A1
E2
G = 5 ±1 dB
361-370 MHz
BPF
A2
OUTPUT
E1
A2
E3
G = 12 ±1 dB
J2
-12 V
FL1
P1
W21
P2
MIN -5 dBm
MAX -1 dBm
P/O A10
-12 V
FL6
J6
A4
E4
MIXER
MIN +15 dBm
MAX +9 dBm
AMPL
E5
J4
G = 20 dB
ATT.
5 dB
E3
A3
E1
MIN -18 dBm
MAX -9 dBm
30-40 MHz
BPF
AMPL
BPF
30-40 MHz
E3
J5
P2
W22
P1
MIN -4 dBm
MAX +10 dBm
P1
W23
MIN -4 dBm
MAX +2 dBm
(PMI J2-C)
30-40 MHz
TO PLUG-IN
A9
-12 V
FL1
A2
J2
E4
AMPL
E1
G - 21 ±2 dB
BPF
A3
A1
E1
AMPL
E3
J1
G = 8 ±1 dB
P1

Figure 6-8. Frequency Extender, Servicing Block Diagram

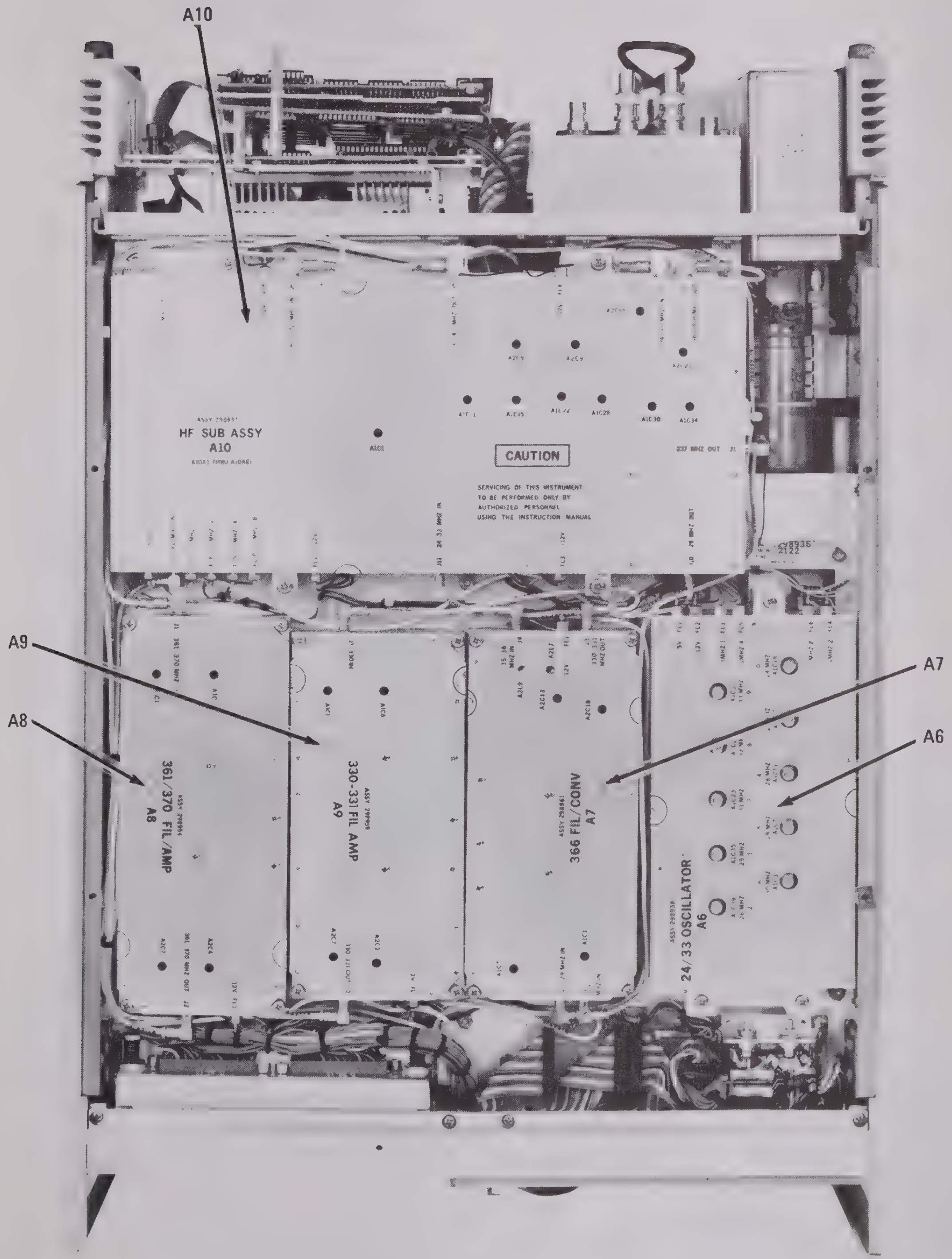
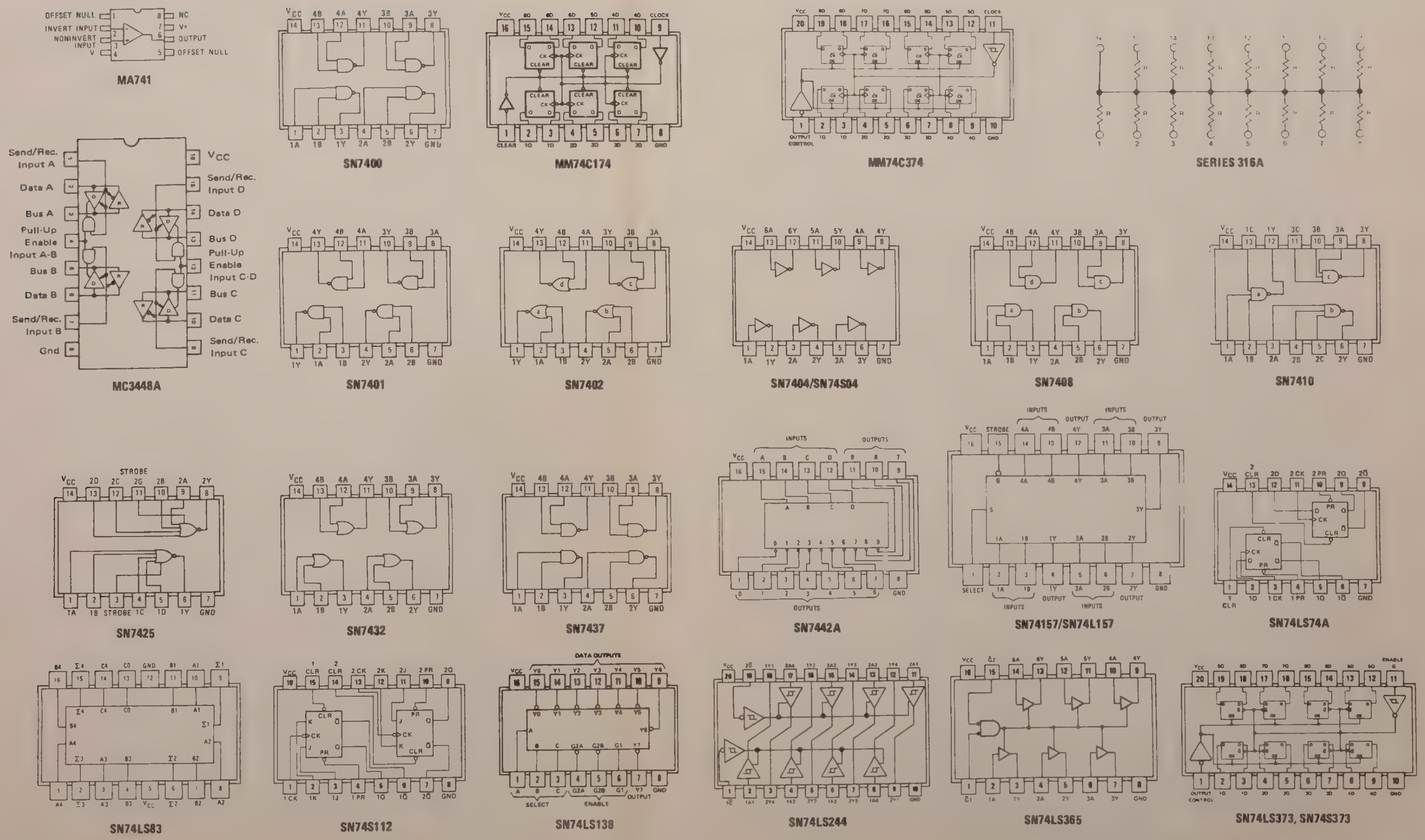

A10
A9
A8
A7
A6
HF SUB ASSY
A10
CAUTION
SERVICING OF THIS INSTRUMENT TO BE PERFORMED ONLY BY AUTHORIZED PERSONNEL USING THE INSTRUCTION MANUAL
337 MHZ OUT
361/370 FIL/AMP
A8
330-331 FIL AMP
A9
366 FIL/CONV
A7
24/33 OSCILLATOR
A6

Figure 6-9. Frequency Extender, Subassembly Locations



MA741 SN7400 MM74C174 MM74C374 SERIES 316A

MC3448A SN7401 SN7402 SN7404/SN74S04 SN7408 SN7410

SN7425 SN7432 SN7437 SN7442A SN74157/SN74L157 SN74LS74A

SN74LS83 SN74S112 SN74LS138 SN74LS244 SN74LS365 SN74LS373, SN74S373

OFFSET NULL
INVERT INPUT
NONINVERT INPUT
V-
NC
V+
OUTPUT
OFFSET NULL
Send/Rec. Input A
Data A
Bus A
Pull-Up Enable Input A-B
Bus B
Data B
Send/Rec. Input B
Gnd
VCC
Send/Rec. Input D
Data D
Bus D
Pull-Up Enable Input C-D
Bus C
Data C
Send/Rec. Input C
STROBE
INPUTS
OUTPUTS
OUTPUT
SELECT
DATA OUTPUTS
ENABLE
OUTPUT CONTROL
CLEAR
CLOCK
GND

Figure 6-10. Integrated Circuits

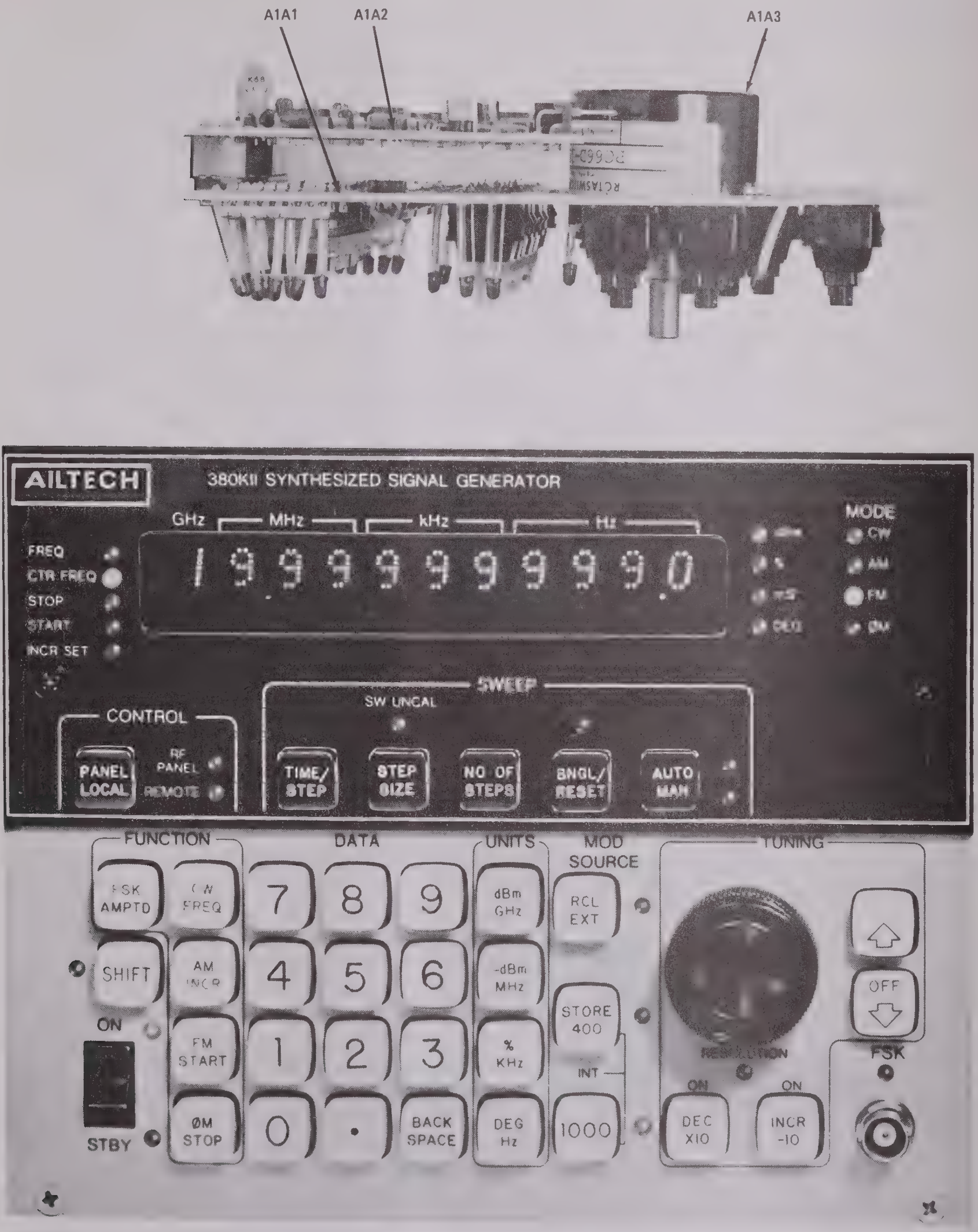

A1A1
A1A2
A1A3
AILTECH
380KII SYNTHESIZED SIGNAL GENERATOR
GHz
MHz
kHz
Hz
FREQ
CTR FREQ
STOP
START
INCR SET
MODE
CW
AM
FM
ØM
SWEEP
SW UNCAL
CONTROL
PANEL LOCAL
RF PANEL
REMOTE
TIME/ STEP
STEP SIZE
NO OF STEPS
BNGL/ RESET
AUTO MAN
FUNCTION
DATA
UNITS
MOD SOURCE
TUNING
FSK AMPTD
CW FREQ
SHIFT
AM INCR
FM START
ØM STOP
ON
STBY
7
8
9
4
5
6
1
2
3
0
.
BACK SPACE
dBm GHz
-dBm MHz
% KHz
DEG Hz
RCL EXT
STORE 400
INT
1000
RESOLUTION
DEC X10
INCR -10
OFF
FSK

Figure 6-11. Front Panel Assembly A1

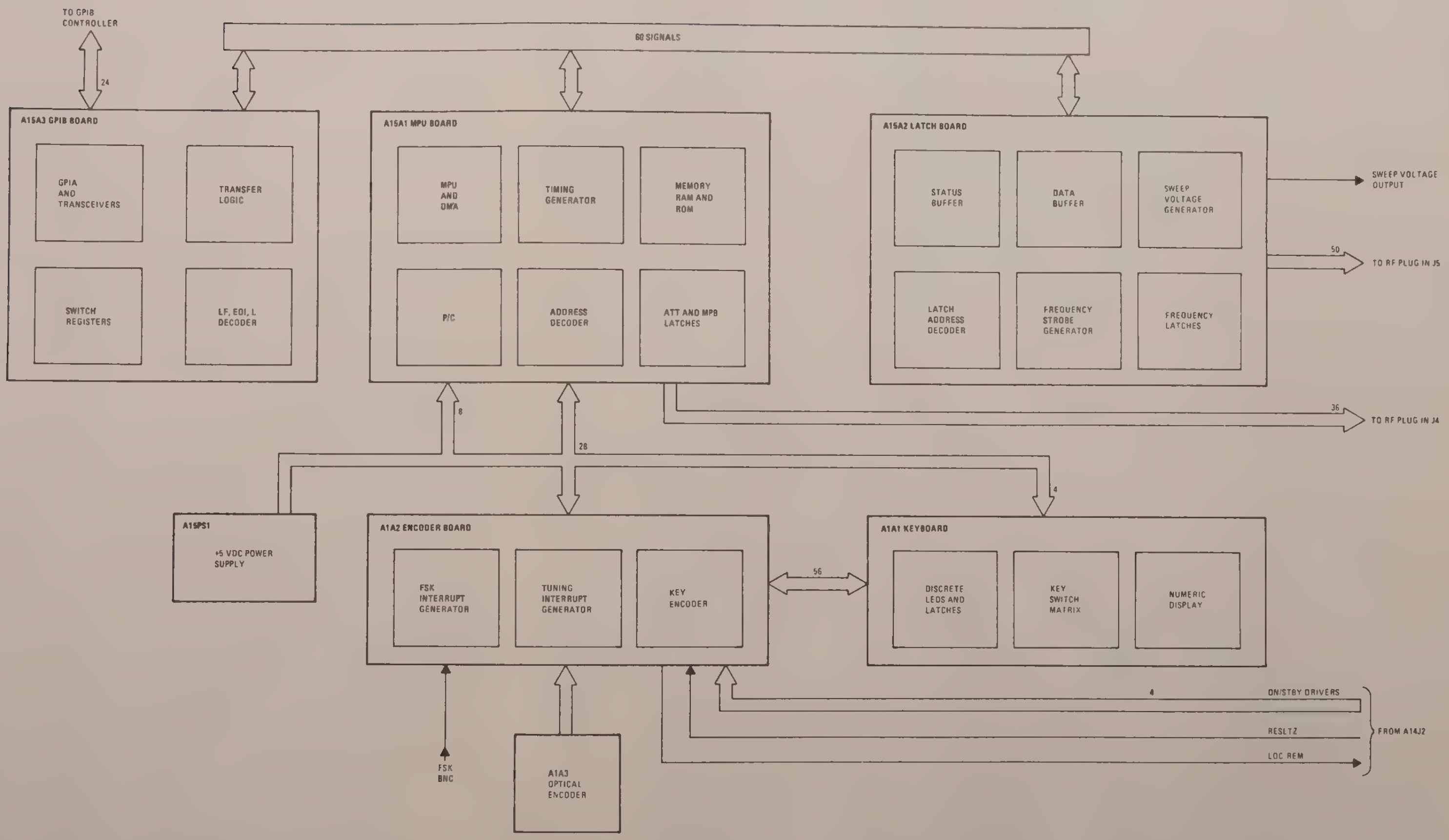

TO GPIB
CONTROLLER
24
60 SIGNALS
A15A3 GPIB BOARD
GPIA
AND
TRANSCEIVERS
TRANSFER
LOGIC
SWITCH
REGISTERS
LF, EOI, L
DECODER
A15A1 MPU BOARD
MPU
AND
DMA
TIMING
GENERATOR
MEMORY
RAM AND
ROM
P/C
ADDRESS
DECODER
ATT AND MPB
LATCHES
A15A2 LATCH BOARD
STATUS
BUFFER
DATA
BUFFER
SWEEP
VOLTAGE
GENERATOR
LATCH
ADDRESS
DECODER
FREQUENCY
STROBE
GENERATOR
FREQUENCY
LATCHES
SWEEP VOLTAGE
OUTPUT
50
TO RF PLUG IN J5
36
TO RF PLUG IN J4
8
28
4
A15PS1
+5 VDC POWER
SUPPLY
A1A2 ENCODER BOARD
FSK
INTERRUPT
GENERATOR
TUNING
INTERRUPT
GENERATOR
KEY
ENCODER
56
A1A1 KEYBOARD
DISCRETE
LEDS AND
LATCHES
KEY
SWITCH
MATRIX
NUMERIC
DISPLAY
4
ON/STBY DRIVERS
RESLTZ
LOC REM
FROM A14J2
FSK
BNC
A1A3
OPTICAL
ENCODER

Figure 6-12. Keyboard Controller Subsystem, Block Diagram

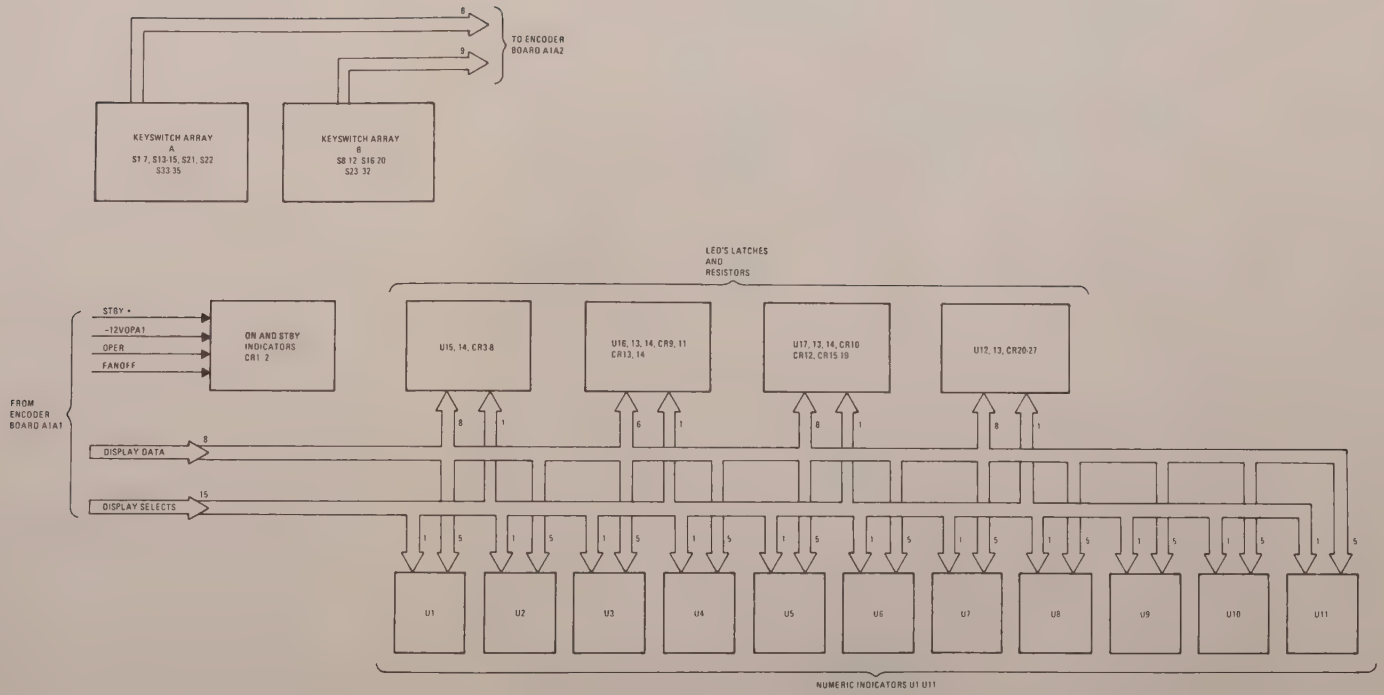
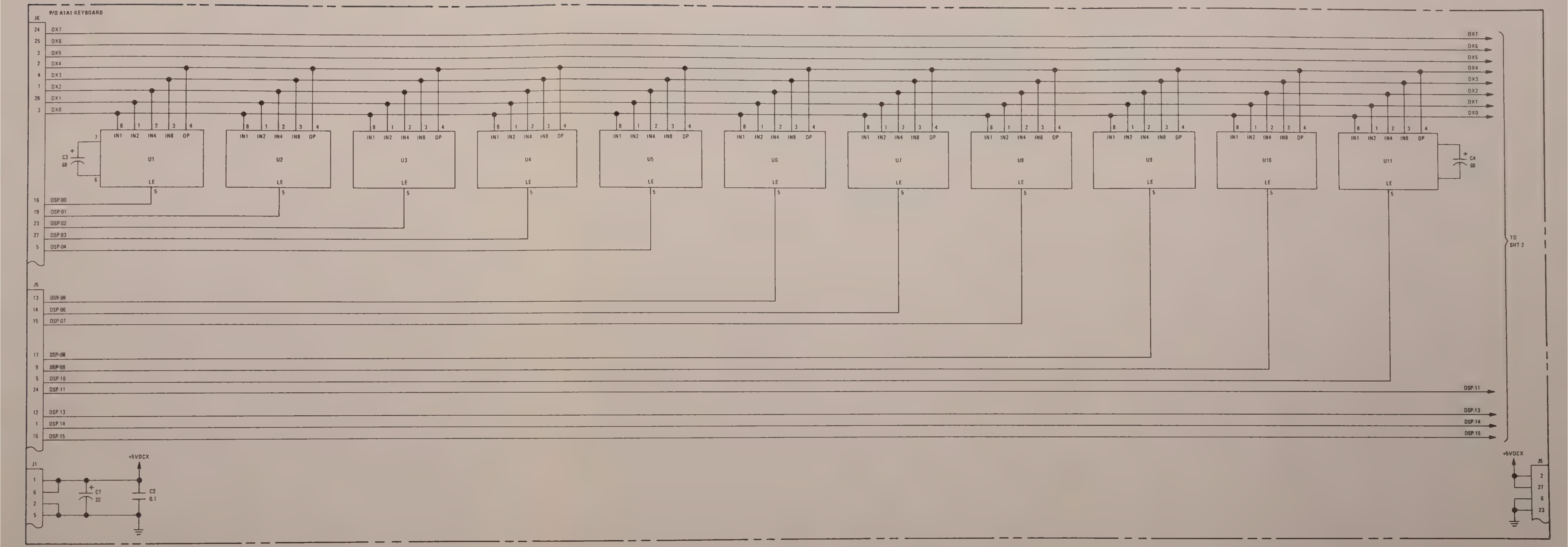

8
9
TO ENCODER
BOARD A1A2
KEYSWITCH ARRAY
A
S1 7, S13-15, S21, S22
S33 35
KEYSWITCH ARRAY
B
S8 12 S16 20
S23 32
LED'S LATCHES
AND
RESISTORS
STBY +
-12VOPA1
OPER
FANOFF
ON AND STBY
INDICATORS
CR1 2
U15, 14, CR3-8
U16, 13, 14, CR9, 11
CR13, 14
U17, 13, 14, CR10
CR12, CR15 19
U12, 13, CR20-27
FROM
ENCODER
BOARD A1A1
8
DISPLAY DATA
15
DISPLAY SELECTS
8
1
6
1
8
1
8
1
1
5
U1
U2
U3
U4
U5
U6
U7
U8
U9
U10
U11
NUMERIC INDICATORS U1 U11

Figure 6-13. Keyboard A1A1, Block Diagram


P/O A1A1 KEYBOARD
J6
24 DX7
25 DX6
3 DX5
2 DX4
4 DX3
1 DX2
28 DX1
3 DX0
16 DSP 00
19 DSP 01
23 DSP 02
27 DSP 03
5 DSP 04
J5
13
14 DSP 06
15 DSP 07
17
9
5 DSP 10
24 DSP 11
12 DSP 13
1 DSP 14
16 DSP 15
8
1
2
3
4
IN1
IN2
IN4
IN8
DP
LE
5
7
6
C3
68
C4
68
U1
U2
U3
U4
U5
U6
U7
U8
U9
U10
U11
DX7
DX6
DX5
DX4
DX3
DX2
DX1
DX0
DSP 11
DSP 13
DSP 14
DSP 15
TO
SHT 2
J1
1
6
2
5
+5VDCX
C1
22
C2
0.1
+5VDCX
J5
2
27
6
23

NOTES

1. UNLESS OTHERWISE SPECIFIED CAPACITOR VALUES ARE IN MICROFARADS.
2. PLACE ONE 0 1 MICROFARAD CAPACITOR NEAR EACH IC BETWEEN 5V AND GND C5 · C19

Figure 6-15. Keyboard A1A1, Schematic Diagram (Sheet 1)

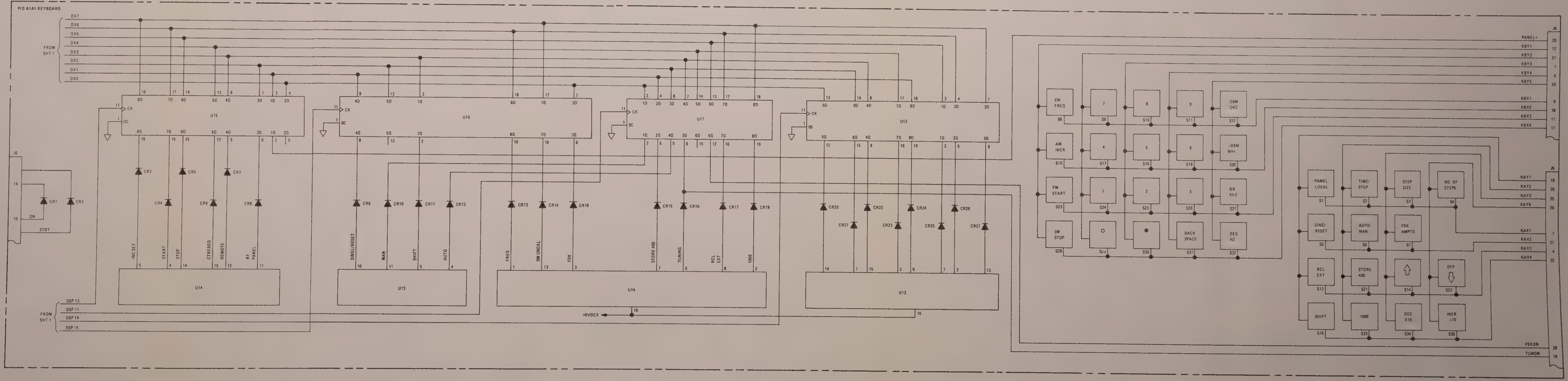

P/O A1A1 KEYBOARD
FROM SHT 1
DX7
DX6
DX5
DX4
DX3
DX2
DX1
DX0
U15
U16
U17
U12
CK
OC
J6
ON
STBY
CR1
CR2
CR3
CR4
CR5
CR6
CR7
CR8
CR9
CR10
CR11
CR12
CR13
CR14
CR15
CR16
CR17
CR18
CR19
CR20
CR21
CR22
CR23
CR24
CR25
CR26
CR27
INC SET
START
STOP
CTRFREQ
REMOTE
RF PANEL
SINGL/RESET
MAN
SHIFT
AUTO
FREQ
SW UNCAL
FSK
STORE 400
TUNING
RCL EXT
1000
U14
U13
+5VDCX
DSP 13
DSP 11
DSP 14
DSP 15
PANEL+
KBY1
KBY2
KBY3
KBY4
KBY5
KBX1
KBX2
KBX3
KBX4
CW FREQ
S8
7
S9
8
S10
9
S11
DBM GHZ
S12
AM INCR
S16
4
S17
5
S18
6
S19
-DBM MHz
S20
FM START
S23
1
S24
2
S25
3
S26
0/8 KHZ
S27
OM STOP
S28
○
S29
●
S30
BACK SPACE
S31
DEG HZ
S32
J5
PANEL LOCAL
S1
TIME/ STEP
S2
STEP SIZE
S3
NO OF STEPS
S4
SING/ RESET
S5
AUTO MAN
S6
FSK AMPTD
S7
RCL EXT
S13
STORE 400
S21
⇧
S14
OFF ⇩
S22
SHIFT
S15
1000
S33
DEC X10
S34
INCR ÷10
S35
KAY1
KAY2
KAY3
KAY4
KAX1
KAX2
KAX3
KAX4
FSKON
TUNON

Figure 6-15. Keyboard A1A1, Schematic Diagram (Sheet 2)

6-35. The latch outputs also generate three signals used external to this board. They are the FSKON-TUNON- and PANEL+ signals. The FSKON- and TUNON- signals are used to enable the FSK and TUNE interrupt generators on the encoder board. The PANEL+ signal is used to generate the LOC/REM signal on the encoder board.

6-36. The ON and STANDBY LEDs are not driven by the latch outputs. These LEDs are driven by external signals. The cathode of the ON LED is driven by the -12VOPA1 signal and its anode is driven by the OPER signal. The cathode of the STANDBY LED is driven by the FANOFF signal and its anode is driven by STDBY+.

6-37. **ENCODER BOARD ASSEMBLY A1A2.** (Figures 6-16 through 6-18). The Encoder board contains the following major component blocks:

a. Key Encoders
b. Keyboard Interrupt Generator
c. Tuning Interrupt Generator
d. FSK/EXT Interrupt Generator
e. Reset Control
f. Display Address Decoders
g. Display Data Buffer
h. Resolution Control
i. LOC/REM Generator

6-38. **Key Encoders (U3, U5).** These CMOS20 key encoders provide the necessary logic to fully encode the two arrays of SPST switches located on the keyboard. The internal debounce circuit, with the external 1.0μf capacitor allows a switch debounce period of 10 ms. The scan rate of approximately 60 Hz is accomplished with an external 1.0μf capacitor.

6-39. The data available output (DAV) goes to a high level when a valid keyboard entry has been made. The DAV output returns to a low level when the entered key is released, even if another key is depressed. The DAV will return high to indicate acceptance of the new key after the debounce period. This two key roll-over is provided between any two switches.

6-40. An internal register remembers the last key pressed, even after the key is released. The tri-state data outputs allow for the outputs of the two encoders to be OR-tied together. These outputs are enabled by driving the OE (output enable) low.

6-41. **Keyboard Interrupt Generator (U2, U7).** The function of the keyboard interrupt generator is to generate the KBDIRQ signal whenever a key is pressed. This circuit maintains the two key roll-over effect between the two encoders. This circuit also generates a data bit to indicate which encoder is sending data. The circuit functions as follows:

6-42. When a key is pressed in array A, the DAV signal (U3-13) goes high after the debounce period plus the scan time (30 ms maximum). Since U7-8 is high at this time, U7-6 goes low. This does two things. First it forces U7-11 to go high and U7-3 to go low. The output enable of U3-14 is activated which enables the data outputs of U3 with the 5 bit code of the key pressed. The sixth bit of the key-code is generated by by U7-11 and is used to identify which encoder is active. It will be high when encoder A is enabled and low for encoder B.

6-43. The second result of U7-6 going low, is to cause U2-9 Q to go low. This causes the KBDIRQ signal U6-10 to go active low. The MPU will respond to the KBDIRQ by setting the RDKBD- (read keyboard) signal U10-1 low. This will enable the keyboard data buffer U10. The MPU will then read the six bit key-code generated. After the code is read, the KBDIRQ will be reset by the MPU. This is done by activating the RSKBD- (reset keyboard interrupt) signal. When RSKBD- goes low, both sections of the U2 flip-flop are preset, causing KBDIRQ to go high.

6-44. If a key was pressed in array B while the key in array A is still held down, a lockout effect takes place which prevents U7-8 from going low and causing an interrupt. When U3-13 goes low as a result of releasing the first key, lockout is removed from encoder B, allowing U7-8 to go low, and thereby enabling the U5 outputs, setting the identifier bit U7-11 low, and activating the KBDIRQ.

6-45. **Tuning Interrupt Generator.** The function of the tuning interrupt generator is to generate the two directional interrupts CWXIRQ and CCWIRQ. CWXIRQ is generated for clockwise rotation and CCWIRQ is generated for counterclockwise rotation of the tuning control. When the tuning control is rotated, the optical encoder will generate the two outputs, OPAOUT and OPBOUT. These signals are referred to as quasisquare waves and have the phase relation shown in Figure 6-18.

6-46. The Q1 and Q2 transistors and the resistors R5 through R10 are used to increase the current drive of the encoder signals. The R5, R6 and R10, R9 resistor combinations are used to bias the base of Q1 and Q2. The transistors will switch when the inputs (OPAOUT and OPBOUT) cross at about 2.2 volts. The result is a square wave being generated at the collector of each transistor, with the same phase relation as the original signals.

6-47. During clockwise rotation, the rising edge of the A signal clocks a low to the CWXIRQ U13-5 output, and the rising edge of the B signal clocks a high to the CCWIRQ U13-9 output. This generates the clockwise interrupt signal. The MPU will process this interrupt and then activate the RSTUN- (reset tune) signal to reset the interrupt.

6-48. The counterclockwise interrupt is generated in a similar manner. The rising edge of the A signal would clock a high to CWXIRQ and the rising edge of B would clock a low to CCWIRQ, thereby generating the counterclockwise interrupt.

6-49. **FSK/EXT Interrupt Generator.** The function of this circuit is to generate two interrupts, FSLIRQ and FSHIRQ. The FSLIRQ will be generated on the falling edge of the FSK-IN signal and the

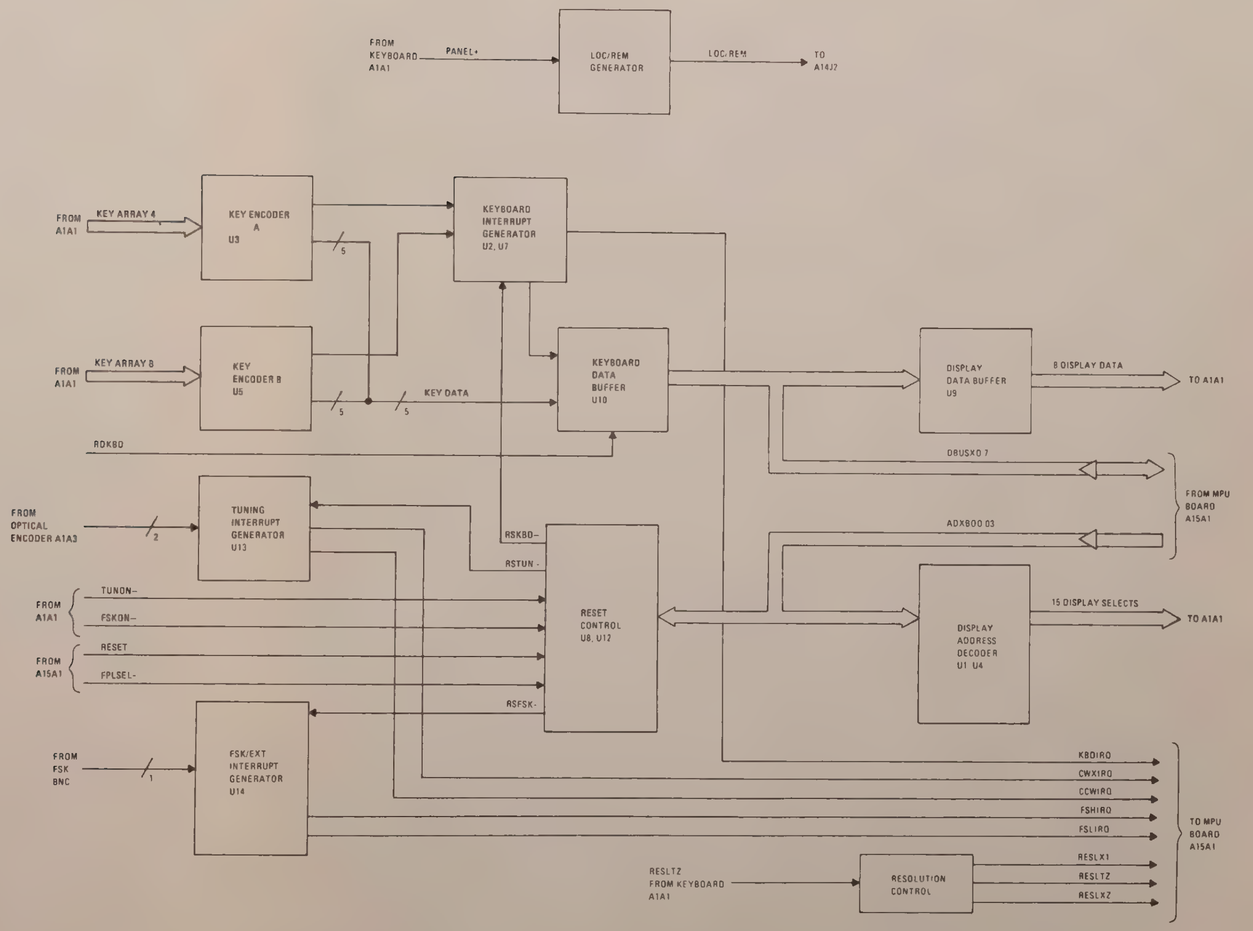

FROM KEYBOARD A1A1
PANEL+
LOC/REM GENERATOR
LOC/REM
TO A14J7
FROM A1A1
KEY ARRAY 4
KEY ENCODER A U3
5
KEYBOARD INTERRUPT GENERATOR U2, U7
FROM A1A1
KEY ARRAY B
KEY ENCODER B U5
5
5
KEY DATA
KEYBOARD DATA BUFFER U10
RDKBD
DISPLAY DATA BUFFER U9
8 DISPLAY DATA
TO A1A1
DBUSX0 7
FROM MPU BOARD A15A1
ADXB00 03
FROM OPTICAL ENCODER A1A3
2
TUNING INTERRUPT GENERATOR U13
RSKBD–
RSTUN -
FROM A1A1
TUNON–
FSKON–
FROM A15A1
RESET
FPLSEL-
RESET CONTROL U8, U12
DISPLAY ADDRESS DECODER U1 U4
15 DISPLAY SELECTS
TO A1A1
RSFSK-
FROM FSK BNC
1
FSK/EXT INTERRUPT GENERATOR U14
KBDIRQ
CWXIRQ
CCWIRQ
FSHIRQ
FSLIRQ
TO MPU BOARD A15A1
RESLTZ FROM KEYBOARD A1A1
RESOLUTION CONTROL
RESLX1
RESLTZ
RESLXZ

Figure 6-16. Encoder A1A2, Block Diagram

FSHIRQ will be generated on the rising edge of the FSK-IN signal. The R4, 10k resistor is used to pull-up the input when either no generator is connected or when an open collector type of driver is employed to generate the FSK-IN signal. This input is accessed at the front-panel BNC connector marked FSK. Diode CR1 will protect the input U11-9 from overshoots above 5.7 volts. The CR2 diode will protect this input from undershoots below 0.7 volts. Inverter U11 provides 0.4 volts of hysterisis to provide greater noise immunity to the FSK-IN signal.

6-50. When FSK-IN goes from high to low, U11-8 goes low to high. This will clock a low to FSLIRQ (U14-5). This is the low FSK transition interrupt to the MPU. The MPU will process this interrupt and then activate the RSFSK- (reset FSK). This will reset the interrupt.

6-51. When FSK-IN returns to the high state, the FSHIRQ signal U14-9 will go low. This is the high FSK transition interrupt and will be processed and reset similarly to FSLIRQ.

6-52. **Reset Control.** The function of the reset control circuit is to create the three interrupt reset signals RSKBD-, RSTUN-, and RSFSK-. When the system is powered-up, the RESET- signal will pulse low and force all three reset signals to go active low thereby resetting all three functions.

6-53. When the tuning control is disabled, the TUNON- signal will be high and RSTUN- U12-6 will be low. This will disable the tuning interrupts. The tuning LED will be out at this time. When the tuning control is enabled, the LED will go on and the TUNON- signal will go low. This will allow RSTUN- to go high because U12-3,4 and 5 are now high. When the MPU receives a tuning interrupt it will reset this interrupt by pulsing FPSEL- (front-panel select) low and ADXB01 high. This will set U8-11 low and U12-6 (RSTUN-) low, thereby resetting the tuning interrupts.

6-54. The FSK reset (RSFSK-) signal is generated in a similar manner. When FSKON- is high, the RSFSK- is forced low disabling the FSK interrupts. When FSKON- is low, the RSFSK is high because U12-9, 10 and 11 are all high. When the MPU desires to reset the FSK interrupts, it will activate FPSEL- low and ADXB02 high, causing U8-6 to go low and U12-8 (RSFSK-) to go low, resetting the FSK interrupt. The keyboard interrupt reset (RSKBD-) has no disabling signal as the other two have. It is, however activated in the same manner as the other two are, when ADXB00 goes high and FPSEL- goes low.

6-55. **Display Address Decoders.** The display address decoders U1 and U4 are the 3 to 8 decoders. They are used to generate the 15 enable strobes for the numeric indicators and the LED latches.

6-56. The AB and C inputs of each decoder is driven by ADXB00, ADXB01 and ADXB02 address lines respectively. ADXB03 drives the active low enable (G2A) of U1 and the active high enable (G1) of U4. Using this arrangement, each hexidecimal code on the four address lines will activate a unique Y output (DSP-00 through DSP-15). These outputs are the enable strobes.

6-57. The selected output will not activate however until the WRTDSP (write display) signal goes low. This signal drives the active low enable (G2B) of each decoder. The selected Y output will remain low until the WRTDSP returns high.

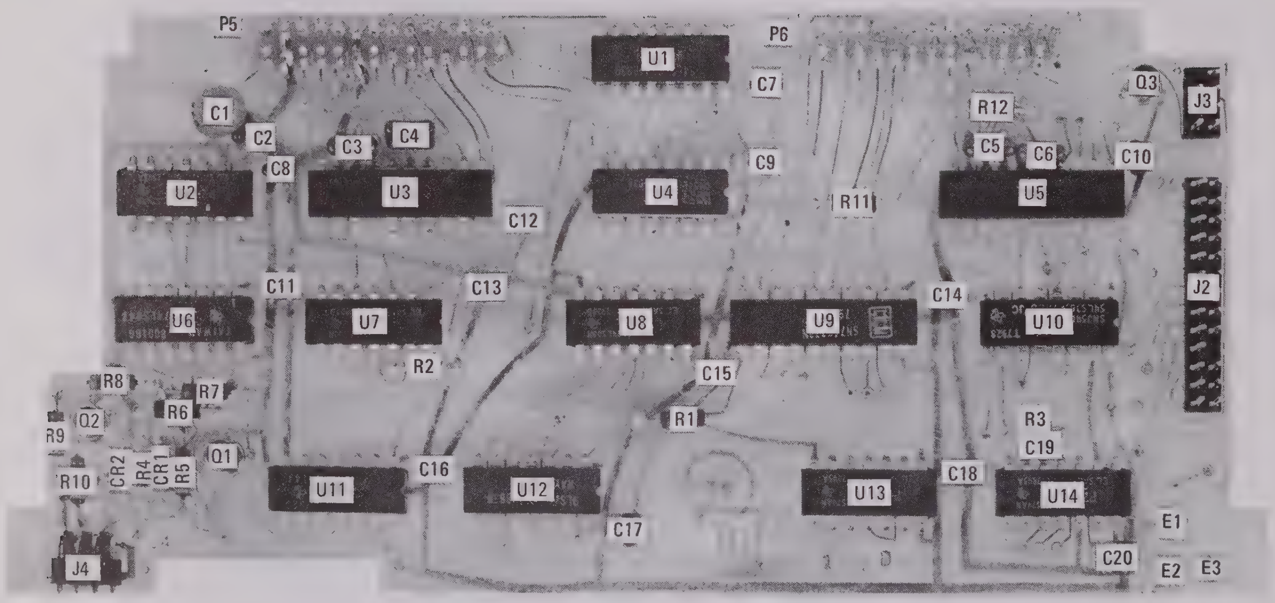
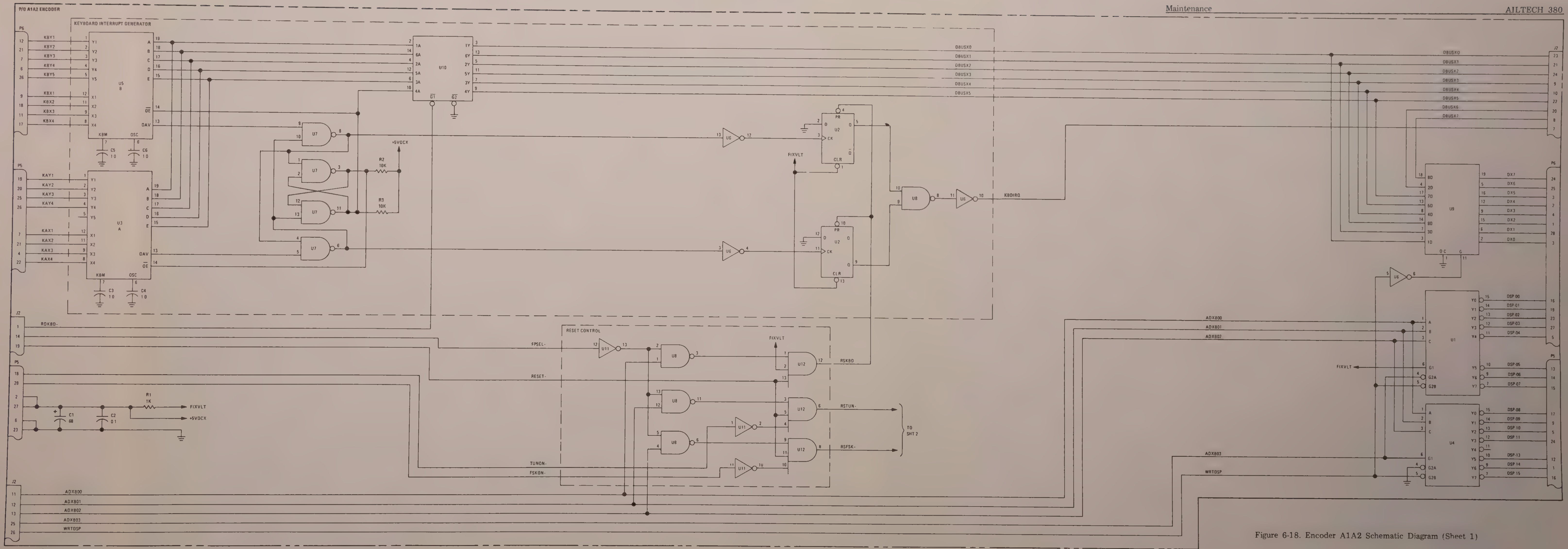

P5
U1
P6
C7
Q3
J3
C1
R12
C2
C4
C3
C5
C6
C10
C8
C9
U2
U3
U4
R11
U5
C12
C11
C13
C14
J2
U6
U7
U8
U9
U10
R2
C15
R8
R7
Q2
R6
R1
R3
R9
Q1
C19
C16
R10
CR2
R4
CR1
R5
U11
U12
U13
C18
U14
E1
C17
C20
J4
E2
E3

Figure 6-17. Encoder A1A2, Component Locations


P/O A1A2 ENCODER
KEYBOARD INTERRUPT GENERATOR
P6
KBY1
KBY2
KBY3
KBY4
KBY5
KBX1
KBX2
KBX3
KBX4
U5
B
KBM
OSC
C5
1 0
C6
1 0
P5
KAY1
KAY2
KAY3
KAY4
KAX1
KAX2
KAX3
KAX4
U3
A
C3
1 0
C4
1 0
DAV
OE
U7
+5VDCX
R2
10K
R3
10K
U10
DBUSX0
DBUSX1
DBUSX2
DBUSX3
DBUSX4
DBUSX5
DBUSX6
DBUSX7
U6
U2
PR
CLR
CK
FIXVLT
U8
KBDIRQ
J2
U9
OC
DX7
DX6
DX5
DX4
DX3
DX2
DX1
DX0
U1
U4
DSP 00
DSP 01
DSP 02
DSP 03
DSP 04
DSP 05
DSP 06
DSP 07
DSP 08
DSP 09
DSP 10
DSP 11
DSP-13
DSP 14
DSP 15
G1
G2A
G2B
ROKBD-
RESET CONTROL
FPSEL-
RESET-
U11
U12
RSKBD
RSTUN-
RSFSK-
TO SHT 2
TUNON-
FSKON-
R1
1K
C1
68
C2
0 1
ADXB00
ADXB01
ADXB02
ADXB03
WRTDSP

NOTES
1. UNLESS OTHERWISE SPECIFIED:
RESISTOR VALUES ARE IN OHMS, ±5% TOLERANCE
CAPACITOR VALUES IN MICROFARADS

2. PLACE ONE 0.1 MICROFARAD CAPACITOR CLOSE TO
EACH I.C. FROM VCC TO GND.

Figure 6-18. Encoder A1A2 Schematic Diagram (Sheet 1)

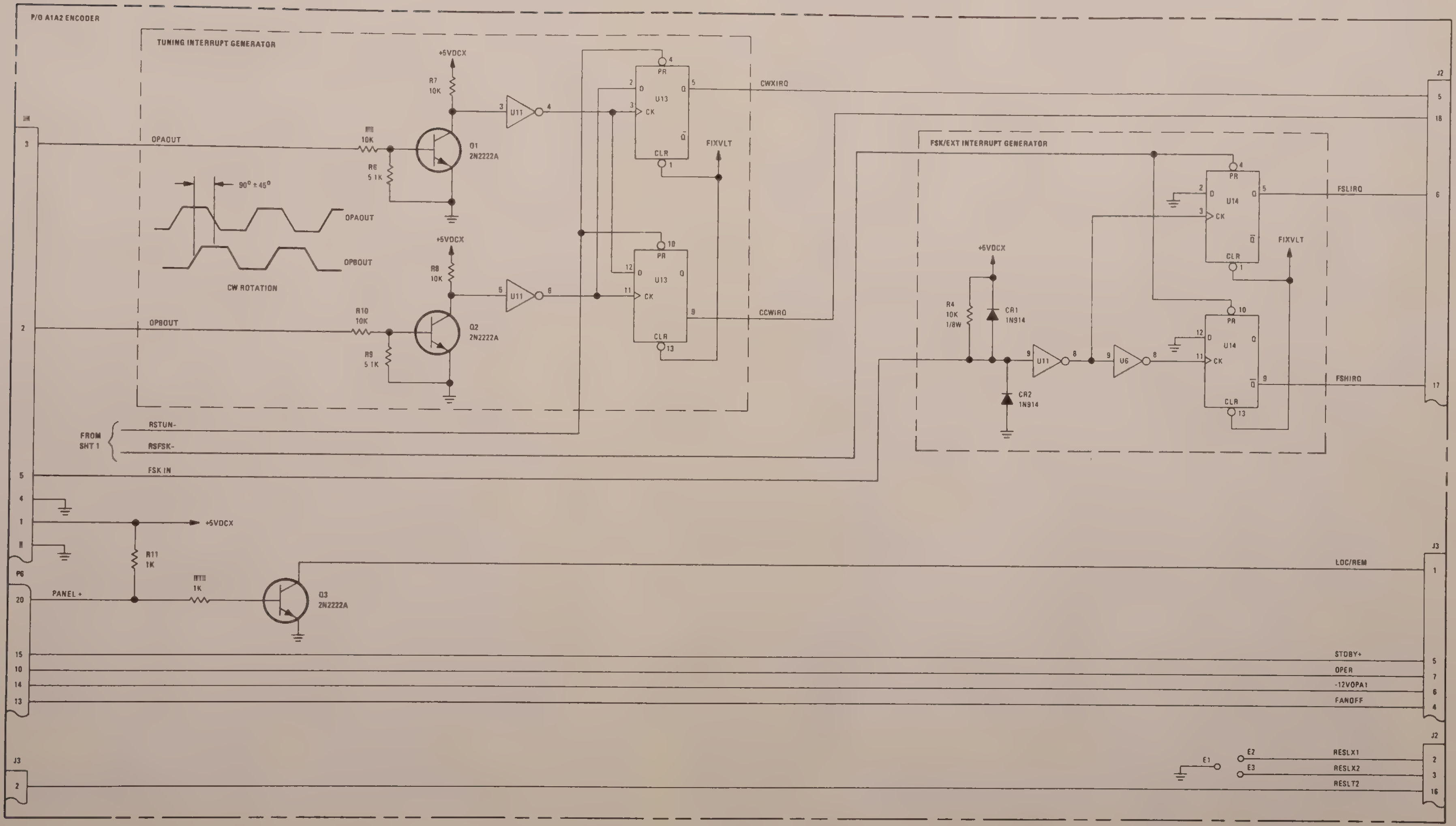

P/O A1A2 ENCODER
TUNING INTERRUPT GENERATOR
+5VDCX
R7
10K
3
U11
4
2
D
PR
4
U13
Q
5
3
CK
Q̄
CLR
1
CWXIRQ
FIXVLT
3
OPAOUT
10K
Q1
2N2222A
R6
5.1K
90° ± 45°
OPAOUT
OPBOUT
CW ROTATION
+5VDCX
R8
10K
5
U11
6
10
PR
12
D
U13
Q
11
CK
9
CLR
13
CCWIRQ
2
OPBOUT
R10
10K
Q2
2N2222A
R9
5.1K
FROM SHT 1
RSTUN-
RSFSK-
5
FSK IN
4
1
+5VDCX
R11
1K
P6
20
PANEL +
1K
Q3
2N2222A
15
10
14
13
J3
2
J2
5
18
FSK/EXT INTERRUPT GENERATOR
4
PR
2
D
U14
Q
5
3
CK
Q̄
CLR
1
FSLIRQ
6
FIXVLT
+5VDCX
R4
10K
1/8W
CR1
1N914
9
U11
8
9
U6
8
10
PR
12
D
U14
Q
11
CK
Q̄
9
CLR
13
FSHIRQ
17
CR2
1N914
J3
LOC/REM
1
STDBY+
5
OPER
7
-12VOPA1
6
FANOFF
4
J2
E1
E2
RESLX1
2
E3
RESLX2
3
RESLT2
16

Figure 6-18. Encoder A1A2 Schematic Diagram (Sheet 2)

6-58. **Display Data Buffer.** The display data buffer U9 is used to provide the high drive current needed on the display data bus (DX0-DX7). It is also used to latch the display data, thus providing the long data hold time (50 nanoseconds minimum) required by the numeric indicators.

6-59. The output control (O.C.) input of U9 is wired to ground for continuous operation since there are no other driving elements on the display data bus.

6-60. The 1D through 8D inputs of the display data buffer are the DBUSX0 through DBUSX7 signals which are received from the MPU PC board A15A1.

6-61. The G (enable) input of U9 goes active high when WRTDSP goes active low. The eight latches in U9 are transparent D type latches meaning that while the enable (G) is high, the Q outputs will follow the data (D) inputs. When the enable is taken low by WRTDSP going high, the output will be latched at the level of the data that was setup.

6-62. **Resolution Control.** This circuit has three output signals which are used by the MPU to adjust the frequency display to the proper resolution. The RESLX1 and RESLX2 signals are permanently conditioned to a logic 1 or 0. A jumper to ground (E1) will condition a low. No jumper will condition a high because of the pull-up resistor located on the latch board. The jumper code can be determined from the following list.

Resolution	Jumpers
0.1 Hz	None
1.0 Hz	E1 to E2
10.0 Hz	E1 to E3
100.0 Hz	E1 to E2, E1 to E3

6-63. When the 380 is programmed to a frequency in the highband ($\geq$ 60 MHz), the resolution drops by one decade. The RESLT2 signal, which is generated in the plug-in, will go high when a highband frequency is programmed. The MPU uses this information to adjust the displayed resolution accordingly. For example, if no jumpers are installed, the displayed frequency resolution is 0.1 Hz for lowband (RESLT2 low) and 1.0 Hz for highband (RESLT2 high).

6-64. **LOC/REM Generator.** This circuit generates the LOC/REM signal which is used by the plug-in to select between keyboard or RF panel control of the amplitude and modulation functions.

6-65. The input to this circuit is the PANEL+ signal generated on the keyboard. When high, the PANEL+ signal turns the Q3 transistor on, which pulls LOC/REM low (local) and enables the RF panel amplitude and modulation controls. When the PANEL+ signal is low, the transistor is turned off and LOC/REM is pulled high by a resistor located in the plug-in. This will enable the remote inputs for modulation and amplitude, which in this case comes from the MPU board.

6-66. **LOWER AND HIGH ORDER BCD SECTION.** The BCD section consisting of modules A2 and A4 form the heart of the frequency synthesizer mainframe. The standard unit has 6 BCD decades generating 10^6 frequency steps with a 1.0-Hz resolution. Option -001 provides 7 decades with 10^7 steps and 0.1-Hz resolution. The four lower order decades, or five decades in the 001 option, are housed in lower order BCD A2. The function of each decade is essentially similar, therefore, the following paragraphs describe the basic theory of a BCD decade. The details of how each module differs will be discussed in later paragraphs under the separate modules.

6-67. Figure 6-19 shows the basic block diagram of all BCD decades. The function of the decade is to provide 10 discrete frequency outputs controlled by a BCD logic code. Given an input frequency of 35.0 MHz and the two comb line frequencies of 64 MHz and 65 MHz, the output frequency will be between 35.0 and 35.9 MHz, depending on the BCD code. If the BCD code is a 3, the output will be 35.3 MHz.

6-68. To understand the operation of the decade, trace a signal through the block diagram of any BCD decade. A starting frequency is chosen at 35 MHz and all RF switches are shown in their 0 position; in other words, a BCD 0 is programmed. In the BCD-1 mixer, a 35-MHz input signal mixes with the 65-MHz LO signal to form a 30-MHz product. This 30-MHz product passes through a 28-30 MHz bandpass filter and is applied to a flip-flop operating in a divide-by-two mode. The output of the flip-flop is a 15-MHz signal which is passed through a 14-15 MHz bandpass filter. This 15-MHz signal is applied to the second mixer where it mixes with 64 MHz generating a 49-MHz signal at the IF port. The 49-MHz signal passes through a 49-50 MHz bandpass filter and again into a flip-flop acting as a divide-by-two circuit. The output of this second divider is a signal of 24.5 MHz. This signal passes through a 24.5 to 25.5 MHz band-pass filter and is applied to the third mixer. The 24.5-MHz signal mixes with 64 MHz generating a signal at 40.5 MHz. This in turn passes through a 38.5 to 40.5 MHz band-pass filter and again a divide-by-two flip-flop generating a signal of 20.25 MHz. This signal passes through a 19.25 to 20.25 MHz band-pass filter and becomes the RF signal for a fourth mixer. The 20.25-MHz signal mixes with 64 MHz producing a mixed product at 43.75 MHz. This frequency is divided by 5 and then multiplied by 4 producing a 35-MHz output. Therefore, for a 35-MHz input with all switches in the zero position, a 35-MHz output results. Now, leave the BCD code in the zero position and apply a 36-MHz signal to the input, which is a 1-MHz change on the input frequency. When tracing this signal through, it is found that out of the first mixer 29 MHz appears, and after the frequency divider the signal is 14.5 MHz. Mixing this again with 64 MHz results in a 49.5-MHz signal and after the frequency divider, 24.75 MHz. After the third mixer the signal is 40.25 MHz and after the fourth mixer the signal is 43.825 MHz resulting in 35.1 MHz at the output. From this example, it is seen that 1 MHz change on the input produces 0.1 MHz on the output or an effective division ratio of 10 going through any BCD decade. This is an important fact because the divide by 10 provided 20 dB of improvement in spurs and noise through each decade.

6-69. To further understand the operation, trace through what happens as the BCD-8 switch is set to logic 1. If the BCD-8 switch is thrown to the 1 position, the input of the mixer increases by 1 MHz. The mixer is followed by a multiplication of 0.8 so that the output signal increases in frequency by 0.8 MHz. Thus,

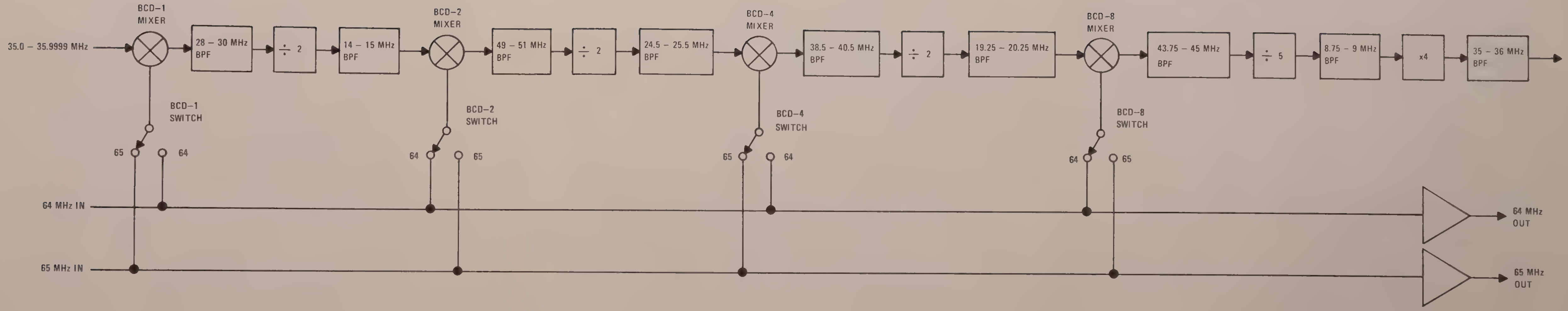

35.0 – 35.9999 MHz
BCD–1 MIXER
28 – 30 MHz BPF
÷ 2
14 – 15 MHz BPF
BCD–2 MIXER
49 – 51 MHz BPF
÷ 2
24.5 – 25.5 MHz BPF
BCD–4 MIXER
38.5 – 40.5 MHz BPF
÷ 2
19.25 – 20.25 MHz BPF
BCD–8 MIXER
43.75 – 45 MHz BPF
÷ 5
8.75 – 9 MHz BPF
x4
35 – 36 MHz BPF
BCD–1 SWITCH
65
64
BCD–2 SWITCH
64
65
BCD–4 SWITCH
65
64
BCD–8 SWITCH
64
65
64 MHz IN
65 MHz IN
64 MHz OUT
65 MHz OUT

Figure 6-19. BCD Decades, Block Diagram

this switch has the relative weight of 8. A binary divider separates the BCD-4 mixer from the BCD-8 mixer; in effect a 1-MHz change from the BCD-4 switch is half of the BCD-8 so this switch has a relative weight of 4. In a similar manner, the BCD-2 switch has a relative weight of 2 and the BCD-1 switch has a relative weight of 1. Therefore, each decade, for a 35.0 MHz input, can generate an output of 35.0, 35.1 all the way up to 35.9 directly from a BCD code. In addition, if the BCD code were a 1 for 35.0 MHz input, then the output frequency would be 35.1 MHz. However, if the input frequency was 35.5, then the output would be 35.15. In other words, for a frequency input of 35.X and a BCD code into the decade of N, the output frequency is 35.NX. Therefore, by cascading 6 of these decades it is seen that the output frequency is anywhere from 35 to 35.999999 MHz in 1 Hz steps. Table 6-2 shows the output frequency and output of the four 64/65 MHz switches for each BCD code on the input to the decade. The output frequency assumes a 35.000000 MHz input.

6-70. LOWER ORDER BCD A2. (Figure 6-20).

6-71. Lower order BCD module A2 (Figure 6-21) in the mainframe consists of 1-kHz BCD decade board A2A1 and three, 0.1-100 Hz BCD decade boards, A2A2, A2A3, and A2A4. A fifth position, A2A5, contains a jumper board. This jumper is removed, and another BCD board is inserted when the -001 option is included to provide another decade of resolution.

6-72. 1-kHz DECADE BOARD A2A1. (Figure 6-22). The 1-kHz, decade board (Figure 6-23) controls the 1-kHz digit in the frequency synthesizer output. The A2A1 output is 35 to 35.999995 MHz depending on the input frequency and BCD code from the 1-kHz logic as previously described. The output level should be greater than -8 dBm. This board contains 64-MHz and 65-MHz buffer amplifiers Q33 and Q34, respectively, which drive the 64 and 65 MHz inputs of A2A2 through A2A5. The level of these signals should be greater than 0 dBm. The remainder of this discussion assumes an input of 35.0000000 MHz.

TABLE 6-2. BCD SWITCH CODING

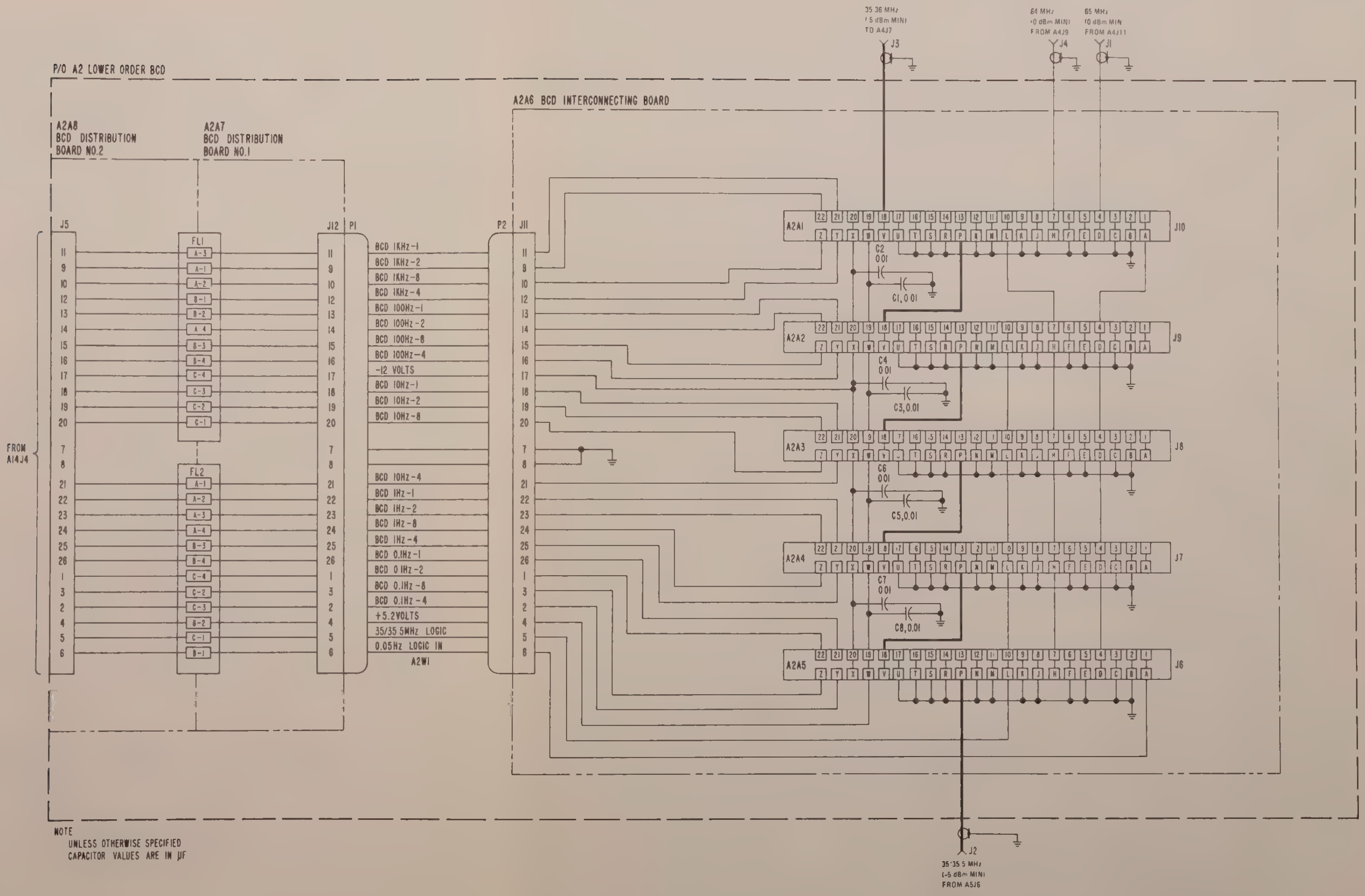
Output Frequency	BCD Code				BCD-1 Switch	BCD-2 Switch	BCD-4 Switch	BCD-8 Switch
	–1	–2	–4	–8				
35.00	0	0	0	0	65	64	65	64
35.10	1	0	0	0	64	64	65	64
35.20	0	1	0	0	65	65	65	64
35.30	1	1	0	0	64	65	65	64
35.40	0	0	1	0	65	64	64	64
35.50	1	0	1	0	64	64	64	64
35.60	0	1	1	0	65	65	64	64
35.70	1	1	1	0	64	65	64	64
35.80	0	0	0	1	65	64	65	65
35.90	1	0	0	1	64	64	65	65

6-73. The 35-MHz input signal to A2A1 is ac coupled through capacitor C1 to BCD-1 mixer integrated circuit U1-pin 11. The signal at this point is 35 MHz, 0.3V peak-to-peak centered around -7 Vdc. This signal is mixed with 65 MHz from the BCD-1 switch, Q1 through Q7. The 65 MHz enters the U1 mixer at pin 5 at a level of 0.7V peak-to-peak centered around -9.4 Vdc. The 65-MHz and 35-MHz signals are mixed in U1 and the composite signal appears at pin 3 at a level of 0.6V peak-to-peak centered around 2 Vdc. The lower sideband of the 30-MHz composite signal, is selected by 28-30 MHz bandpass filter comprising L1, L2, C9, C10, and C11. The 30 MHz is amplified by driver Q8 to a level of 2 volts peak-to-peak to drive divide-by-two circuit U2. The output of U2 is a 15-MHz square wave at about 2.5V peak-to-peak. This 15.0-MHz square wave is filtered by the 14-15 MHz bandpass filter formed by L4, L5, C13, C14, and C79, and is applied to pin 11 of U3 at a level of 1V peak-to-peak centered around 7.5 Vdc.

6-74. Integrated circuit U3 functions as a BCD-2 mixer. The 64-MHz output of BCD-2 switch Q9 through Q15 is applied to U3 pin 5 at a level of 0.7V peak-to-peak centered on -9.4 Vdc. The 64 MHz mixes with 15 MHz and the composite signal appears at pin 3 at a level of 0.5V peak-to-peak centered around 3.0 Vdc. The lower sideband of this composite signal, 49 MHz, is filtered by 49-51 MHz bandpass filter comprising L6, L7, C22, C23, and C24 and amplified by driver Q16. This signal is applied to a flip-flop which acts as a frequency divider producing 24.5 MHz. The input to U4 pin 13 is a 49-MHz sine wave centered at 1 Vdc and approximately 1.5V peak-to-peak. The output of U4 at 24.5 MHz is passed through a 24.5-25.5 MHz band-pass filter formed by L9, L10, C27, C28, and C29.

6-75. The output of the 24.5-25.5 MHz band-pass filter connects to the BCD-4 mixer, U5 pin 11. The signal is 24.5 MHz at a level of 0.6V peak-to-peak centered at -2.4 Vdc. This signal is mixed in U5 with the output of the BCD-4 switch, a 65-MHz signal of 0.7V peak-to-peak centered around -9.4 Vdc. The BCD-4 switch is made up of transistors Q17 through Q23. The output of the U5 mixer appears at pin 3 at a level of 0.7V peak-to-peak centered around -2.3 Vdc. The lower sideband at this composite signal, 40.5 MHz, is selected by a 38.5-40.5 MHz bandpass filter consisting of L13, L14, C39, C40, and C41, amplified by driver Q24, and applied to U6 at a level of 1.5V peak-to-peak at a dc level of 1.0 volt. Flip-flop U6 functions as a frequency divide-by-two circuit producing a 20.25-MHz output signal. This signal is passed through a 19.25 to 20.25 MHz bandpass filter consisting of L16, L17, L19, C43, C44, C45, C46, and C80.

6-76. The filtered 20.25-MHz signal is the RF input to BCD-8 mixer U7. This input is at pin 11 at a level of 0.8 peak-to-peak centered around -6.6 Vdc. It is mixed with the output of the BCD-8 switch formed by transistors Q25 through Q31. The output of this switch at U7 pin 5 is 64 MHz at 0.7V peak-to-peak centered around -9.4 Vdc. This mixes with the 20.25 MHz producing a composite signal at pin 3 of 1.0V peak-to-peak centered around -1.75 Vdc. The lower sideband of this signal, 43.75 MHz, is selected by the 43.75-45 MHz bandpass filter, consisting of L21-L24 and C54-C59. The signal is amplified by Q35 so that the output of the filter is at a sufficient level for the driver.


35 36 MHz
(5 dBm MIN)
TO A4J7
J3
84 MHz
(0 dBm MIN)
FROM A4J9
J4
85 MHz
(0 dBm MIN
FROM A4J11
J1
P/O A2 LOWER ORDER BCD
A2A6 BCD INTERCONNECTING BOARD
A2A8
BCD DISTRIBUTION
BOARD NO.2
A2A7
BCD DISTRIBUTION
BOARD NO.1
J5
FL1
J12
P1
P2
J11
FROM
A14J4
FL2
BCD 1KHz-1
BCD 1KHz-2
BCD 1KHz-8
BCD 1KHz-4
BCD 100Hz-1
BCD 100Hz-2
BCD 100Hz-8
BCD 100Hz-4
-12 VOLTS
BCD 10Hz-1
BCD 10Hz-2
BCD 10Hz-8
BCD 10Hz-4
BCD 1Hz-1
BCD 1Hz-2
BCD 1Hz-8
BCD 1Hz-4
BCD 0.1Hz-1
BCD 0.1Hz-2
BCD 0.1Hz-8
BCD 0.1Hz-4
+5.2VOLTS
35/35.5MHz LOGIC
0.05Hz LOGIC IN
A2W1
A2A1
J10
C2
0.01
C1,0.01
A2A2
J9
C4
0.01
C3,0.01
A2A3
J8
C6
0.01
C5,0.01
A2A4
J7
C7
0.01
C8,0.01
A2A5
J6
J2
35/35.5 MHz
(-5 dBm MIN)
FROM A5J6

NOTE
UNLESS OTHERWISE SPECIFIED
CAPACITOR VALUES ARE IN µF

Figure 6-20. Lower Order BCD Assembly A2, Interconnecting Diagram

6-77. The filtered signal is amplified by driver Q32 and applied to U8. Integrated circuit U8 is a frequency divider which divides the 43.75 MHz by 5 producing 8.875 MHz rich in harmonics due to fast rise times on the output of U8. The fourth harmonic of the 8.875 MHz signal is selected by the 35-36 MHz bandpass filter consisting of L26, L27, L28, C61, C62, C63, C64, and C65. The input to U8 at pin 6 is 43.75 MHz at 2V peak-to-peak at a level of 1.25 Vdc. The output of the 35-36 MHz filter is 0.4V peak-to-peak or -5 dBm.

6-78. **0.1 to 100 Hz BCD DECADE BOARDS A2A2 THROUGH A2A5.** (Figure 6-24). The 0.1 to 100 Hz BCD decade boards A2A2, A2A3, A2A4, and A2A5 (-001 option) are identical to each other and are similar to A2A1. The only difference between these boards and A2A1, is that these boards do not contain 64 and 65 MHz buffer amplifiers Q33 and Q34. See Figure 6-25 for component locations of board A2A2.

6-79. **HIGHER ORDER BCD A4.**

6-80. Higher order BCD module A4 (Figure 6-26) produces the 100-kHz and 10-kHz decades in the mainframe output. The 10-kHz decade is contained on board A4A11; the 100-kHz decade is contained on boards A4A1 through A4A10.

6-81. **BCD-1 BOARD A4A1.** (Figures 6-27 and 6-28). The input to A4A1 is a 35- to 36-MHz signal at -5 dBm which comes from 10 kHz BCD board A4A11. This signal passes through a lowpass filter (C1, C2, and L1) and a 3-dB pad and is applied to the RF input of mixer M1. The local oscillator for the mixer is 64 or 65 MHz from BCD-1 switch A4A7. The mixed signal is passed through the 28 to 30 MHz bandpass filter consisting of C3 through C6 and L2, L3, and L4 which selects the lower sideband. This signal is amplified in Q1 and applied to U1 which acts as a frequency divide-by-two circuit. The output of U1 is filtered by 14 to 15 MHz bandpass filter C10 through C15 and L7 through L13 which provides a 14-15 MHz signal at E4 at a level of +3 dBm.

6-82. **BCD-2 BOARD A4A2.** (Figures 6-27 and 6-29). The input to BCD-2 board A4A2 is the 14- to 15-MHz output from A4A1. This signal passes through a lowpass filter and attenuator comprising of C1, C2, C3, L1, L2, R1, R2, and R3 and is applied to mixer M1 at a level of -5 dBm. This signal is mixed with 64 or 65 MHz from BCD-2 switch A4A8. The lower order sideband of the mixer is selected by the 49- to 51-MHz bandpass filter consisting of C4 through C7, and L3 through L5. This signal is amplified in Q1 and applied to U1 which acts as a frequency divider-by-two. The output of U1 is filtered by 24.5 to 25.5 MHz bandpass filter C14 through C19 and L8 through L14. The output of this filter appears at E5 at a level of 0 dBm.

6-83. **BCD-4 BOARD A4A3.** (Figures 6-27 and 6-30). The input to BCD-4 board A4A3 is the 24.5- to 25.5-MHz output from A4A2. This signal is passed through a lowpass filter and attenuator comprising of C1, C2, C3, L1, L2, R1, R2, and R3, and applied to mixer M1. This signal is mixed with 64 or 65 MHz from BCD-4 switch A4A9. The lower sideband of this mixer is selected by the 38.5 to 40.5 MHz bandpass filter consisting of C4 through C8 and L3 through L7. This signal is amplified by Q1 and applied to U1 which acts as a frequency divider-by-two circuit. The output of U1 is filtered by 19.25 to 20.25 MHz bandpass filter C16 through C21 and L11 through L17. The output of this filter appears at E3 at a level of +2 dBm.

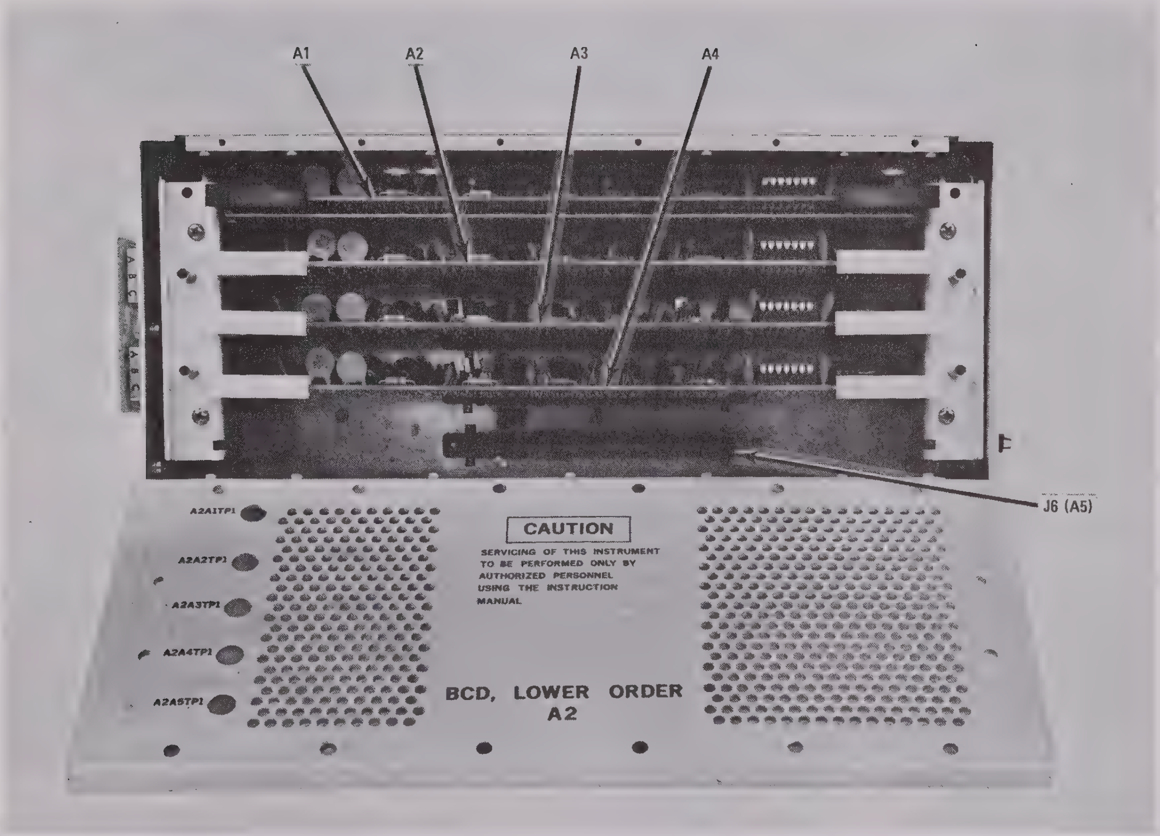
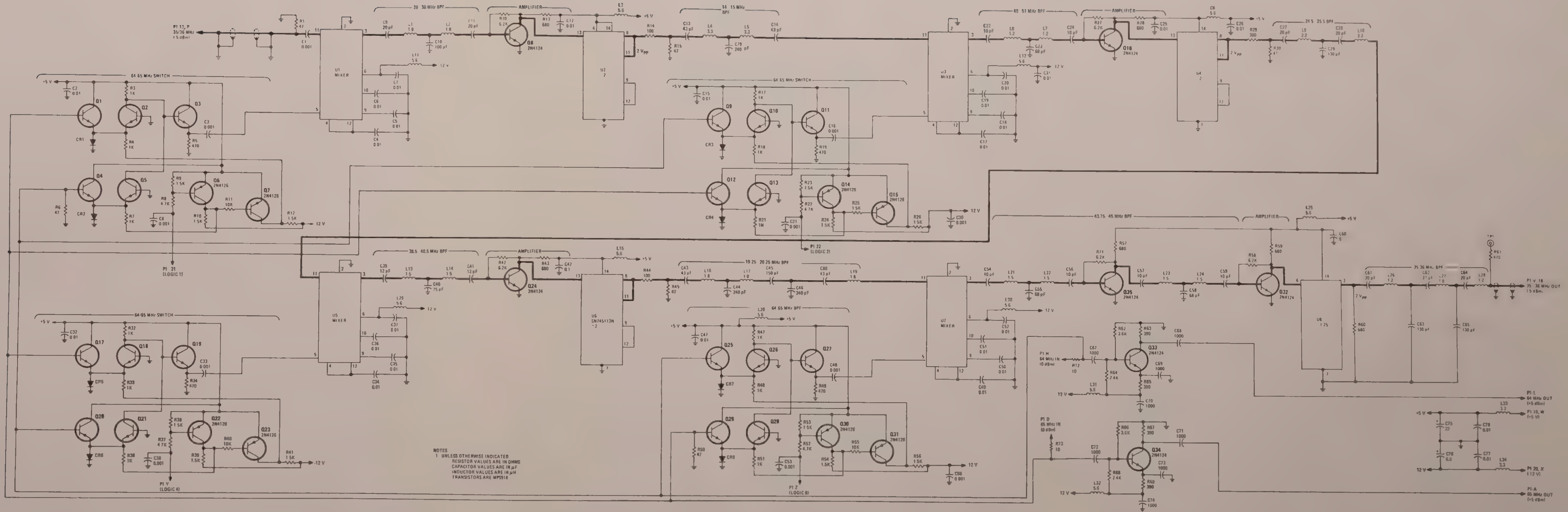

A1
A2
A3
A4
J6 (A5)
A2A1TP1
A2A2TP1
A2A3TP1
A2A4TP1
A2A5TP1
CAUTION
SERVICING OF THIS INSTRUMENT TO BE PERFORMED ONLY BY AUTHORIZED PERSONNEL USING THE INSTRUCTION MANUAL
BCD, LOWER ORDER
A2

Figure 6-21. Lower Order BCD Assembly A2


P1 13, P
35/36 MHz
(-5 dBm)
28 30 MHz BPF
AMPLIFIER
U1
MIXER
Q8
2N4124
U2
÷2
2 Vpp
14 15 MHz
BPF
64 65 MHz SWITCH
U3
MIXER
48 51 MHz BPF
AMPLIFIER
Q16
2N4124
U4
÷2
24.5 25.5 MHz BPF
P1 21
(LOGIC 1)
P1 22
(LOGIC 2)
38.5 40.5 MHz BPF
AMPLIFIER
Q24
2N4124
U5
MIXER
U6
SN74S113N
÷2
19.25 20.25 MHz BPF
64 65 MHz BPF
U7
MIXER
43.75 45 MHz BPF
AMPLIFIER
Q35
2N4124
Q32
2N4124
U8
÷1.75
35 36 MHz BPF
TP1
P1 V 18
35 36 MHz OUT
(-5 dBm)
64/65 MHz SWITCH
P1 Y
(LOGIC 4)
P1 Z
(LOGIC 8)
P1 H
64 MHz IN
(0 dBm)
Q33
2N4124
P1 D
65 MHz IN
(0 dBm)
Q34
2N4124
P1-L
64 MHz OUT
(+5 dBm)
P1 19, W
(+5 V)
P1 20, X
(12 V)
P1-A
65 MHz OUT
(+5 dBm)
+5 V
12 V
-12 V

NOTES
1 UNLESS OTHERWISE INDICATED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH
TRANSISTORS ARE MPS918

Figure 6-22. 1-kHz Decade Board A2A1, Schematic Diagram

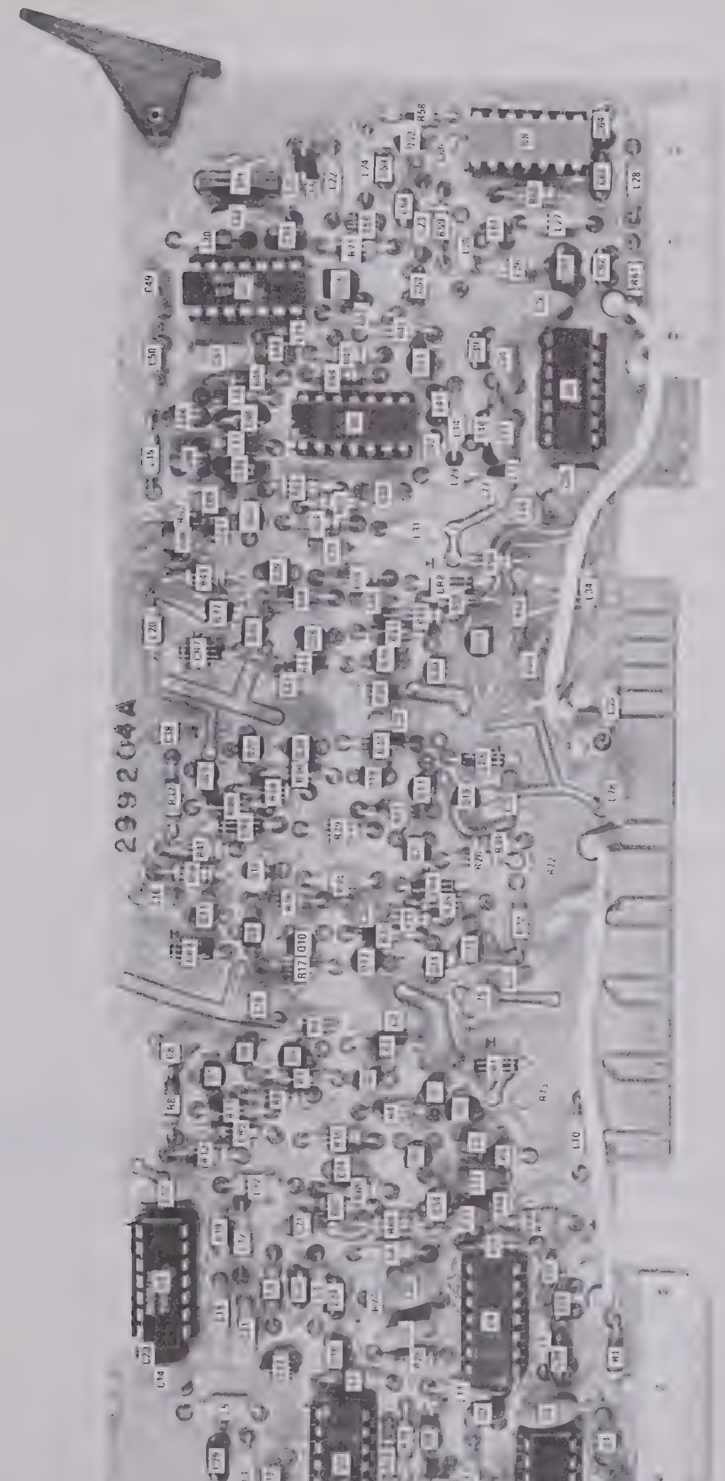


Figure 6-23. 1-kHz Decade Board A2A1, Component Locations

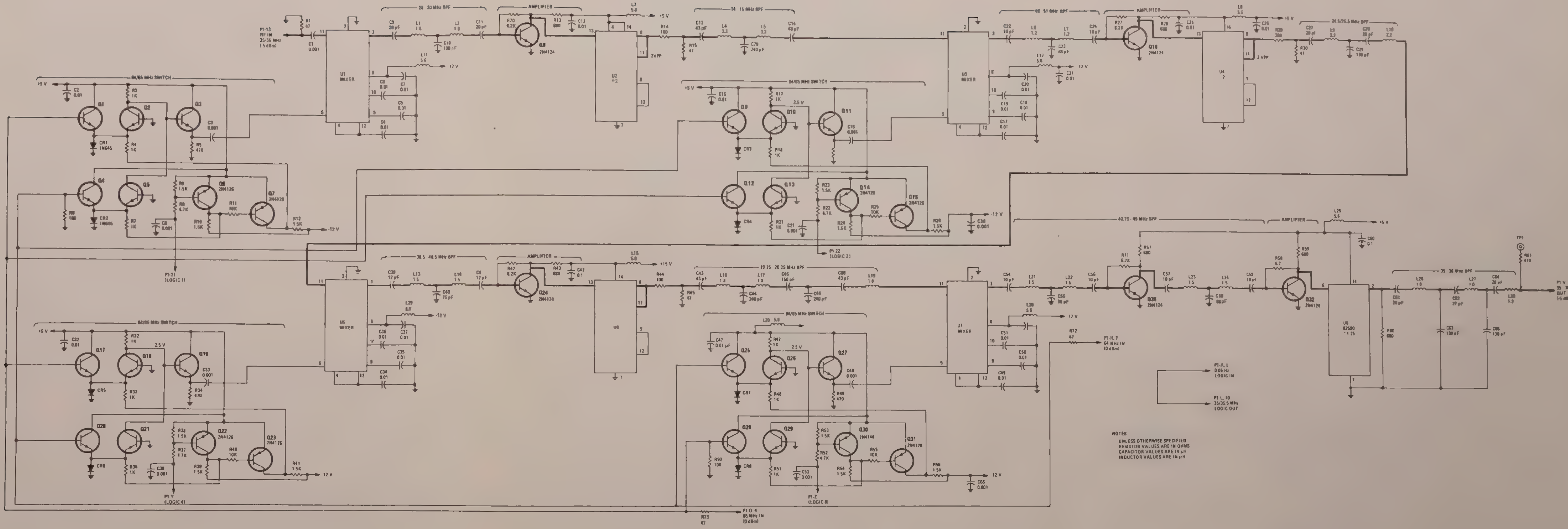
6-84. BCD-8 (NO. 1) BOARD A4A4. (Figures 6-27 and 6-31). The 19.25-20.25 MHz signal from A4A3 is applied to A4A4 at E1 and is filtered by lowpass filter C1, C2, L1 and attenuator R1, R2, and R3. The signal is applied to mixer M1 where it mixes with 64 or 65 MHz from BCD-8 switch A4A10. The lower sideband from the mixer is selected by the 43.75 to 45.0 MHz bandpass filter formed by C3 through C8 and L2 through L8. This signal is amplified to a level of -5 dBm and applied to BCD-8 (No. 2) board A4A5.

6-85. BCD-8 (NO. 2) BOARD A4A5. (Figures 6-32 and 6-33). The 43.75- to 45.0-MHz input signal from A4A4 is filtered by the bandpass filter formed by C1 through C4, and L1-L3 and amplified by Q1 to a level sufficient to drive U1. U1 is a frequency divide-by-5 circuit producing an output equal to one fifth the input or 8.75 to 9.0 MHz. This signal is amplified and passed through a 250 kHz bandwidth filter consisting of C13 through C21 and L7 through L13. The output of this filter is multiplied by 4 in the multiplier section consisting of Q3 and Q4. C27 through C31, L15 through L18, and L25 form a bandpass filter at the 35- to 36-MHz output frequency to reject the 8.75- to 9.0-MHz sidebands of the multiplier. The 35- to 36-MHz signal is amplified in transistor stage Q5 and passed through a lowpass filter (C35, C36, L19) and attenuator (R20, R21, and R22) to produce an output signal about -2 dBm. This signal is the final output of the mainframe.

6-86. BUFFER AMPLIFIER A4A6. (Figures 6-34 and 6-35). The 64- and 65-MHz inputs at J2 and J4 are amplified by transistor stages Q1 and Q2, respectively. The output of Q1 is filtered by a 64-MHz filter consisting of C4 through C7 and L3 through L5 and connects to the 64-MHz input of 10 KHz BCD board A4A11 through a 3-dB attenuator comprising R7, R8, and R9. The output of Q2 is filtered by a 65-MHz filter consisting of C13 through C16 and L9 through L11 and passed through a 3-dB attenuator comprising R16, R17, and R18. This signal becomes the 65-MHz input to 10 kHz BCD board A4A11.

6-87. 64/65 MHz SWITCHES A4A7 THROUGH A4A10. (Figures 6-34 and 6-36). The 64/65 MHz switches (A4A7, A4A8, A4A9, and A4A10) select 64 or 65 MHz for each of the higher order BCD boards A4A1, A4A2, A4A3, and A4A4, respectively. The input level is about 200 mV rms (0 dBm into 50 ohms). The logic input at E3 is a TTL compatible level where a high level selects the B input at E2 and a low level selects the A input at E1. When a low level is applied at E3, Q7 goes on and turns Q8 off. With Q7 on, CR2 if forward biased and Q4 and Q5 are reversed biased thereby isolating the "B" input at E2. With Q8 off, CR1 is biased off and Q1 and Q2 are biased on. Q1 and Q2 amplify the "A" input and apply it to buffer amplifier Q3. The signal from Q3 is further amplified in Q9 and passed through a 64- to 65-MHz band-pass filter consisting of C11 through C14, and L5 through L7. The output of this filter is at a level of +7 dBm and drives the LO input to the mixer. When a "high" level is applied at E3, Q7 goes off and turns Q8 on. With Q8 on, CR1 is biased on, and Q1 and Q2 are biased off thereby isolating the "A" input at E1. With Q7 off, Q4 and Q5 are biased on and CR2 is biased off thereby amplifying the "B" input and connecting it to the buffer Q3 as described above.

6-88. 10 kHz BCD BOARD A4A11. (Figures 6-37 and 6-38). The input to 10 kHz BCD board A4A11 is a 35- to 36-MHz signal at -5 dBm, from lower order BCD assembly A2. This signal is mixed in the BCD-1 mixer with 64 or 65 MHz as selected by the BCD-1 switch (Q5 through Q11 and Q1). The level out of all the switches measured at the mixer LO input is +7 dBm nominal. The 28 to 30 MHz bandpass filter


P1-13
RF IN
35/36 MHz
(-5 dBm)
R1 47
C1 0.001
U1 MIXER
28 30 MHz BPF
C9 20 pF
L1 1.8
L2 1.8
C10 100 pF
C11 20 pF
L11 5.6
12 V
C6 0.01
C7 0.01
C5 0.01
C4 0.01
AMPLIFIER
R70 6.2K
R13 680
C12 0.01
Q8 2N4124
L3 5.6
+5 V
U2 ÷2
2VPP
R14 100
R15 47
14 15 MHz BPF
C13 43 pF
L4 3.3
L5 3.3
C79 240 pF
C14 43 pF
64/65 MHz SWITCH
+5 V
C2 0.01
Q1
Q2
Q3
R3 1K
CR1 1N645
R4 1K
R5 470
C3 0.001
Q4
Q5
Q6 2N4126
Q7 2N4126
R6 100
CR2 1N645
R7 1K
R9 1.5K
R8 4.7K
C8 0.001
R10 1.5K
R11 10K
R12 1.5K
-12 V
P1-21
(LOGIC 1)
64/65 MHz SWITCH
C15 0.01
Q9
Q10
Q11
R17 1K
2.5 V
CR3
R18 1K
C16 0.001
Q12
Q13
Q14 2N4126
Q15 2N4126
CR4
R21 1K
C21 0.001
R23 1.5K
R22 4.7K
R24 1.5K
R25 10K
R26 1.5K
C30 0.001
-12 V
P1 22
(LOGIC 2)
U3 MIXER
48 51 MHz BPF
C22 10 pF
L6 1.2
L7 1.2
C23 68 pF
C24 10 pF
L12 5.6
12 V
C31 0.01
C20 0.01
C19 0.01
C18 0.01
C17 0.01
AMPLIFIER
R27 6.2K
R28 680
C25 0.01
Q16 2N4124
L8 5.6
C26 0.01
+5 V
U4 2
2 VPP
R29 390
R30 47
24.5/25.5 MHz BPF
C27 20 pF
L9 2.2
C28 20 pF
C29 130 pF
L10 2.2
38.5 40.5 MHz BPF
U5 MIXER
C39 12 pF
L13 1.5
L14 1.5
C40 75 pF
C41 12 pF
L29 6.8
-12 V
C36 0.01
C37 0.01
C35 0.01
C34 0.01
AMPLIFIER
R42 6.2K
R43 680
C42 0.1
Q24 2N4126
L15 5.6
+15 V
U6
R44 100
R45 47
19 25 20 25 MHz BPF
C43 43 pF
L16 1.8
L17 1.0
C65 150 pF
C44 240 pF
C46 240 pF
C68 43 pF
L19 1.8
64/65 MHz SWITCH
+5 V
C32 0.01
Q17
Q18
Q19
R32 1K
2.5 V
CR5
R33 1K
R34 470
C33 0.001
Q20
Q21
Q22 2N4126
Q23 2N4126
CR6
R36 1K
R38 1.5K
R37 4.7K
C38 0.001
R39 1.5K
R40 10K
R41 1.5K
12 V
P1-Y
(LOGIC 4)
64/65 MHz SWITCH
L20 5.6
C47 0.01 µF
Q25
Q26
Q27
R47 1K
2.5 V
CR7
R48 1K
R49 470
C48 0.001
Q28
Q29
Q30 2N4146
Q31 2N4126
R50 100
CR8
R51 1K
C53 0.001
R53 1.5K
R52 4.7K
R54 1.5K
R55 10K
R56 1.5K
C66 0.001
12 V
P1-Z
(LOGIC 8)
R73 47
P1 D 4
65 MHz IN
(0 dBm)
U7 MIXER
L30 5.6
12 V
C51 0.01
C50 0.01
C49 0.01
R72 47
P1-H, 7
64 MHz IN
(0 dBm)
43.75 - 46 MHz BPF
C54 10 pF
L21 1.5
L22 1.5
C55 68 pF
C56 10 pF
R71 6.2K
R57 680
Q35 2N4124
C57 10 pF
L23 1.5
L24 1.5
C58 68 pF
C59 10 pF
AMPLIFIER
L25 5.6
+5 V
C60 0.1
R58 6.2
R59 680
Q32 2N4124
U8 82590
÷1 25
R60 680
35 36 MHz BPF
C61 20 pF
L26 1.0
C62 27 pF
C63 130 pF
L27 1.0
C64 20 pF
C65 130 pF
L28 1.2
TP1
R61 470
P1 V 18
35 36 MHz
OUT
(-5 dBm)
P1-A, L
0.05 Hz
LOGIC IN
P1 L, 10
35/35.5 MHz
LOGIC OUT

NOTES.
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN µF
INDUCTOR VALUES ARE IN µH

Figure 6-24. 0.1 to 100 Hz BCD Decade Boards A2A2-A2A5, Schematic Diagram

consisting of L3, L4, C4, C5, and C6 selects the lower sideband of the BCD-1 mixer. This signal is amplified by driver Q2 to a level of 2V peak-to-peak centered at +1.2V and is applied to U1 pin 13. U1 is frequency divide-by-2 circuit which has an output of 14 to 15 MHz at 2.5 V peak-to-peak. This signal is filtered by a 14 to 15 MHz bandpass filter consisting of L6, L7, C8, C9, and C10 and applied to the RF input of the BCD-2 mixer.

6-89. BCD-2 mixer M2 mixes the 14- to 15-MHz signal with 64 or 65 MHz from the BCD-2 switch which consists of Q3, and Q12 through Q18. The lower sideband, or difference frequency, is selected by a 49 to 51 MHz bandpass filter consisting of L10, L11, C12, C15, and C16. This filtered signal is amplified by driver Q4 to a level of 1.6V peak-to-peak centered on 1.2V and applied to flip-flop U2 which functions as a frequency divide-by-2 circuit. The output of U2 is 24.5 to 25.5 MHz which is filtered by the bandpass filter formed by C19, C20, C21, L13, and L14.

6-90. BCD-4 mixer M3 heterodynes this signal, which is at a level of 0.6V peak-to-peak, with the output of the BCD-4 switch consisting of Q24 through Q30 and Q19. The lower sideband of the 38.5 to 40.5 MHz, is selected by the bandpass filter formed by C32, C34, C35, L21 and L22. This signal is amplified by transistor driver Q20 and applied to U3 at a level of 2V peak-to-peak centered at 1.1V. Flip-flop U3 functions as a frequency divide-by-two circuit. The output of this circuit is filtered by the 19.25 to 20.25 MHz bandpass filter formed by C37 through C41, L24, L25, and L26. This signal is applied to the BCD-8 mixer at a level of 0.4V peak-to-peak.

6-91. BCD-8 mixer M4 mixes the 19.25 to 20.25 MHz signal with the output of the BCD-8 switch which consists of Q31 through Q37 and Q21. The output of M4 is filtered by C43, C46, C47, L27, and L30 which selects the lower sideband, 43.75 to 45.0 MHz. This signal is amplified in Q22 and filtered by an identical filter C48, C49, C50, L31, and L32 for more rejection. The filtered signal is amplified by transistor driver Q23 and applied to U4 at a level of 1.6V peak-to-peak centered at 1V. U4 is a decade counter connected as a frequency divide-by-five circuit. Due to the fast rise times on the output of U4, the output signal is rich in harmonics. The fourth harmonic is selected by a 35 to 36 MHz filter consisting of C52 through C56, L34, L35, and L36. The output of this filter is the output of the board at a level of -5 dBm.

6-92. The 64- and 65-MHz inputs for the switches come from A4A6. In addition to being applied to the switches, these inputs are applied to buffer amplifiers and bandpass filters to isolate the lower order BCD assembly from the 10-kHz output. The 64-MHz buffer amplifier is Q38 and the filter is L43, L45, C67, C73, and C75. The 65-MHz buffer amplifier is Q39 and the filter is C70, C74, C78, L44 and L46. The output of both of these amplifiers is a 0 dBm.

6-93. FIXED FREQUENCY GENERATOR A5.

6-94. The fixed frequency generator (Figure 6-39) produces all the fixed reference frequencies required by the frequency synthesizer. The output frequencies are 10, 60, 64, 65, and 35 or 35.5 MHz. All of these signals are derived directly from the 5- or 10-MHz input by simple operations of multiplication, division, addition, and subtraction.

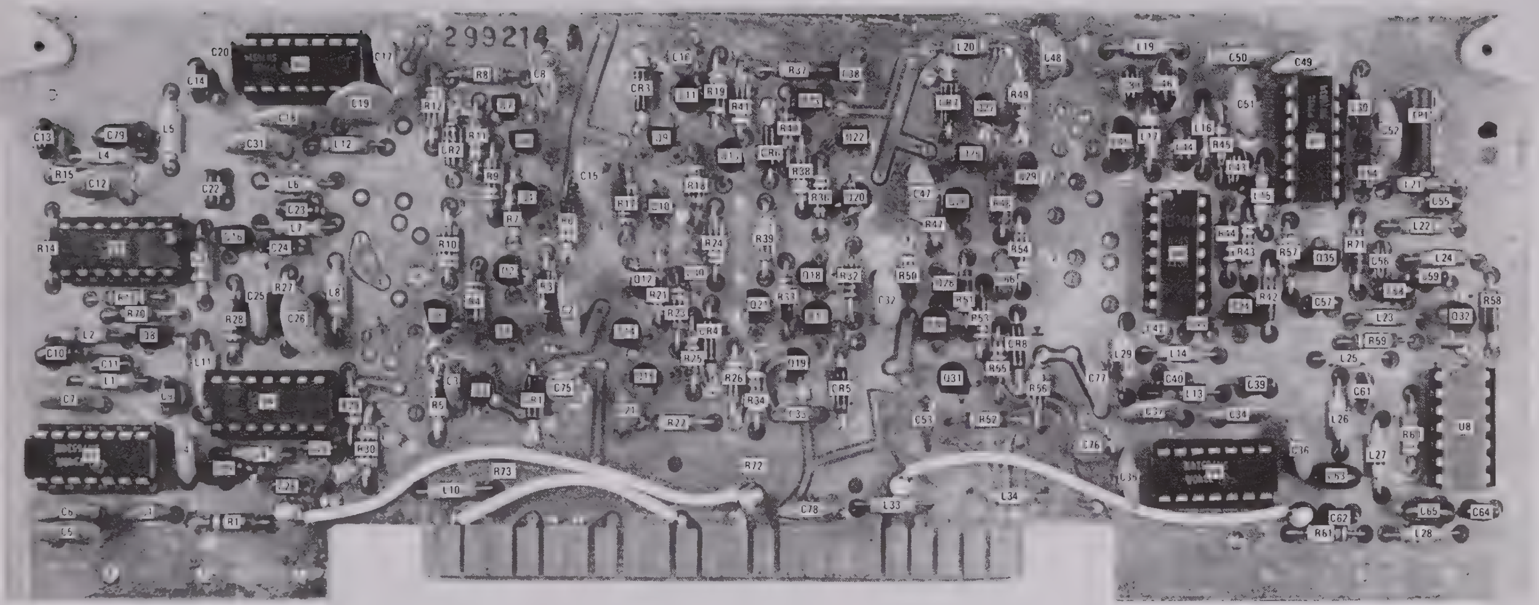

299214
C20
C17
C14
C19
C13
C79
L5
L4
R15
C12
C31
L12
C22
L6
C23
L7
R14
C24
C25
R27
C26
L8
R28
C10
L2
C11
L1
C7
L11
R30
C6
C5
R1
R8
C8
R12
CR2
R11
R9
R7
R10
C15
CR3
L16
R19
R41
R37
L38
R38
R36
L20
R22
R24
Q17
R21
R23
CR4
R25
R26
R34
C75
R5
R22
R73
L10
R72
C78
L33
L34
R39
Q21
Q18
R32
Q19
CR5
R20
R49
L48
C47
R47
R50
C32
Q31
C53
R52
R54
R51
P53
CR8
R55
R56
C77
L19
C50
C49
C51
L16
R45
L17
R44
R43
R57
Q35
R71
C56
L24
L22
C55
L21
C52
R58
Q32
C57
R42
L23
R59
L25
C61
L29
L14
C40
L13
C39
C37
C34
L26
L27
R6
U8
C36
C76
C62
R61
L28
L65
C64

Figure 6-25. 0.1 to 100 Hz BCD Decade Boards A2A2-A2A5, Component Locations

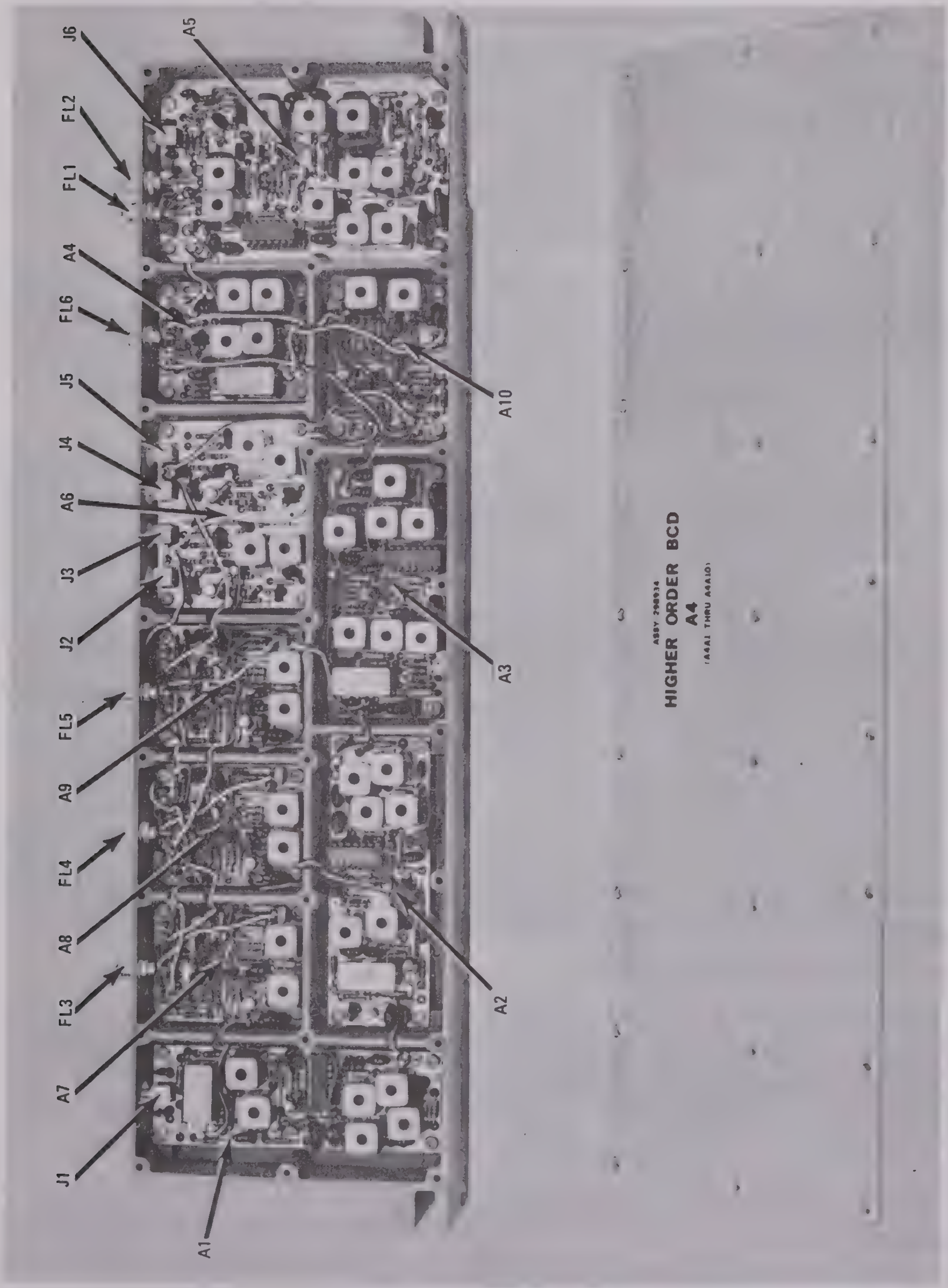

J6
FL2
FL1
A4
FL6
J5
J4
A6
J3
J2
FL5
A9
FL4
A8
FL3
A7
J1
A1
A5
A10
A3
A2
HIGHER ORDER BCD
A4
(A4A1 THRU A4A10)

Figure 6-26. Higher Order BCD Assembly A4

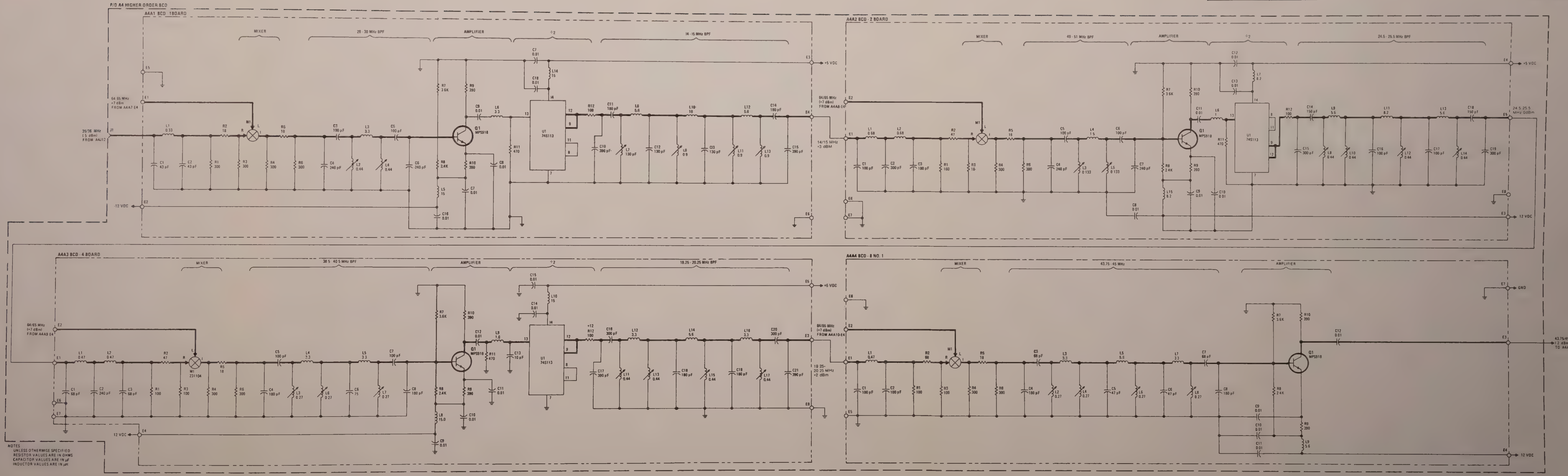
6-95. **FIXED FREQUENCY INPUT BOARD A5A1.** (Figures 6-40 and 6-41). The input to this board is from the REF IN connector at the rear panel of the frequency synthesizer. The input signal is 5 or 10 MHz at a level of 0 dBm minimum. This signal goes through saturated amplifier Q1 which limits the 10 MHz or produces harmonics of 5 MHz. C4, L1, C6, L2, and C7 form a 10 MHz bandpass filter which selects the correct harmonic (10 MHz) of the input signal. The 10-MHz signal is further amplified by Q2 and filtered by 4-pole bandpass filter L3, L5, L6, and L8. The output of this filter is amplified again and passed through crystal filter Y1 which reduces excess noise on the reference by 20 dB for noise sidebands greater than 500 Hz away from the carrier. The output of this board is 0 dBm into 50 ohms. The 10-MHz signal drives all the other units in the fixed frequency generator.

6-96. **10 MHz OUTPUT AMPLIFIER A5A2.** (Figures 6-40 and 6-42). This printed circuit board buffers the 10-MHz signal. One buffered output at A5J2 provides 10 MHz at +3 dBm for the RF plug-in unit. The other output at A5J3 provides a 10-MHz reference output at +3 dBm at the rear panel of the frequency synthesizer. The board consists of two amplifiers, Q1 and Q2, and two, 10-MHz bandpass filters: L1, L2, C1, C2, and C3; and L4, L5, C6, C7, and C8.

6-97. **60 MHz GENERATOR A5A3.** (Figures 6-40 and 6-43). The 60 MHz generator provides a 60-MHz output at +5 dBm. The 10-MHz signal from fixed frequency input board A5A1 is amplified by Q1. Transistor Q2 is a saturated amplifier stage followed by 60-MHz, four-pole bandpass filter L3 through L6. The combination of the saturated amplifier and bandpass filter form a X6 multiplier. The output of the bandpass filter is a 60-MHz signal which is amplified by Q3 and filtered by 60-MHz bandpass filter L9, L10, C13, C19, and C20. The output of A5A3 is a +5 dBm signal at 60 MHz for use in the frequency extender module.

6-98. **65 MHz GENERATOR A5A4.** (Figures 6-44 and 6-45). The input to the 65 MHz generator is 10 MHz from fixed frequency input board A5A1. This signal is amplified by amplifier Q1. Transistor Q2 is a class C amplifier which produces a 10-MHz signal rich in harmonics. The 13th harmonic, 130 MHz, is selected by four-pole bandpass filter L4 through L7 and C8 through C13 and C15 and C16. The 130-MHz signal is amplified by Q3 which forms a driver stage for U1 - a high speed flip-flop operating as a frequency divide-by-two circuit producing 65 MHz. The 65 MHz is amplified by Q4 and filtered by five-pole bandpass filter L14 through L18 and C24 through C34. The 65-MHz output at 0 dBm minimum provides one of the reference frequencies required for the BCD section.

6-99. **64 MHz GENERATOR A5A5.** (Figures 6-46 and 6-47). The 64 MHz generator starts with 10 MHz from fixed frequency input board A5A1 and amplifies the 10 MHz in Q1. Transistor Q2 is a saturated amplifier stage which produces harmonics of 10 MHz. The eighth harmonic (80 MHz) is selected by five-pole bandpass filter L5 through L9, C5, and C8 through C16. The output of this filter is 80-MHz signal which is amplified by Q3 (part of a driver for U1 and U2). U1 and U2 are Schottky TTL flip-flops connected to form a frequency divide-by-five circuit. The output at U2 pin 6 is a 16-MHz signal rich in harmonics due to the fast rise times of the Schottky TTL logic. L12, L13, C19, C20, and C21 form a bandpass filter selecting the fourth harmonic (64 MHz) which is amplified by Q4. L16 through L20 and C25 through C34 form a five-pole bandpass filter at 64 MHz which filters out sidebands from the divider. The output of this filter is 64 MHz at 0 dBm minimum which is one of the reference frequencies used in the BCD section.


P/O A4 HIGHER ORDER BCD
A4A1 BCD 1 BOARD
MIXER
28 - 30 MHz BPF
AMPLIFIER
÷2
14 - 15 MHz BPF
E5
64 65 MHz
+7 dBm
FROM A4A7 E4
E1
35/36 MHz
(5 dBm)
FROM A4J12
J1
L1 0.33
R2 18
R
M1
L
I
R5 18
C3 100 pF
L3 3.3
C5 100 pF
C1 43 pF
C2 43 pF
R1 300
R3 300
R4 300
R6 300
C4 240 pF
L2 0.44
L4 0.44
C6 240 pF
E2
-12 VDC
R7 3.6K
R9 390
R8 2.4K
L5 15
C16 0.01
C9 0.01
L6 3.3
Q1 MPS918
R10 390
C8 0.01
C7 0.01
R11 470
C7 0.01
L14 15
C18 0.01
U1 74S113
14
13
12
9
11
8
7
R12 100
C11 180 pF
L8 5.6
L10 10
L12 5.6
C14 180 pF
C10 390 pF
L7 130 pF
C12 130 pF
L9 0.9
C13 130 pF
L11 0.9
L13 0.9
C15 390 pF
E3
+5 VDC
E4
E6
14/15 MHz
+3 dBM
A4A2 BCD - 2 BOARD
MIXER
49 - 51 MHz BPF
AMPLIFIER
÷2
24.5 - 25.5 MHz BPF
64/65 MHz
(+7 dBm)
FROM A4A8-E4
E2
E1
L1 0.68
L2 0.68
R2 47
M1
R5 18
C5 100 pF
L4 1.5
C6 100 pF
C1 100 pF
C2 300 pF
C3 100 pF
R1 160
R3 16
R4 300
R6 300
C4 240 pF
L3 0.133
L5 0.133
C7 240 pF
E6
E7
R7 3.6K
R10 390
R8 2.4K
L15 8.2
R9 390
C9 0.01
C10 0.01
C8 0.01
C11 0.01
L6 1
Q1 MPS918
R11 470
C12 0.01
C13 0.01
L7 8.2
U1 74S113
R12 100
C14 150 pF
L9 5.6
L11 8.2
L13 5.6
C18 150 pF
C15 300 pF
L8 0.44
L10 0.44
C16 100 pF
L12 0.44
C17 100 pF
L14 0.44
C19 300 pF
E4
+5 VDC
E5
24.5 25.5 MHz 0dBm
E8
E3
12 VDC
A4A3 BCD - 4 BOARD
MIXER
38.5 40.5 MHz BPF
AMPLIFIER
÷2
19.25 - 20.25 MHz BPF
64/65 MHz
(+7 dBm)
FROM A4A9 E4
E2
E1
L1 0.47
L2 0.47
R2 47
R
L
I
M1 231104
R5 18
C5 100 pF
L4 2.3
L5 3.3
C7 100 pF
C1 68 pF
C2 240 pF
C3 68 pF
R1 100
R3 100
R4 300
R6 300
C4 180 pF
L3 0.27
L6 0.27
C6 75
L7 0.27
C8 180 pF
E6
E7
E4
12 VDC
R7 3.6K
R10 390
R8 2.4K
L8 15.0
C9 0.01
C12 0.01
L9 1.0
Q1 MPS918
R11 470
C13 10 pF
R9 390
C11 0.01
C10 0.01
C15 0.01
L10 15
C14 0.01
U1 74S113
+12
R12 100
C16 300 pF
L12 3.3
L14 5.6
L16 3.3
C20 300 pF
C17 390 pF
L11 0.44
L13 0.44
C18 180 pF
L15 0.44
C19 180 pF
L17 0.44
C21 390 pF
E5
+5 VDC
E3
E8
19.25-
20.25 MHz
+2 dBm
A4A4 BCD - 8 NO. 1
MIXER
43.75 - 45 MHz
AMPLIFIER
E6
64/65 MHz
(+7 dBm)
FROM A4A10-E4
E2
E1
L1 0.47
R2 68
M1
R5 18
C3 68 pF
L3 3.3
L5 5.6
L7 3.3
C7 68 pF
C1 100 pF
C2 100 pF
R1 100
R3 100
R4 300
R6 300
C4 180 pF
L2 0.27
L4 0.27
C5 47 pF
L6 0.27
C6 47 pF
L8 0.27
C8 180 pF
E5
R7 3.6K
R10 390
R8 2.4K
Q1 MPS918
C12 0.01
C9 0.01
C10 0.01
C11 0.01
R9 390
L9 5.6
E7
GND
E3
43.75/45 MHz
(2 dBm)
TO A4A5-E1
E4
12 VDC

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-27. BCD Boards A4A1-A4A4, Schematic Diagram

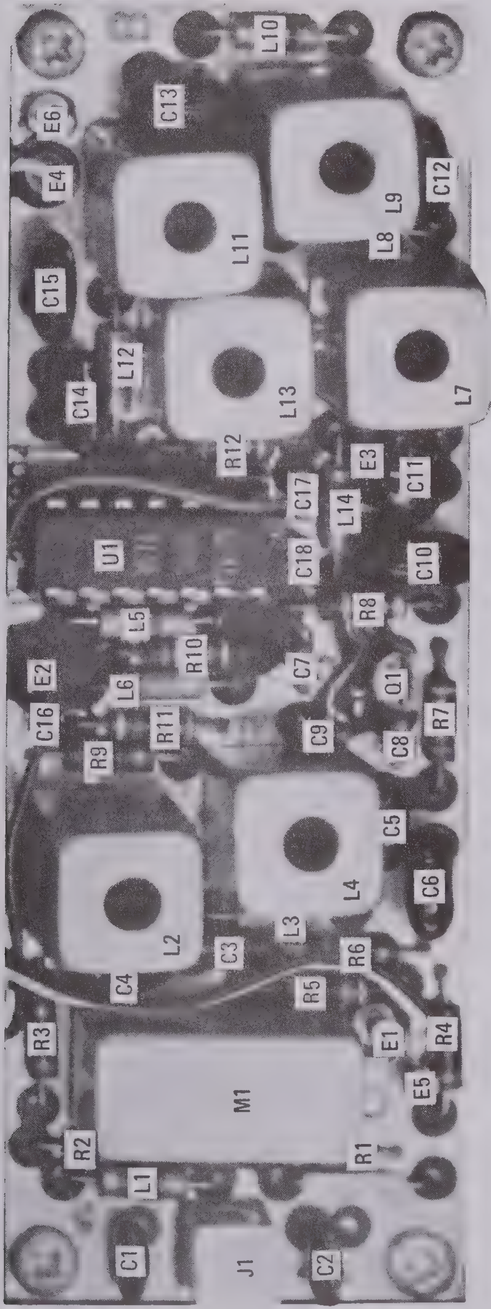

L10
C13
E6
E4
C12
L9
L8
L11
C15
L12
L13
L7
C14
R12
E3
C17
L14
C11
U1
C18
C10
L5
R8
R10
C7
E2
Q1
L6
C16
R11
C9
R7
C8
R9
C5
L4
C6
L2
L3
C3
R6
C4
R5
R3
E1
R4
E5
M1
R2
R1
L1
C1
J1
C2

Figure 6-28. BCD-1 Board A4A1, Component Locations

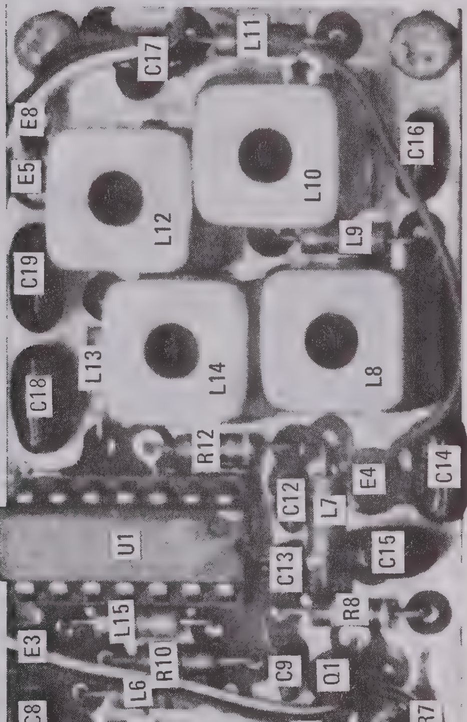

C17
L11
E8
E5
L12
L10
C16
L9
C19
L13
L14
L8
C18
R12
E4
C14
C12
L7
U1
C15
C13
R8
L15
E3
R10
C9
Q1
L6
C8
R7
R11
C11
C10
R9
C6
L5
C7
L3
L4
R6
C4
C5
R5
R3
E2
R4
M1
E7
E6
R2
R1
L1
L2
C1
E1
C2
C3

Figure 6-29. BCD-2 Board A4A2, Component Locations

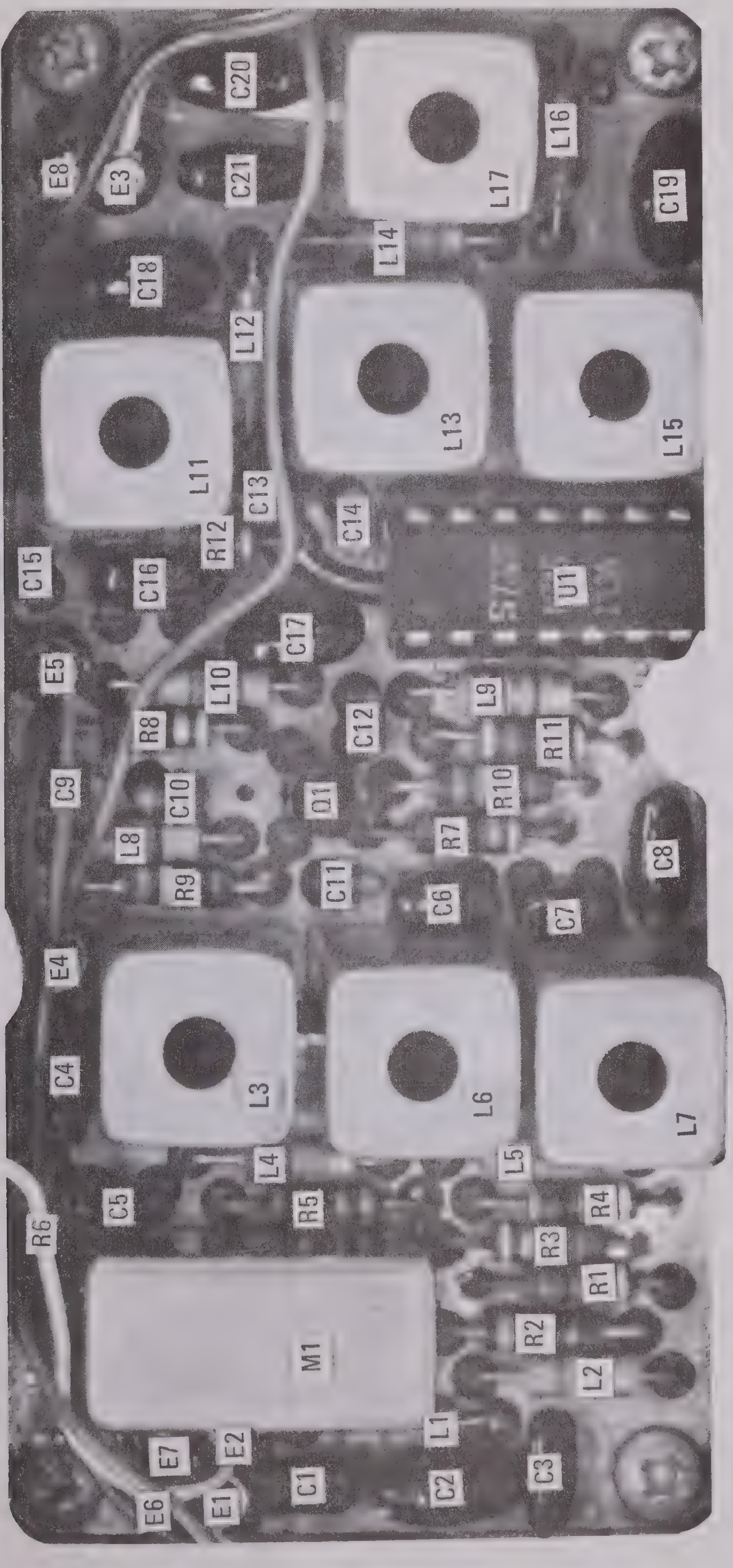

C20
E8
E3
C21
L17
L16
C19
L14
C18
L12
L11
L13
L15
C13
R12
C14
C15
C16
U1
C17
E5
L10
L9
R8
C12
R11
C9
C10
Q1
R10
L8
R7
C8
R9
C11
C6
C7
E4
C4
L3
L6
L7
L4
L5
C5
R5
R4
R6
R3
R1
M1
R2
L2
L1
E7
E2
C1
C2
C3
E6
E1

Figure 6-30. BCD-4 Board A4A3, Component Locations

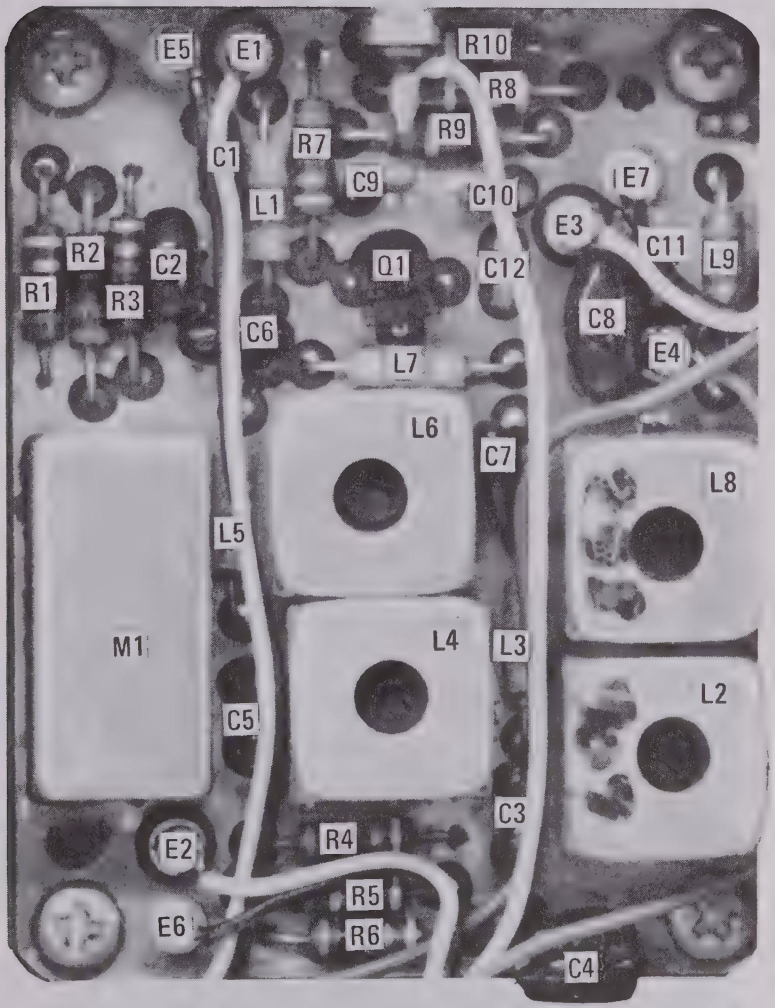
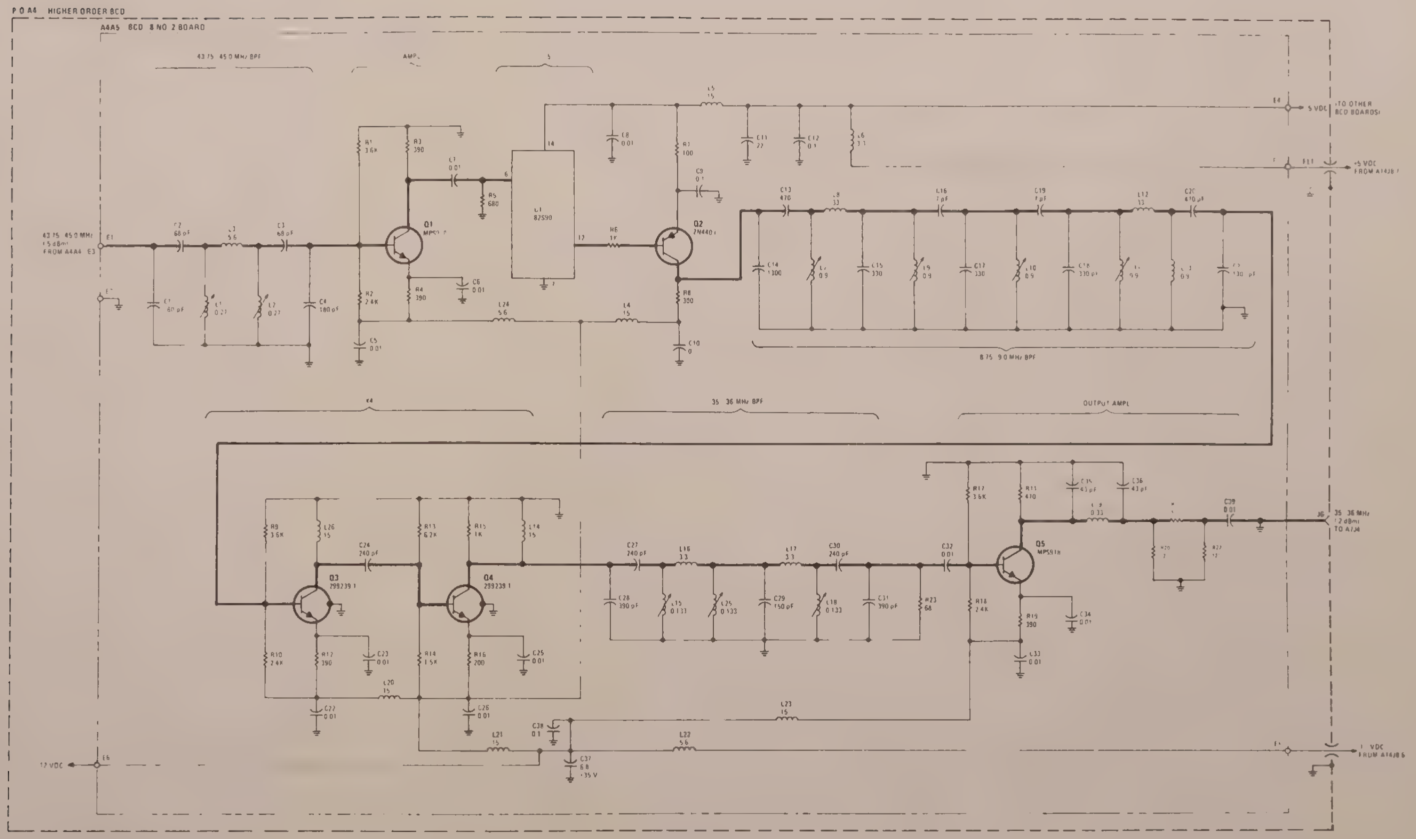

E5
E1
R10
R8
R9
R7
C1
C9
C10
E7
L1
E3
C11
L9
R2
C2
Q1
C12
R1
R3
C8
C6
E4
L7
L6
C7
L8
L5
M1
L4
L3
L2
C5
C3
R4
E2
R5
E6
R6
C4

Figure 6-31. BCD-8-1 Board A4A4, Component Locations


P O A4 HIGHER ORDER BCD
A4A5 BCD 8 NO 2 BOARD
43 75 45 0 MHz BPF
AMPL
5
43 75 45 0 MHz
15 dBm
FROM A4A4 E3
E1
C1 60 pF
C2 68 pF
L3 56
C3 68 pF
L1 0 27
L2 0 27
C4 180 pF
R1 3 6K
R2 2 4K
R3 390
R4 390
C5 0 01
C6 0 01
C7 0 01
R5 680
Q1
U1 82S90
14
6
12
7
R6 1K
C8 0 01
L5 15
R7 100
C9 0 1
C10 0
C11 22
C12 0 1
L6 3 3
Q2 2N4401
R8 300
L24 5 6
L4 15
C13 470
L8 33
L16 7 pF
C19 1 pF
L12 33
C20 470 pF
C14 1100
L7 0 9
C15 330
L9 0 9
C17 330
L10 0 9
C18 330 pF
L11 0 9
L13 0 9
C21 130 pF
8 75 9 0 MHz BPF
E4
5 VDC
TO OTHER BCD BOARDS
FL1
+5 VDC FROM A14J8
X4
35 36 MHz BPF
OUTPUT AMPL
R9 3 6K
L26 15
C24 240 pF
Q3 299239 1
R10 2 4K
R12 390
C23 0 01
R13 6 2K
R15 1K
L14 15
Q4 299239 1
R14 1 5K
R16 200
C25 0 01
L20 15
C22 0 01
C26 0 01
C27 240 pF
L16 3 3
L17 3 3
C30 240 pF
C28 390 pF
L15 0 133
L25 0 133
C29 150 pF
L18 0 133
C31 390 pF
R23 68
C32 0 01
R17 3 6K
R11 470
C35 43 pF
C36 43 pF
L19 0 33
C39 0 01
Q5 MPS918
R18 2 4K
R19 390
C34 0 01
C33 0 01
R20
R22
J6
35 36 MHz
12 dBm
TO A1J4
L23 15
C38 0 1
L21 15
L22 5 6
C37 6 8 35 V
E6
12 VDC
VDC FROM A14J8 6

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN µF
INDUCTOR VALUES ARE IN µH

Figure 6-32. BCD-8 Board A4A5, Schematic Diagram

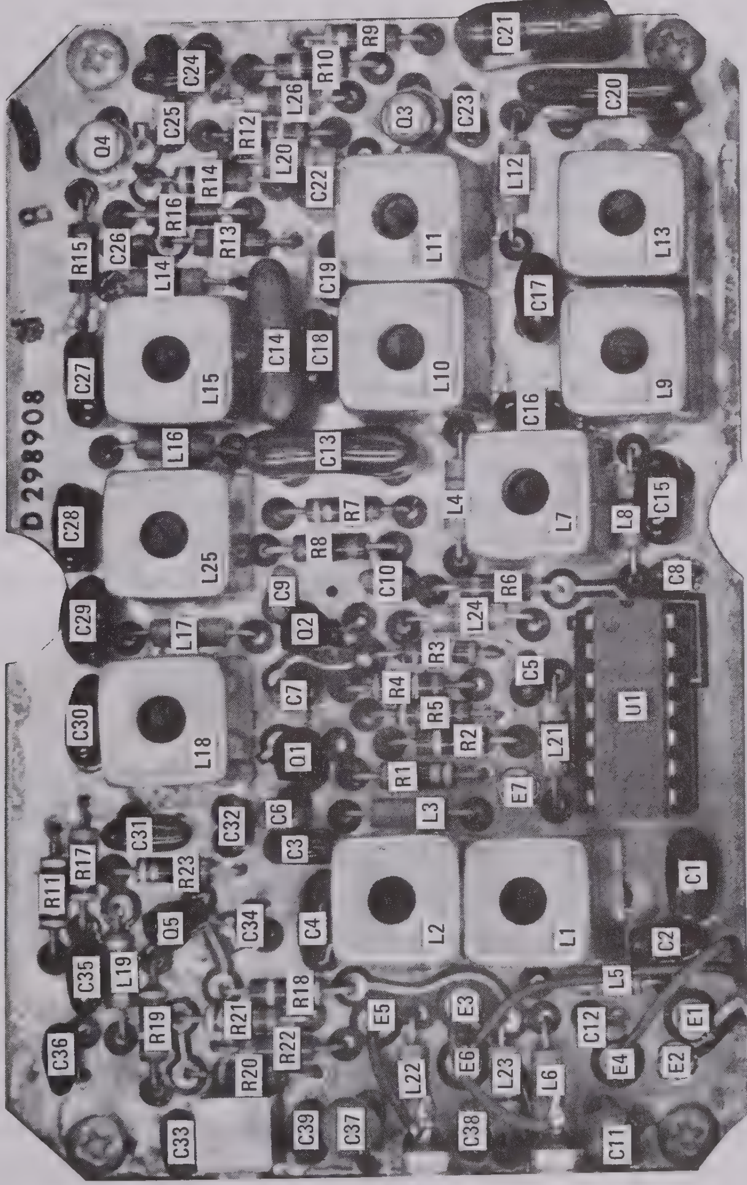

D 298908
C21
R9
C24
R10
C20
L26
Q3
C23
C25
R12
Q4
L20
L12
R14
C22
L11
L13
R16
R13
C26
R15
L14
C19
C17
C14
C18
L15
L10
L9
C27
C16
L16
C13
L4
C28
C15
L25
R7
L7
L8
R8
C9
C10
R6
C8
C29
L24
L17
Q2
R3
C5
C7
R4
C30
R5
U1
L18
Q1
R2
L21
R1
E7
C6
C32
L3
C31
R17
C3
R23
C1
R11
Q5
C34
C4
L2
L1
C2
C35
L19
R18
L5
R21
E5
E3
C12
E1
C36
R19
R22
E6
E4
E2
R20
L22
L23
L6
C33
C39
C37
C38
C11

Figure 6-33. BCD-8-2 Board A4A5, Component Locations

6-100. **35/35.5 MHz BOARD A5A6.** (Figures 6-46 and 6-48). The third reference frequency required by the BCD section is 35.0 or 35.5 MHz. The output frequency is switched with a TTL compatible command at A5FL3. A logic 0 ($\leq$ 0.8V) produces 35.0 MHz and a logic 1 ($\geq$ 2.2V) produces 35.5 MHz at the output. With A5FL3 at logic 0, 10 MHz from fixed frequency input board A5A1 is amplified in saturated transistor Q1 which generates a 10-MHz signal rich in harmonics. The seventh harmonic (70 MHz) is selected by the 70-MHz bandpass filter C2, C5, C6, L1, and L2. This 70-MHz signal is amplified by amplifier Q2 and filtered by 70-MHz bandpass filter L3, C9, C10, L5, and C11. The 70 MHz signal is applied to driver stage Q3 which drives U1. U1 is a Schottky TTL flip-flop operating as a frequency divide-by-two circuit producing 35 MHz. The 35 MHz passes through RF switch Q8 and CR1. Q8 is conducting and CR1 is reverse biased so that the 35-MHz signal appears as an output at A5J6. Note that U2 and the second half (U1B) of U1 are inhibited so that no other signals appear at the output.

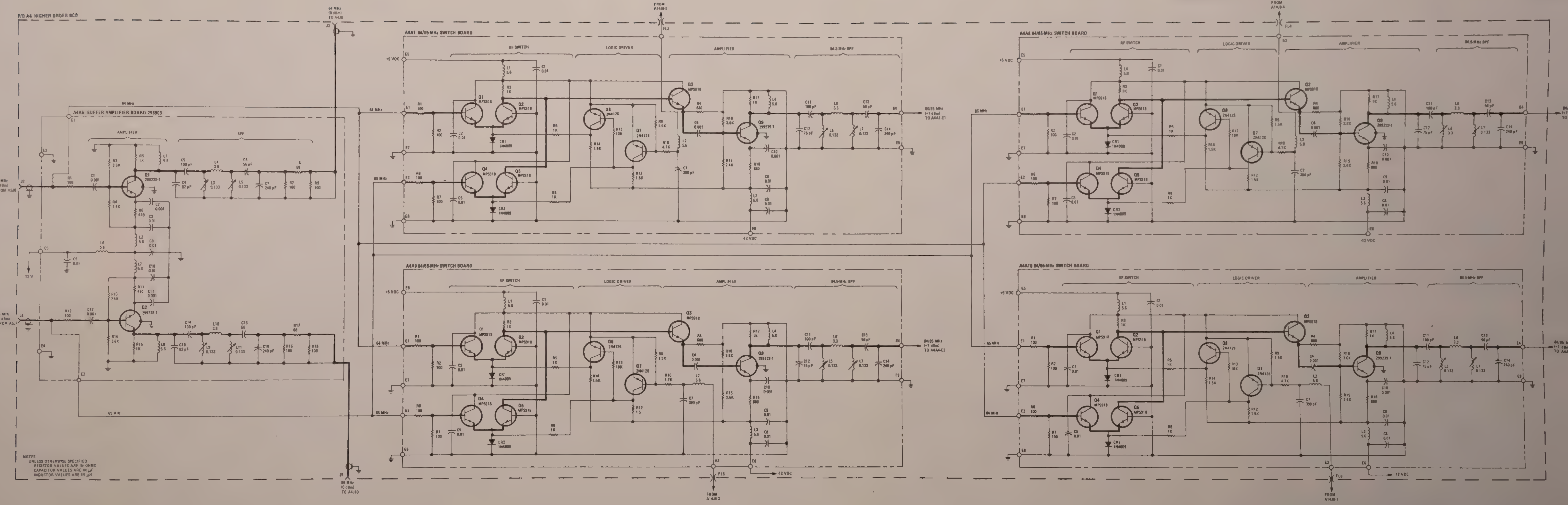
6-101. To generate 35.5 MHz, 64 MHz from A5A5 is amplified and isolated by amplifier stages Q4 and Q5 so that a buffered 64-MHz signal is applied to the base of Q6. A5FL3 is set at a logic 1 level which enables U2 and both halves of U1. A logic 1 also turns on CR1 and turns off Q8 so that 35 MHz is isolated from the output by more than 20 dB. The 35 MHz present at U1 pin 5 is connected to pin 8 of U2 which is a Schottky BCD counter functioning as a frequency divide-by-ten circuit producing a 3.5 MHz at U2 pin 12. This 3.5 MHz signal is applied to the base of Q6 alone with the 64-MHz signal causing Q6 to act as a mixer generating sum and difference products at 64 MHz and 3.5 MHz and its harmonics. The 71-MHz product is selected by 71-MHz bandpass filter L10, L11, C29, C30, and C31. This 71-MHz signal is amplified by driver Q7 and applied to U1 pin 13. U1 is a frequency divide-by-two circuit generating the required 35.5 MHz. The output of A5A6 is -5 dBm and all sidebands, not harmonically related, are 20 dB below the carrier. The output signal provides the input reference for the lower order BCD assembly.

6-102. **POWER SUPPLY A11.**

6-103. The power supply is a series-regulated, constant-voltage, low-noise supply. It has dual outputs of +5.2 Vdc at 5 amps and -12 Vdc at 4.5 amps. The power supply consists of four sections: most of rear panel assembly A12, part of front panel assembly A1, filter/regulator boards A11A1 through A11A3, and part of logic and power distribution board A14. The following paragraphs describe each of the sections that make up the power supply.

6-104. **REAR PANEL ASSEMBLY A12.** (Figures 6-49 and 6-50).

6-105. The input line voltage passes through the concom line filter/fuse/voltage selector. The built-in 120/240V selector and fuse allow protected operation on either of these line voltages. The hot line connection to the fan is passed through a relay on A12A1. When the system is on, the fan off input of this board is floating and the relay contact is in its normally closed position, thus applying ac power to the fan. When the system is switched to STBY, -12 volt is present, activating the relay and opening it's contact. At the output of transformer T1 are two 0.1 μF capacitors (C1 and C2) which are used to suppress transients. The transformer outputs are wired to two full wave bridges (BR1 and BR2) mounted on a heat sink of the rear panel. Transistors Q1 and Q2 are also located on the heat sink. An auxiliary power supply is


P/O A4 HIGHER ORDER BCD
64 MHz (0 dBm) TO A4J8
J3
A4A6 BUFFER AMPLIFIER BOARD 298909
AMPLIFIER
BPF
64 MHz (0 dBm) FROM A5J6
J2
Q1 299239-1
Q2 299239 1
65 MHz (0 dBm) FROM A5J7
J4
12 V
J5
65 MHz (0 dBm) TO A4J10
FROM A14J8-5
FL3
A4A7 64/65-MHz SWITCH BOARD
RF SWITCH
LOGIC DRIVER
AMPLIFIER
64.5-MHz BPF
+5 VDC
64 MHz
65 MHz
-12 VDC
64/65 MHz (+7 dBm) TO A4A1-E1
A4A9 64/65-MHz SWITCH BOARD
64/65 MHz (+7 dBm) TO A4A4-E2
FROM A14J8 3
FL5
FROM A14J8-4
FL4
A4A8 64/65-MHz SWITCH BOARD
64/65 MHz (+7 dBm) TO A4A2 E2
A4A10 64/65-MHz SWITCH BOARD
64/65 MHz (+7 dBm) TO A4A4-E2
FROM A14J8 1
FL6
12 VDC

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-34. Buffer Amplifier A4A6 and 64/65 MHz Switches A4A7-A4A10, Schematic Diagram

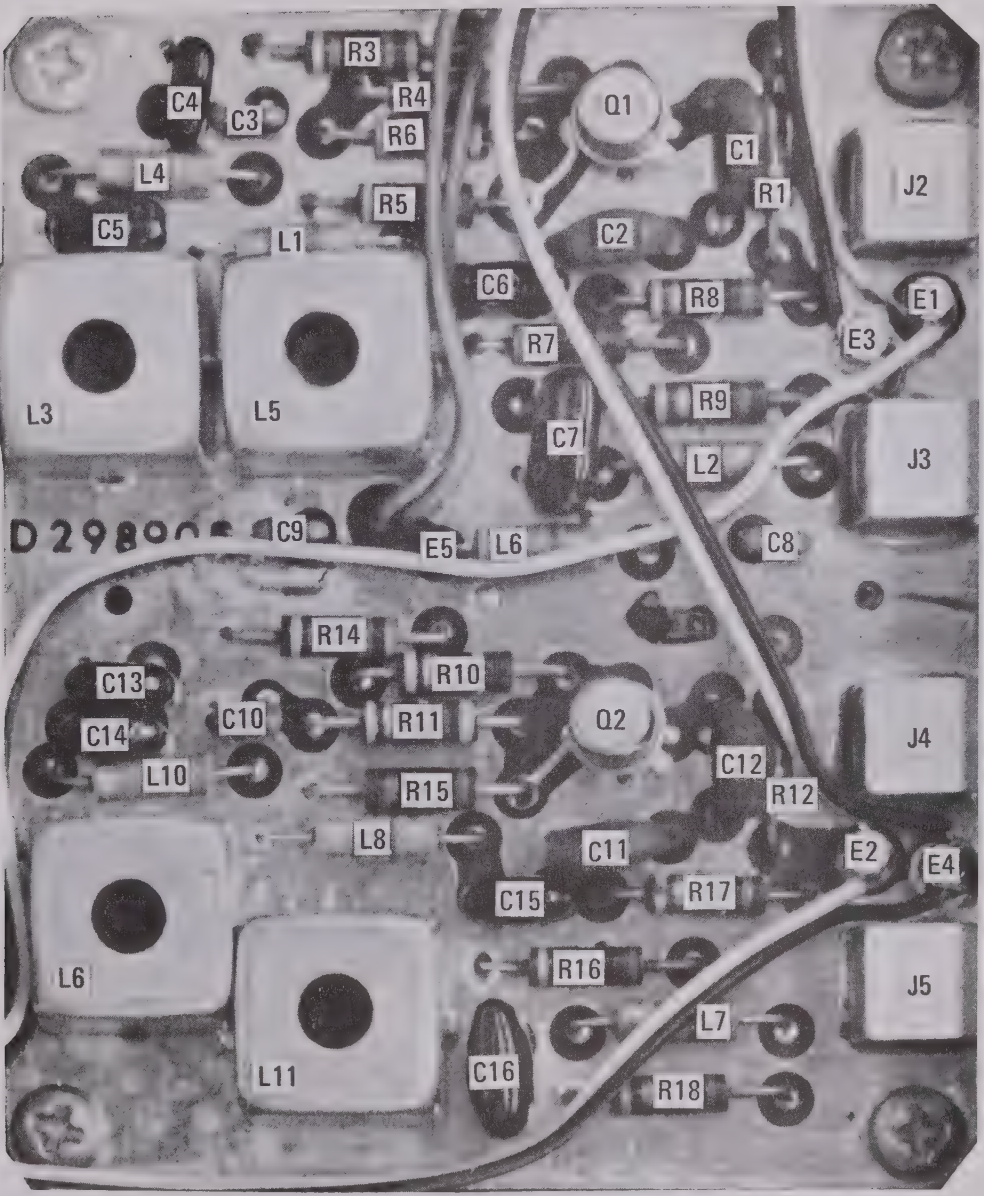

R3
C4
C3
R4
Q1
R6
C1
L4
J2
R5
R1
C5
L1
C2
C6
R8
E1
R7
E3
R9
L3
L5
C7
J3
L2
D298
C9
E5
L6
C8
R14
R10
C13
C10
R11
Q2
J4
C14
C12
L10
R15
R12
L8
C11
E2
E4
C15
R17
L6
R16
J5
L7
L11
C16
R18

Figure 6-35. Buffer Amplifier A4A6, Component Locations

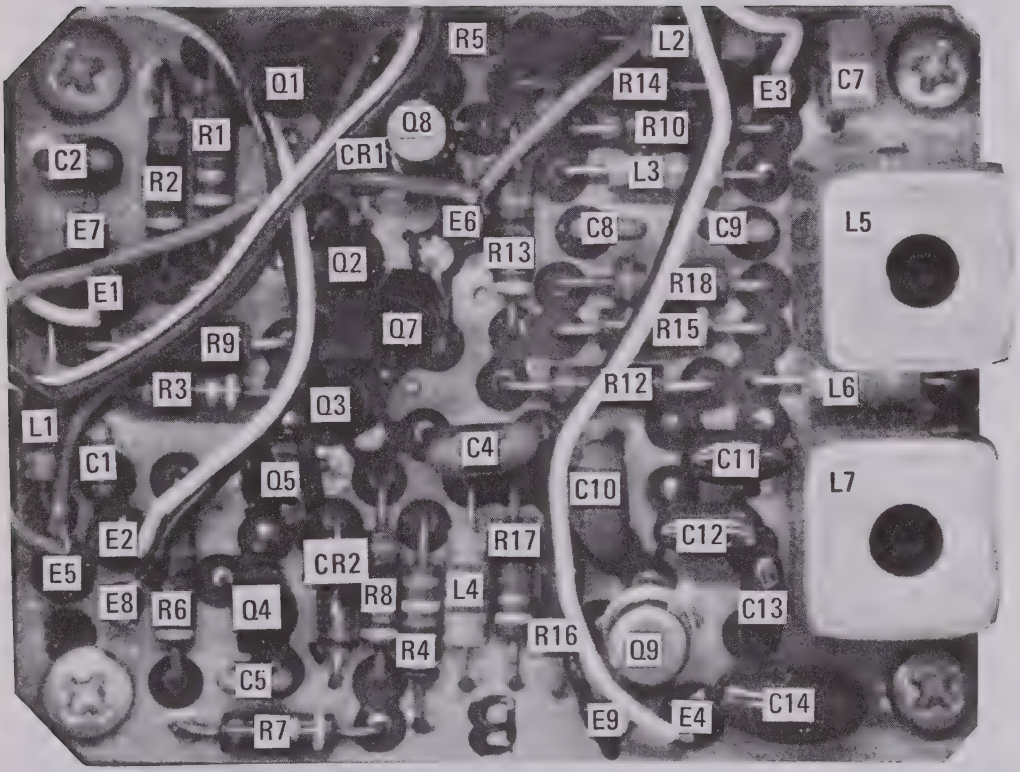
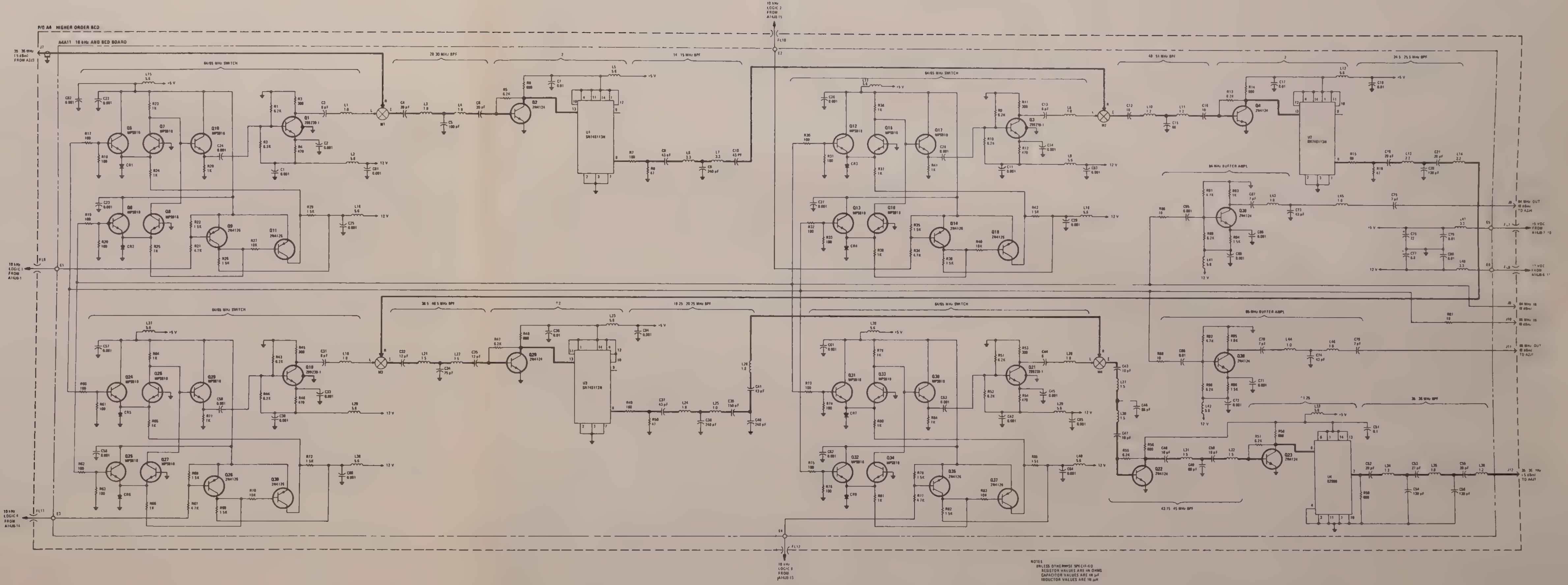

R5
L2
Q1
R14
E3
C7
Q8
R10
R1
CR1
C2
L3
R2
E6
C8
C9
L5
E7
R13
Q2
R18
E1
Q7
R15
R9
R12
R3
L6
Q3
L1
C4
C11
C1
L7
Q5
C10
E2
C12
R17
CR2
E5
R8
L4
E8
R6
Q4
C13
R16
R4
Q9
C5
C14
R7
E9
E4

Figure 6-36. 64/65 MHz Switches A4A7-A4A10, Component Locations


P/O A4 HIGHER ORDER BCD
A4A11 10 kHz AND BCD BOARD
35 36 MHz 15 dBm FROM A2J3
J7
64/65 MHz SWITCH
28 30 MHz BPF
14 15 MHz BPF
U1 SN74S113N
10 kHz LOGIC 1 FROM A14J8-1
10 kHz LOGIC 2 FROM A14J8-15
FL10
E2
40 51 MHz BPF
24.5 25.5 MHz BPF
U2 SN74S113N
84 MHz BUFFER AMPL
84 MHz OUT 10 dBm TO A2J4
+5 VDC FROM A14J8-7
12 VDC FROM A14J8-6
38.5 40.5 MHz BPF
18.25 20.25 MHz BPF
U3 SN74S113N
85 MHz BUFFER AMPL
85 MHz OUT 10 dBm TO A2J1
43.75 45 MHz BPF
35 36 MHz BPF
U4
35 36 MHz +5 dBm TO A4J1
J12
10 kHz LOGIC 3 FROM A14J8-13
10 kHz LOGIC 4 FROM A14J8-14
FL11
FL12
E3
E4
12 V
+5 V

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-37. 10 kHz BCD Board A4A11, Schematic Diagram

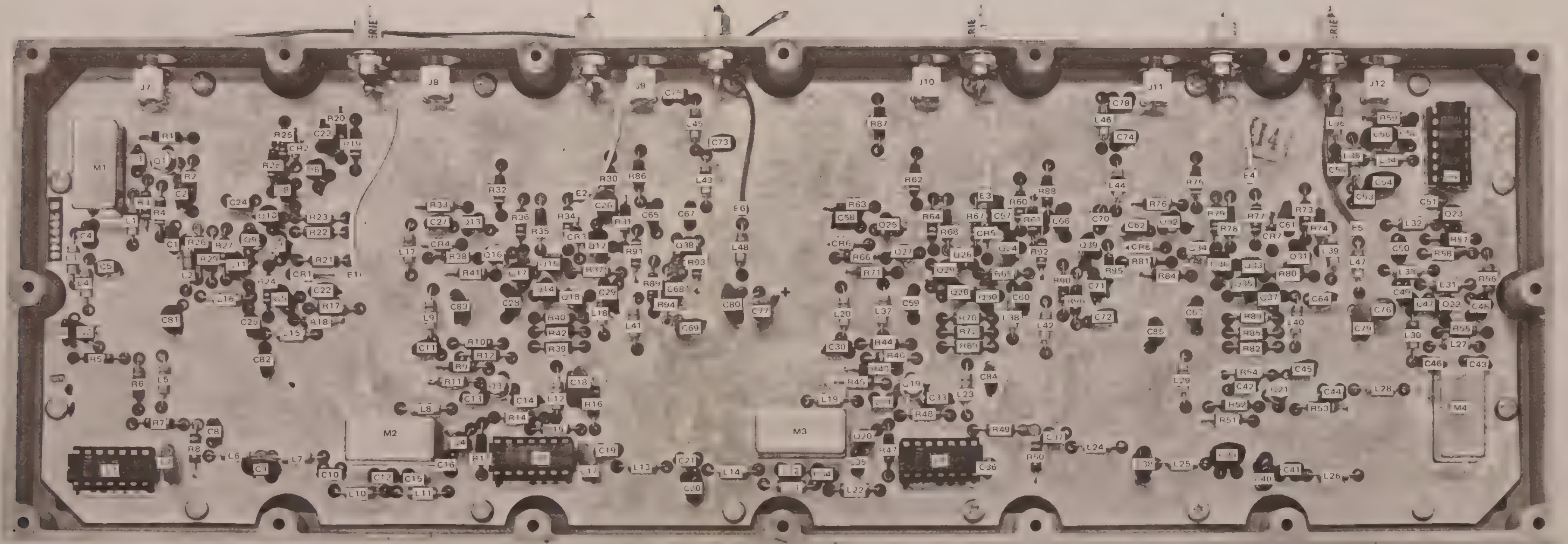

J7
J8
J9
C75
J10
J11
J12
C78
L46
C74
R87
R1
R25
C23
R20
R19
CR2
M1
Q1
R2
C2
R3
R4
C24
Q10
R23
R22
C4
L1
C1
R26
R27
Q9
R21
C5
R29
L2
CR1
E1
L4
R24
C22
L16
R17
C81
C25
R18
L15
C82
R5
R6
L5
R7
C8
R8
L6
L7
C10
L10
C12
C15
L11
M2
C16
R11
L8
C13
R14
C14
L12
R16
C18
R10
R12
R9
C11
L9
C83
R33
C27
Q13
CR4
R38
R41
L17
R32
R36
R35
Q16
C28
R30
R86
E2
C26
R34
CR3
Q12
R31
C65
C67
L45
C73
L43
E6
L48
R91
Q38
R93
R89
C68
R94
C69
C80
C77
C29
Q18
R40
R42
R39
L18
L41
C19
L13
C21
C20
L14
M3
C58
R63
Q25
CR6
R66
R71
Q27
R62
R64
R68
E3
R67
C57
CR5
R60
R88
C66
C70
Q26
Q29
Q28
R70
C60
Q30
L38
C59
L20
L37
C30
R44
R46
R45
L19
Q19
C33
L23
C84
R48
R49
R47
Q20
L22
C36
R50
C37
L24
L25
C41
L26
R92
R90
C71
R95
L44
R75
R76
E4
C62
Q32
CR8
R81
R84
L42
C72
C85
Q34
R79
R77
R78
CR7
C61
R73
R74
Q33
R80
Q31
C64
L39
R83
R85
R82
L40
L29
R54
C42
C45
Q21
R52
R51
R53
C44
L28
L47
E5
C76
C79
L30
C46
C43
M4
L27
R55
Q22
C47
C49
L31
R56
C48
R57
R58
C50
L32
Q23
C51
C54
C53
R59
C56
L36

Figure 6-38. 10 kHz BCD Board A4A11, Component Locations

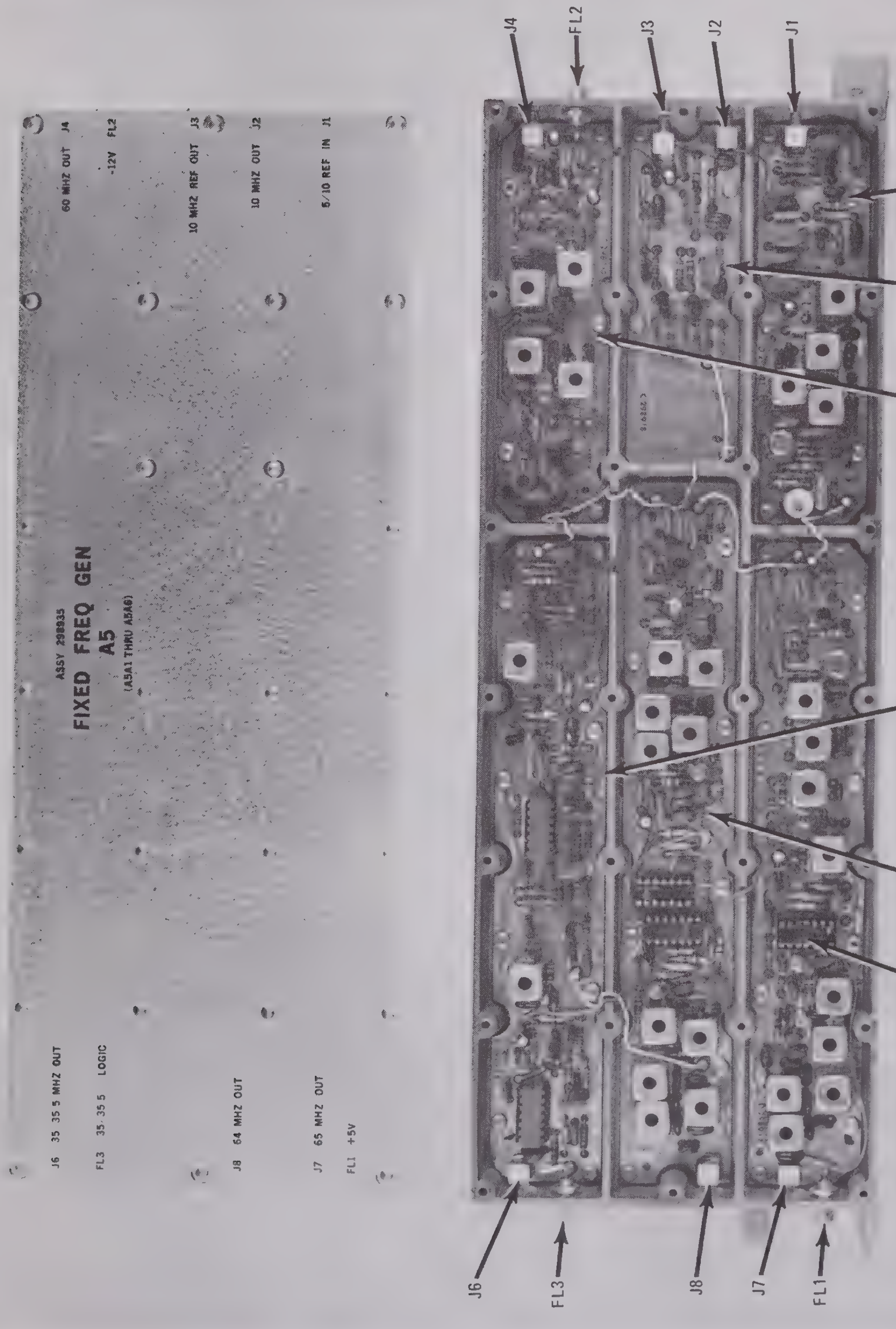
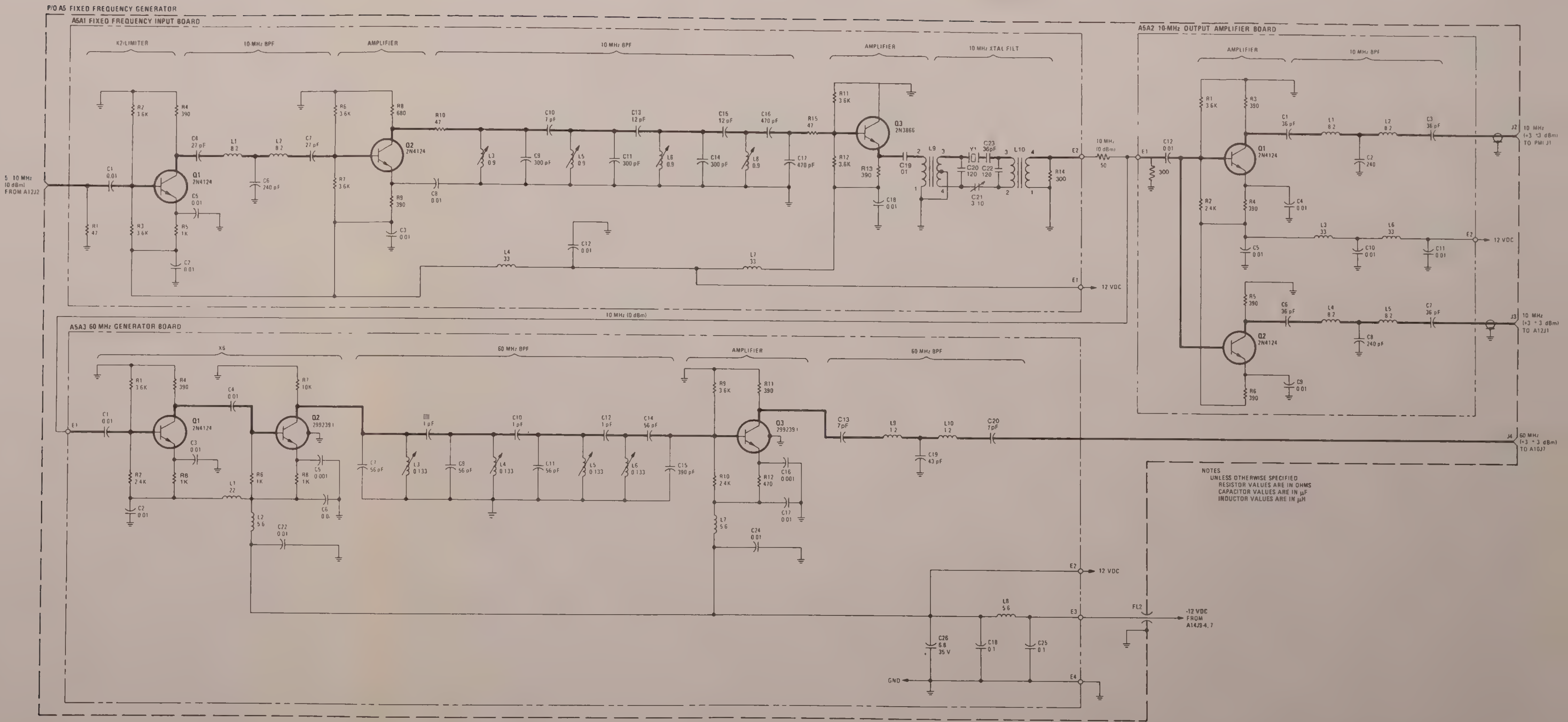

J4
FL2
J3
J2
J1
A1
A2
A3
A6
A5
A4
J6
FL3
J8
J7
FL1
60 MHZ OUT J4
-12V FL2
10 MHZ REF OUT J3
10 MHZ OUT J2
5/10 REF IN J1
ASSY 298935
FIXED FREQ GEN
A5
(A5A1 THRU A5A6)
J6 35 35.5 MHZ OUT
FL3 35-35.5 LOGIC
J8 64 MHZ OUT
J7 65 MHZ OUT
FL1 +5V

Figure 6-39. Fixed Frequency Generator A5


P/O A5 FIXED FREQUENCY GENERATOR
A5A1 FIXED FREQUENCY INPUT BOARD
X2/LIMITER
10 MHz BPF
AMPLIFIER
10 MHz BPF
AMPLIFIER
10 MHz XTAL FILT
5 10 MHz
(0 dBm)
FROM A12J2
C1 0.01
R1 47
R2 3.6K
R3 3.6K
R4 390
R5 1K
C5 0.01
C7 0.01
Q1 2N4124
C4 27 pF
L1 8.2
L2 8.2
C6 240 pF
C7 27 pF
R6 3.6K
R7 3.6K
R8 680
R9 390
C3 0.01
C8 0.01
Q2 2N4124
R10 47
C10 7 pF
L3 0.9
C9 300 pF
L5 0.9
C13 12 pF
C11 300 pF
L6 0.9
C15 12 pF
C14 300 pF
C16 470 pF
L8 0.9
C17 470 pF
R15 47
L4 33
C12 0.01
L7 33
R11 3.6K
R12 3.6K
Q3 2N3866
R13 390
C18 0.01
C19 01
L9
Y1
C23 36pF
C20 120
C22 120
C21 3 10
L10
R14 300
E2
E1
12 VDC
10 MHz (0 dBm)
10 MHz
(0 dBm)
50
A5A2 10-MHz OUTPUT AMPLIFIER BOARD
AMPLIFIER
10 MHz BPF
E1
300
C12 0.01
R1 3.6K
R2 2.4K
R3 390
R4 390
C4 0.01
Q1 2N4124
C1 36 pF
L1 8.2
L2 8.2
C3 36 pF
C2 240
J2 10 MHz
(+3 ±3 dBm)
TO PMI J1
L3 33
L6 33
C5 0.01
C10 0.01
C11 0.01
E2
12 VDC
R5 390
C6 36 pF
L4 8.2
L5 8.2
C7 36 pF
C8 240 pF
Q2 2N4124
R6 390
C9 0.01
J3 10 MHz
(+3 ±3 dBm)
TO A12J1
A5A3 60 MHz GENERATOR BOARD
X6
60 MHz BPF
AMPLIFIER
60 MHz BPF
E1
C1 0.01
R1 3.6K
R2 2.4K
R4 390
R8 1K
C2 0.01
Q1 2N4124
C3 0.01
C4 0.01
L1 22
R6 1K
L2 5.6
C22 0.01
R7 10K
R8 1K
Q2 2992391
C5 0.001
C6 0.0
C7 56 pF
1 pF
L3 0.133
C9 56 pF
C10 1 pF
L4 0.133
C11 56 pF
C12 1 pF
L5 0.133
C14 56 pF
L6 0.133
C15 390 pF
R9 3.6K
R10 2.4K
R11 390
R12 470
Q3 2992391
C16 0.001
C17 0.01
L7 5.6
C24 0.01
C13 7pF
L9 1.2
L10 1.2
C20 7pF
C19 43 pF
J4 60 MHz
(+3 ±3 dBm)
TO A10J7
E2
12 VDC
L8 5.6
E3
FL2
-12 VDC
FROM
A14J9-4, 7
C26 6.8 35 V
C18 0.1
C25 0.1
GND
E4

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-40. Fixed Frequency Input Board A5A1, 10 MHz Output Amplifier A5A2, and 60 MHz Generator A5A3, Schematic Diagram

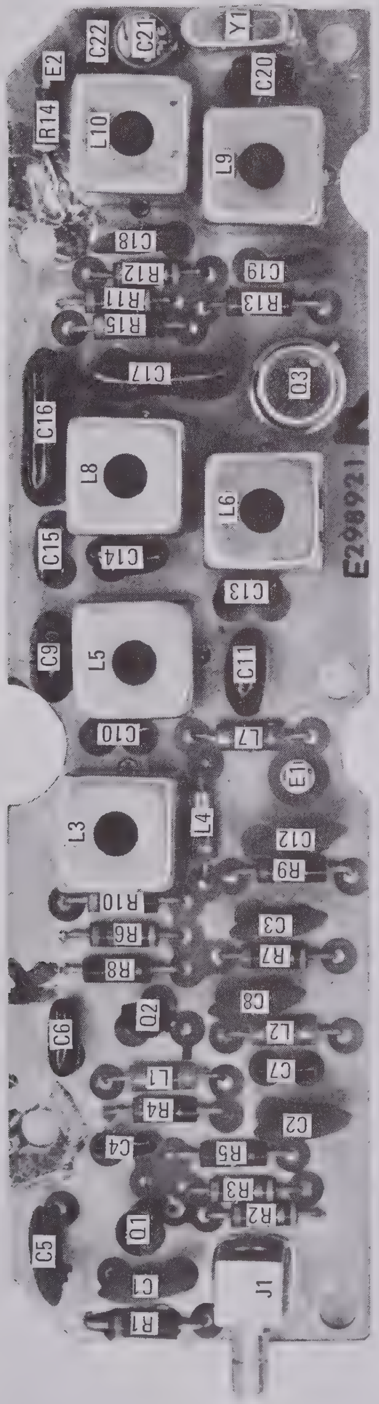

Y1
C22
C21
E2
C20
R14
L10
L9
C18
R12
C19
R11
R13
R15
C17
Q3
C16
L8
L6
E298921
C15
C14
C13
C9
L5
C11
C10
L7
E1
L3
L4
C12
R9
R10
C3
R6
R7
R8
C8
Q2
C6
L2
L1
C7
R4
C2
C4
R5
R3
R2
Q1
C5
C1
J1
R1

Figure 6-41. Fixed Frequency Input Board A5A1, Component Locations

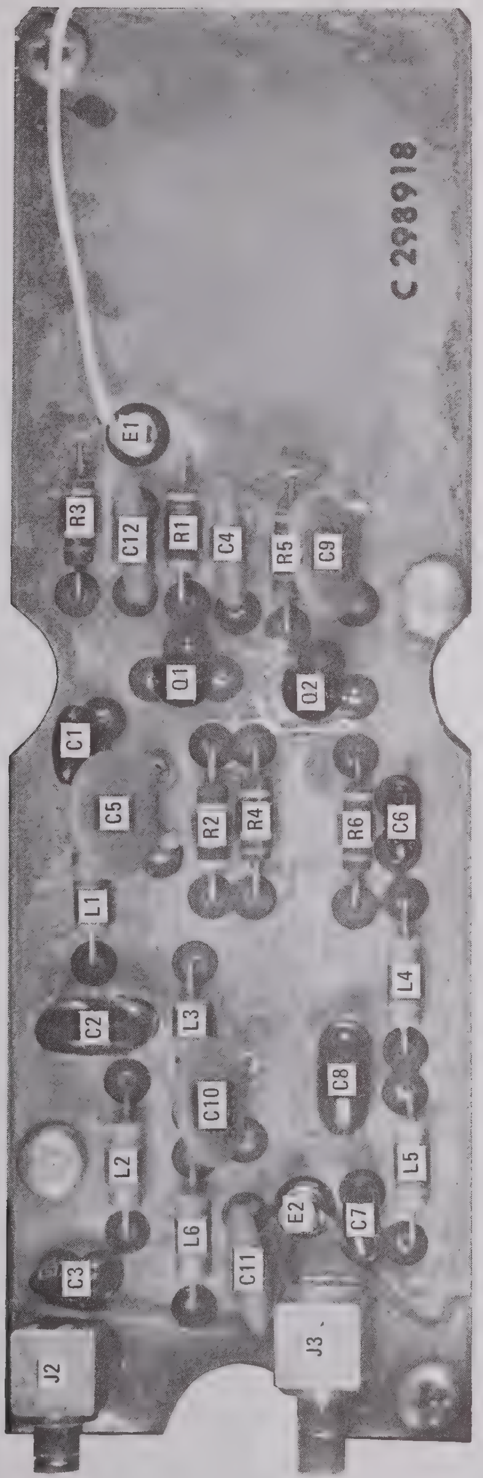

C 298918
E1
R3
C12
R1
C4
R5
C9
Q1
Q2
C1
C5
R2
R4
R6
C6
L1
L3
L4
C2
C8
C10
L2
L5
E2
C7
L6
C11
C3
J2
J3

Figure 6-42. 10 MHz Output Amplifier A5A2, Component Locations

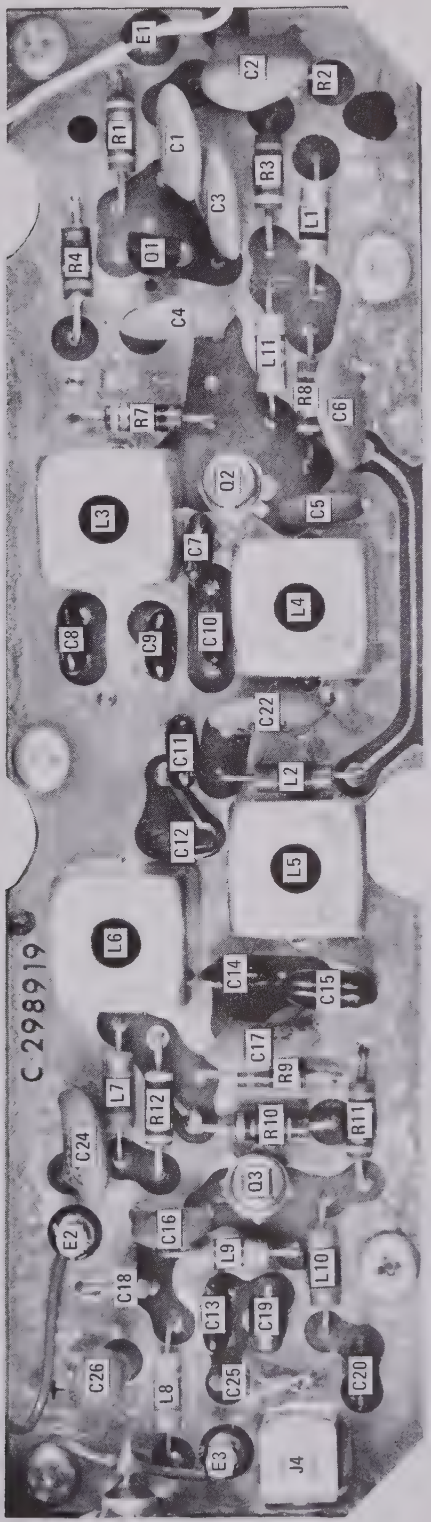

E1
C2
R2
R1
C1
R3
C3
L1
R4
Q1
C4
L11
R8
C6
R7
Q2
C5
L3
C7
L4
C8
C9
C10
C22
C11
L2
C12
L5
L6
C 298919
C14
C15
C17
R9
L7
R12
R10
R11
C24
Q3
E2
C16
L9
L10
C18
C13
C19
C26
L8
C25
C20
E3
J4

Figure 6-43. 60 MHz Generator A5A3, Component Locations

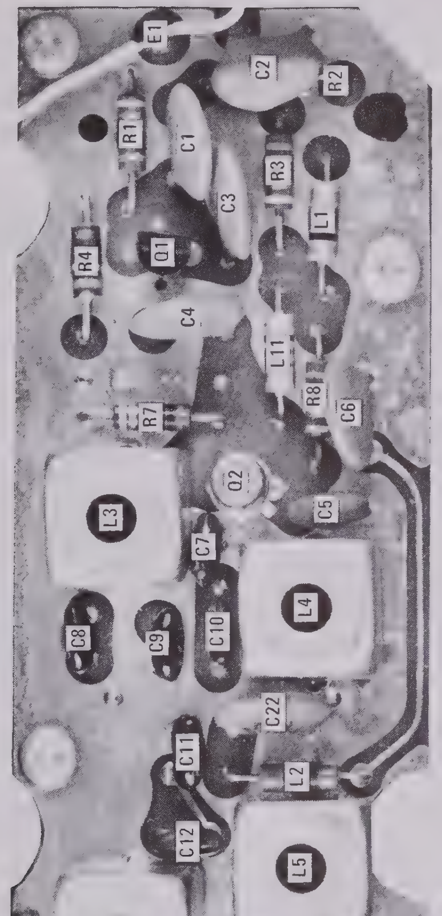
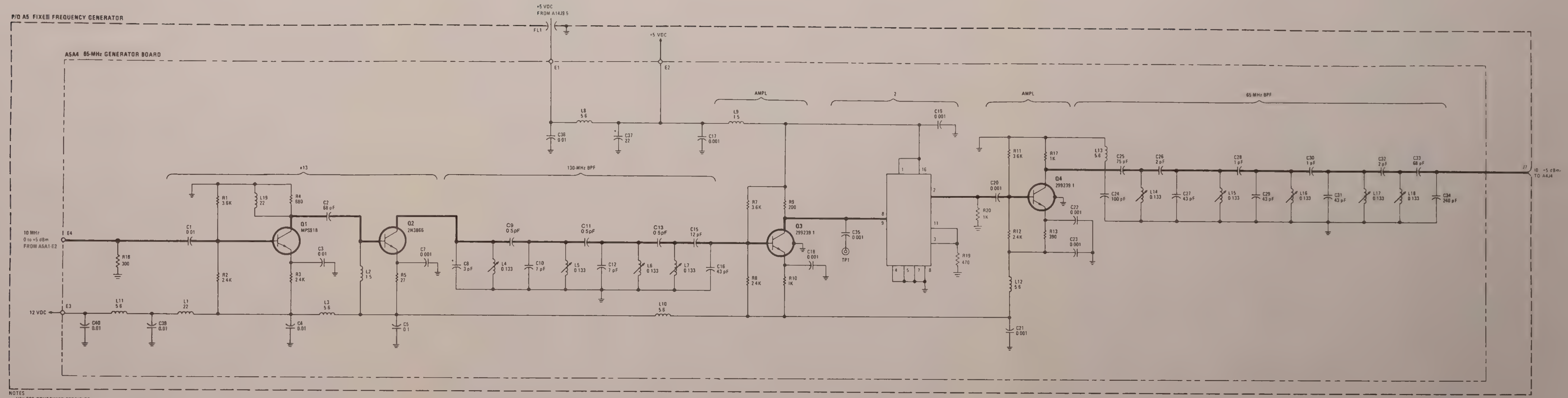

E1
C2
R2
R1
C1
R3
C3
L1
R4
Q1
C4
L11
R8
C6
R7
Q2
L3
C5
C7
L4
C8
C9
C10
C22
C11
L2
C12
L5
L6
C 298919
C14
C15
C17
R9
L7
R12
R10
R11
C24
Q3
E2
C16
L9
L10
C18
C13
C19
C26
L8
C25
C20
E3
J4

Figure 6-43. 60 MHz Generator A5A3, Component Locations


P/O A5 FIXED FREQUENCY GENERATOR
A5A4 65-MHz GENERATOR BOARD
+5 VDC
FROM A14J9 5
FL1
+5 VDC
E1
E2
L8
5.6
C36
0.01
C37
22
C17
0.001
L9
1.5
AMPL
2
C19
0.001
AMPL
65 MHz BPF
×13
130-MHz BPF
10 MHz
0 to +5 dBm
FROM A5A1-E2
E4
R18
300
C1
0.01
R1
3.6K
L19
22
R4
680
C2
68 pF
Q1
MPS918
C3
0.01
R2
2.4K
R3
2.4K
L2
1.5
Q2
2N3866
C7
0.001
R5
27
C8
3 pF
L4
0.133
C9
0.5pF
C10
7 pF
L5
0.133
C11
0.5pF
C12
7 pF
L6
0.133
C13
0.5pF
L7
0.133
C15
12 pF
C16
43 pF
R7
3.6K
R9
200
R8
2.4K
Q3
299239 1
C18
0.001
R10
1K
C35
0.001
TP1
1
16
2
8
9
11
3
4
5
7
8
R19
470
R20
1K
C20
0.001
R11
3.6K
R12
2.4K
L12
5.6
C21
0.001
R17
1K
Q4
299239 1
C22
0.001
R13
390
C23
0.001
L13
5.6
C24
100 pF
C25
75 pF
C26
2 pF
L14
0.133
C27
43 pF
C28
1 pF
L15
0.133
C29
43 pF
C30
1 pF
L16
0.133
C31
43 pF
C32
2 pF
L17
0.133
C33
68 pF
L18
0.133
C34
240 pF
J7
10 +5 dBm
TO A4J4
E3
12 VDC
C40
0.01
L11
5.6
C39
0.01
L1
22
C4
0.01
L3
5.6
C5
0.1
L10
5.6

NOTES
UNLESS OTHERWISE SPECIFIED.
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-44. 65 MHz Generator A5A4, Schematic Diagram

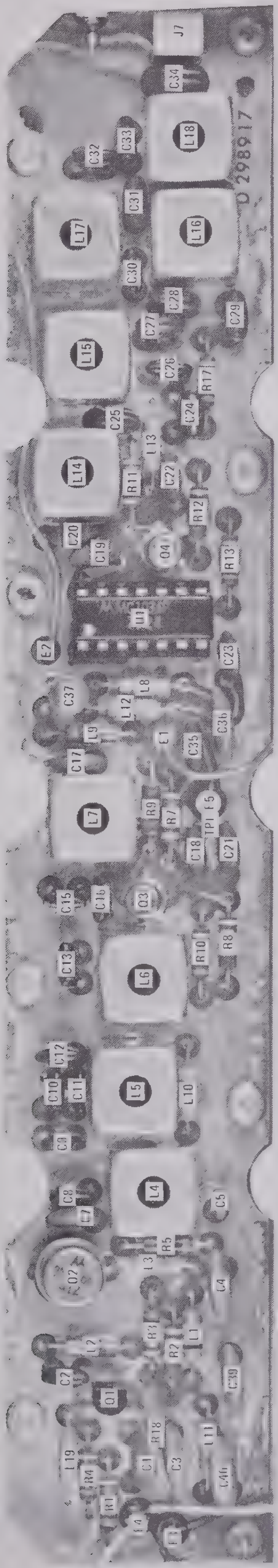

J7
C34
C33
C32
L18
D 298917
C31
L17
L16
C30
C28
C29
C27
L15
C26
R17
C24
C25
L13
L14
R11
C22
R12
C20
C19
Q4
R13
U1
E2
C23
C37
L8
L12
C36
L9
E1
C35
C17
L7
R9
R7
TP1
E5
C18
C21
C15
C16
Q3
C13
R10
R8
L6
C12
C10
C11
L5
L10
C9
C8
L4
C5
Q2
R5
L3
C4
R3
L2
R2
L1
C7
Q1
C39
R18
L11
L19
C1
C3
R4
R1
C40
E4
E3

Figure 6-45. 65 MHz Generator A5A4, Component Locations

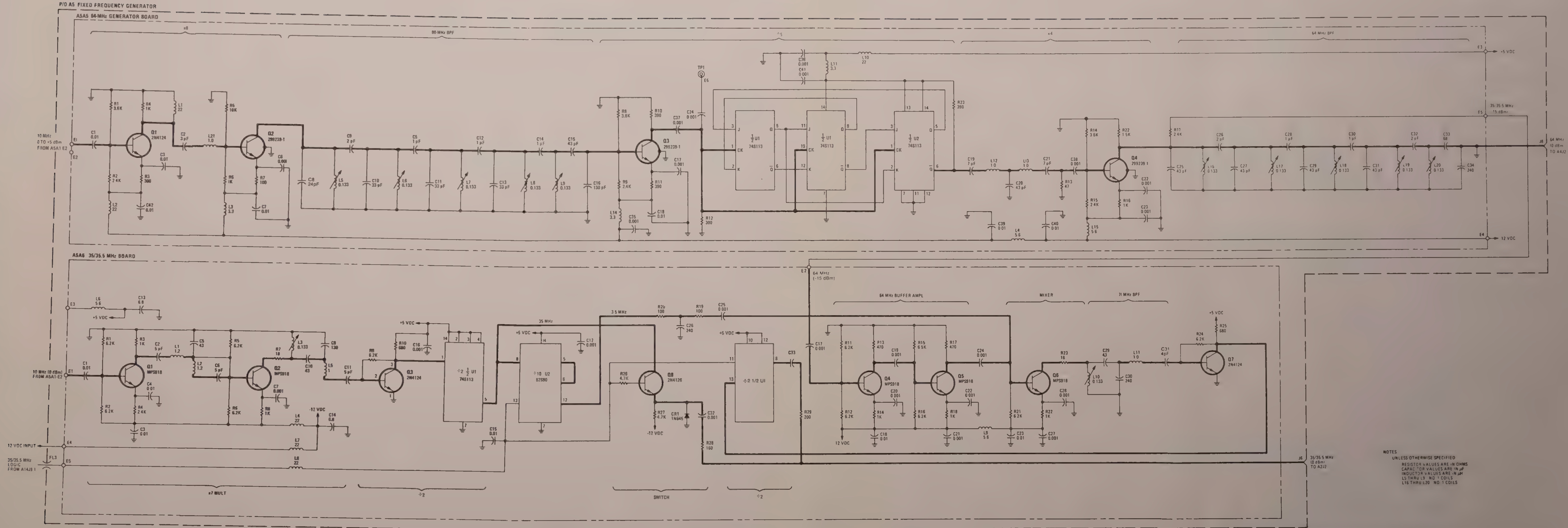

P/O A5 FIXED FREQUENCY GENERATOR
A5A5 64-MHz GENERATOR BOARD
80-MHz BPF
64 MHz BPF
10 MHz 0 TO +5 dBm FROM A5A1 E2
Q1 2N4124
Q2 2N2222
Q3 2N5229-1
Q4 2N5229-1
U1 74S113
U2 74S113
TP1
+5 VDC
12 VDC
35/35.5 MHz
A5A6 35/35.5 MHz BOARD
10 MHz 10 dBm FROM A5A1-E2
12 VDC INPUT
35/35.5 MHz LOGIC FROM A14J3
35 MHz
3.5 MHz
64 MHz (-15 dBm)
64 MHz BUFFER AMPL
MIXER
71 MHz BPF
U2 82S90
CR1 1N645
Q4 MPS918
Q5 MPS918
Q6 MPS918
Q7 2N4124
35/35.5 MHz 12 dBm TO A2J2
+7 MULT
SWITCH
÷2
NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN µF
INDUCTOR VALUES ARE IN µH
L5 THRU L9 NO 1 COILS
L16 THRU L20 NO 1 COILS

Figure 6-46. 64 MHz Generator A5A5 and 35/35.5 MHz Board A5A6, Schematic Diagram

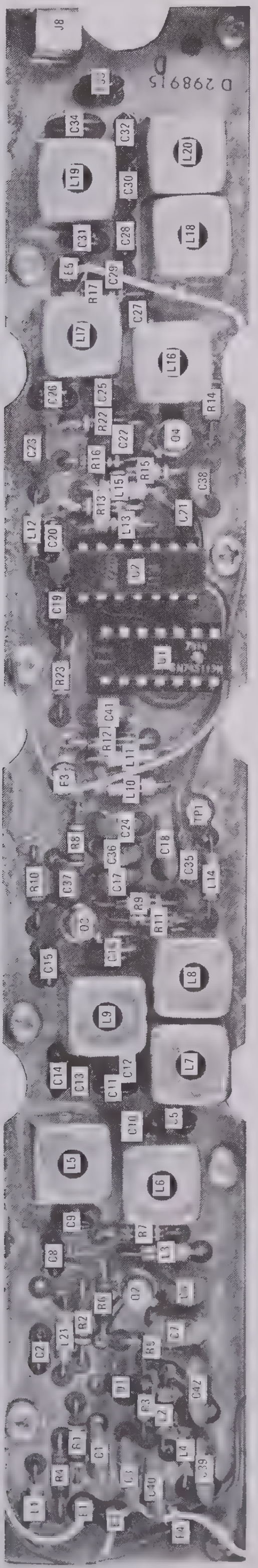

J8
D 298915
C34
C32
L20
L19
C30
L18
C31
C28
E5
C29
R17
C27
L17
L16
C26
C25
R14
R22
C22
Q4
C23
R16
R15
C38
R13
L15
L13
L21
L12
C20
U2
C19
U1
R23
C41
R12
L11
E3
L10
TP1
C24
R8
C36
C18
C35
R10
C37
C17
L14
R9
R11
Q3
C15
L8
L9
L7
C14
C13
C11
C12
C10
L5
L6
C9
R7
C8
L3
R6
Q2
R2
C2
C7
R5
D1
C42
R3
R1
C1
L4
R4
C39
C40
L1
E1
E4

Figure 6-47. 64 MHz Generator A5A5, Component Locations

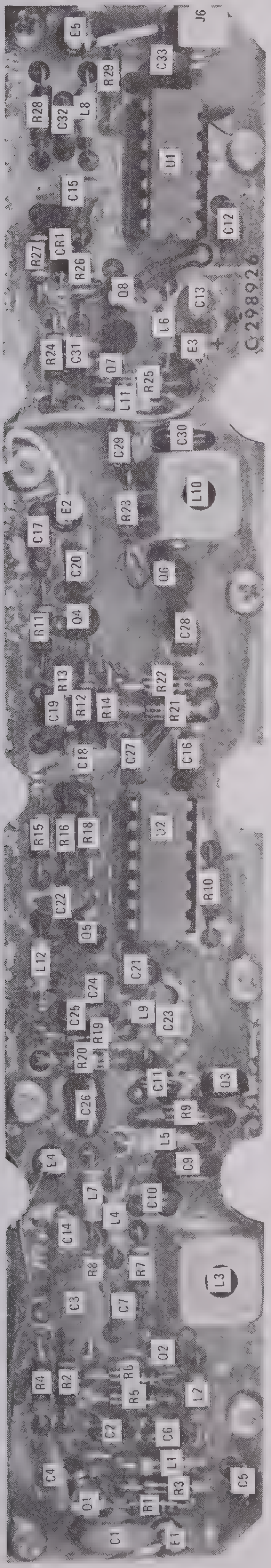
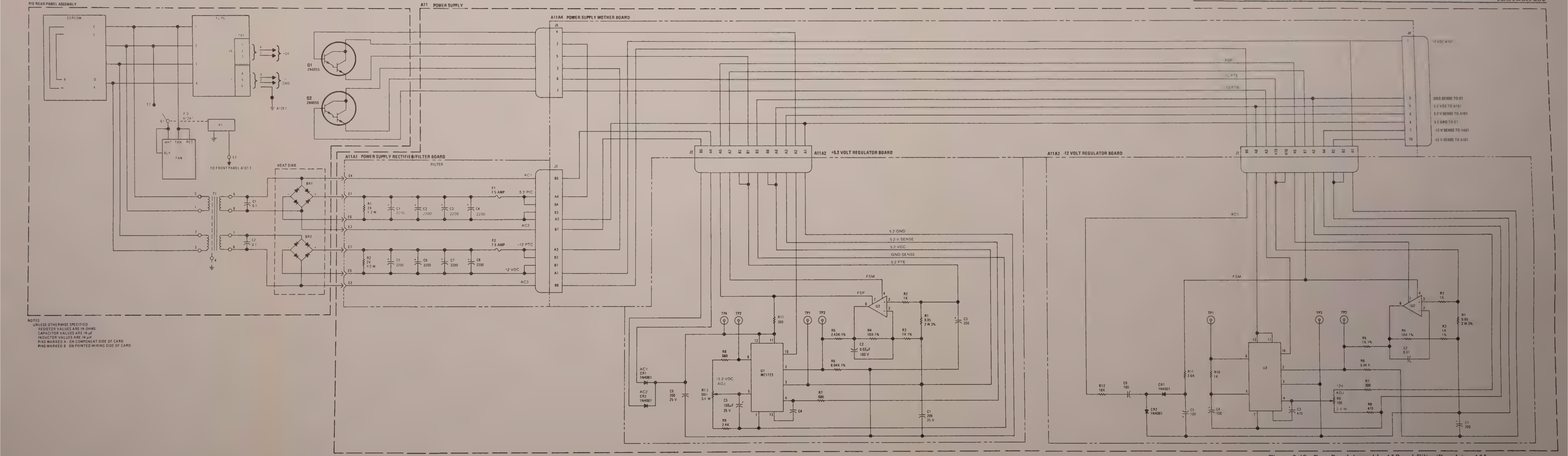

J6
E5
C33
R29
R28
C32
L8
U1
C15
C12
R27
CR1
R26
Q8
C13
C298926
L6
E3
R24
C31
Q7
R25
L11
C29
C30
L10
E2
R23
C17
C20
Q6
R11
Q4
C28
R13
R22
C19
R12
R14
R21
C18
C27
C16
R15
R16
R18
U2
R10
C22
Q5
L12
C21
C24
C25
L9
C23
R19
R20
C11
Q3
C26
R9
L5
E4
C9
L7
C10
L4
C14
R8
R7
L3
C3
C7
Q2
R4
R2
R6
R5
L2
C2
C6
C4
L1
C5
Q1
R1
R3
C1
E1

Figure 6-48. 35/35.5 MHz Board A5A6, Component Locations


P/O REAR PANEL ASSEMBLY
CORCOM
C
E
B
D
H
F
5 V PS
TB1
1
2
3
4
+5
4
5
6
+5V
GND
A12E1
E2
E1
P/O
A12A1
K1
E3
TO FRONT PANEL A1S1 1
WHT
TAN
RED
BLK
FAN
A11 POWER SUPPLY
Q1
2N6055
Q2
2N6055
A11A4 POWER SUPPLY MOTHER BOARD
J6
4
2
5
3
6
7
T1
0
1
2
3
4
5
6
7
8
C1
0.1
C2
0.1
HEAT SINK
BR1
BR2
A11A1 POWER SUPPLY RECTIFIER/FILTER BOARD
FILTER
E4
E1
E6
E3
E7
E5
E2
R1
2K
1/2 W
C1
2200
C2
2200
C3
2200
C4
2200
R2
2K
1/2 W
C5
2200
C6
2200
C7
2200
C8
2200
F1
7.5 AMP
F2
7.5 AMP
AC1
5.2 PIC
AC2
-12 PTC
12 VDC
AC3
J1
B6
A4
B4
B3
A3
B7
A2
B2
B1
A1
B8
J2
B5
A4
A5
A7
B2
B1
B3
B8
A8
A3
A2
A1
A11A2 +5.2 VOLT REGULATOR BOARD
5.2 GND
5.2 V SENSE
5.2 VDC
GND SENSE
5.2 PTE
FSM
FSP
U2
R2
1K
R1
0.05
2 W 3%
C3
200
TP4
TP2
R11
390
TP1
TP3
R5
2.43K-1%
R4
10K-1%
R3
1K 1%
C2
0.02µF
100 V
R8
680
R6
6.04K 1%
U1
MC1723
AC1
CR1
1N4001
AC2
CR2
1N4001
C6
200
25 V
+5.2 VDC
ADJ
R10
500
3/4 W
C5
100µF
25 V
R9
2.4K
R7
680
C4
C1
200
25 V
FSP
12 PTE
12 PTB
J4
7
12 VDC A1S1
5
9
6
8
1
10
GND SENSE TO E1
5.2 VDC TO A1S1
5.2 V SENSE TO A1S1
5.2 GND TO E1
-12 V SENSE TO 1AS1
-12 V SENSE TO A1S1
J3
B6
A8
A3
A10
B10
A5
B7
A2
A4
B2
B3
A1
A11A3 -12 VOLT REGULATOR BOARD
AC3
FSM
U2
R2
1K
R1
0.05
2 W 3%
TP1
TP3
TP2
R4
10K 1%
R3
1K
1%
R5
1K 1%
C2
0.01
R6
6.04 K
U2
R12
18K
C6
100
CR1
1N4001
CR2
1N4001
R11
3.6K
R10
1K
C5
100
C4
100
C3
470
12V
ADJ
R9
100
3/4 W
R7
300
R8
470
C1
200

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN µF
INDUCTOR VALUES ARE IN µH
PINS MARKED A - ON COMPONENT SIDE OF CARD
PINS MARKED B - ON PRINTED WIRING SIDE OF CARD

Figure 6-49. Rear Panel Assembly A12 and Filter/Regulator A11, Schematic Diagram

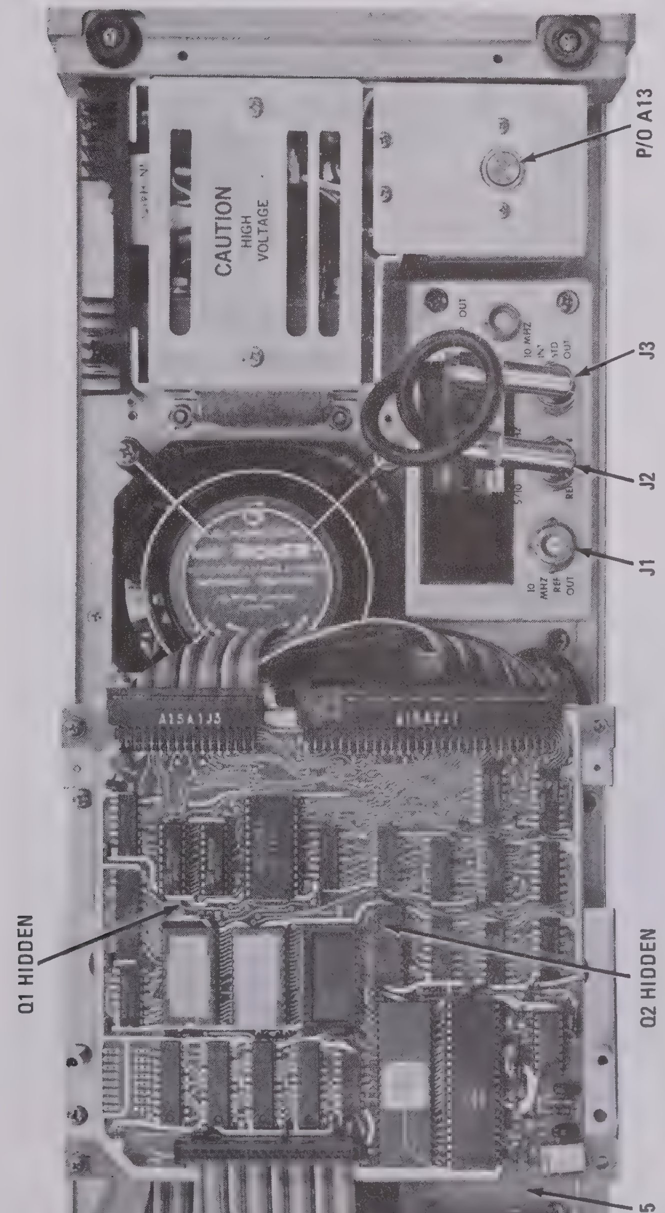

P/O A13
J3
J2
J1
Q1 HIDDEN
Q2 HIDDEN
J5
J4
CAUTION
HIGH
VOLTAGE
10 MHZ
REF
OUT

Figure 6-50. Rear Panel Assembly A12, Component Locations

also mounted on the rear panel. This supply is comprised of a PC board, transformer, a capacitor mounted in the unit and a pass transistor located on the side casting. The supply is +5V @ 4 amps.

6-106. FILTER/REGULATOR ASSEMBLY A11. (Figure 6-49).

6-107. The filter regulator assembly consists of four PC boards: filter board A11A1, +5.2 V regulator A11A2, -12 V regulator A11A3, and motherboard A11A4. The motherboard (Figure 6-51) provides all the interconnection between the other three PC boards, rear panel assembly A12 and, logic and power distribution board A14.

*F1 and F2 should be 1.5 Amp Slo-Blo for 230 volt operation

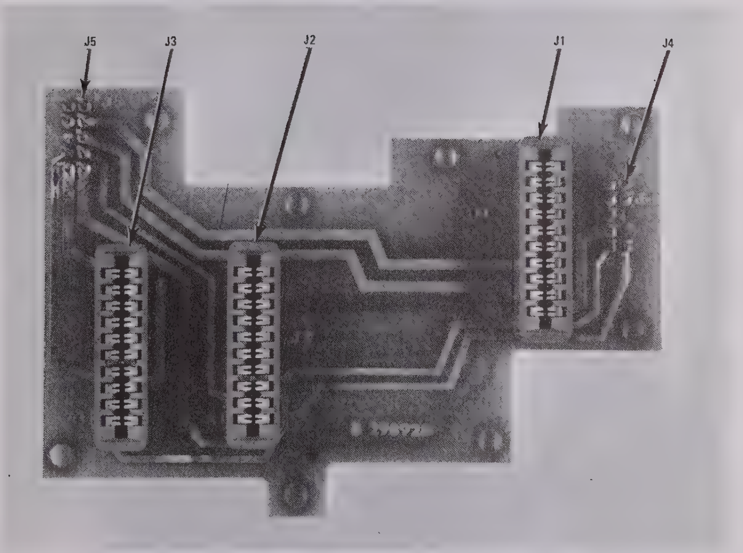


Figure 6-51. Power Supply Motherboard A11A4

6-108. FILTER BOARD A11A1. Filter board A11A1 (Figure 6-52) has seven individually removable inputs (E1 through E7) coming from bridge rectifiers BR1 and BR2 and transformer T1. Four of the inputs are connected across filter capacitors. The inputs at E1 and E6 are filtered by capacitors C1 through C4 in parallel and the filtered dc is applied through motherboard A11A4 to the collector of +5.2 V pass transistor A12Q1. The inputs at E7 and E5 are filtered by capacitors C5 through C8 in parallel and the filtered dc is applied through motherboard A11A4 to the collector of -12 V pass transistor A12Q2. Fuses F1 and F2 are provided to protect the supply from a catastrophic failure such as the pass transistor shorting where the regulators would not be able to control the current and very high dc currents would flow. Resistors R1 and R2 across the filter capacitors are bleeder resistors which allow the capacitors to discharge when the dc load current path is opened. The other three ac voltage inputs to the filter board are fed through the motherboard to the appropriate regulator board.

6-109. 5.2 V REGULATOR A11A2. The 5.2 V regulator (Figure 6-53) is basically an IC regulator driving a pass transistor (A12Q1) to boost current output capability. The regulators feature foldback current limiting and overvoltage protection. On the 5.2 V regulator, inputs AC1 and AC2 are rectified and filtered by CR1, CR2, and C6 providing the dc bias for U1. This input must be at least 3 volts greater than the output of pin 10. The output of U1 (pin 10), 5.2 PTB, drives the base of Darlington pass transistor A12Q1 while the emitter is returned to the regulator board where the dc load current passes through .05 ohm resistor R1 and is then brought out to the front panel assembly through connector J4 on the motherboard. Three other outputs from this board are 5.2 GND, 5.2 V SENSE, and GND SENSE which are also brought out to J4 on the motherboard.

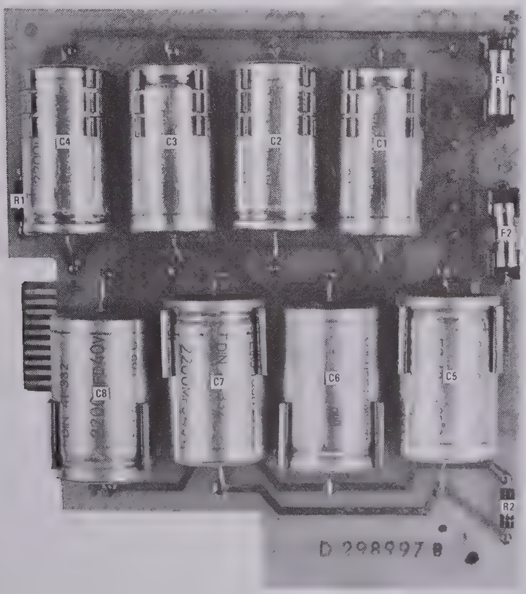


Figure 6-52. Filter Board A11A1, Component Locations

6-110. The 5.2V SENSE line is connected through R7 to pin 4 of U1 which is the inverting input to a differential amplifier inside U1. This is balanced against a reference input into pin 5 which is the noninverting input. The reference output of U1 (pin 6) is 7.15 volts which is divided down by R8, R9, and R10. R10 is the voltage adjust potentiometer which allows the reference input at pin 5 to be adjusted to +5.2 volts. Capacitor C5 is used to reduce the noise and ripple voltage on the regulated dc output. Capacitor C4 assures frequency stability of U1.

6-111. An important feature of this regulator is foldback current limiting which is provided by R1 through R6, C2, and U2. The voltage drop across R1 is amplified by U2 and applied through a voltage divider to pin 2 of U1. The voltage at pin 2 of U1 is now directly dependent on the voltage drop across R1 which depends on the load current. When the load current reaches 5.5 amp (0.5 amp above rated load), the voltage at pin 2 of U1 is approximately 0.7 volts greater than that at pin 3 of U1, which turns on an internal transistor. The transistor reduces the voltage and current drive capability of U1. As a result, the voltage and current outputs of the regulator start to drop. U2 requires two supplies, positive and negative. The positive supply comes from the voltage input to U1 through dropping resistor R11 to keep the supply voltage below the rated limit of U1. This voltage is also used to power U2 on the -12 V regulator and is therefore, an output from the 5.2 V regulator (foldback supply positive, FSP). The negative supply is generated on the -12 regulator board and is an input to this board (foldback supply minus, FSM).

6-112. **-12 V REGULATOR A11A3.** The -12 V regulator (Figure 6-54) is basically the same design as the 5.2 V regulator so only the differences will be described. Basically it is still a positive output regulator design where the positive end is grounded and the negative end is taken as the -12 volt output. Since the desired output of 12 volts is greater than the internal reference of 7.15 volts, the voltage divider adjust resistors (R7, R8, and R9) are wired to the inverting input of U1 (pin 4), where the adjustment is now made to place a voltage of +7.15 volts with respect to the -12 volt output (or -4.85 volts with respect to ground) on pin 4. Pin 4 is balanced against the internal reference of 7.15 volts which is connected to pin 5 from pin 6 of U1 through R10. An ac input (AC3) from the transistor is wired to R12, C5, C6, CR1 and CR2 to create the negative supply voltage for U2 on both regulator boards. All other component functions are identical to the 5.2 V regulator.

6-113. **LOGIC AND POWER DISTRIBUTION A14.** (Figures 6-55 and 6-56).

6-114. The voltage output and sense wires for the -12 V and +5.2 V regulators are connected at the input to line switch A1S1. The output of the switch is then wired to the logic and power distribution board A14 where they are distributed to the appropriate connectors which carry power to the different section of the system. In addition, the fan off signal comes from the line switch and is wired such that it is floating when the system is on and is wired to -12 V when the system is in standby. Another function of this board is to provide overvoltage protection for the system. The +5.2 volt overvoltage circuit consists of CR1 and SCR1. If the 5.2 V exceeds 7 volts, SCR1 fires shorting the 5.2 V to ground. Similarily CR2 and SCR2 protect the -12 V for voltages exceeding -15 volts. These circuits are used only in the power ON mode. In STBY

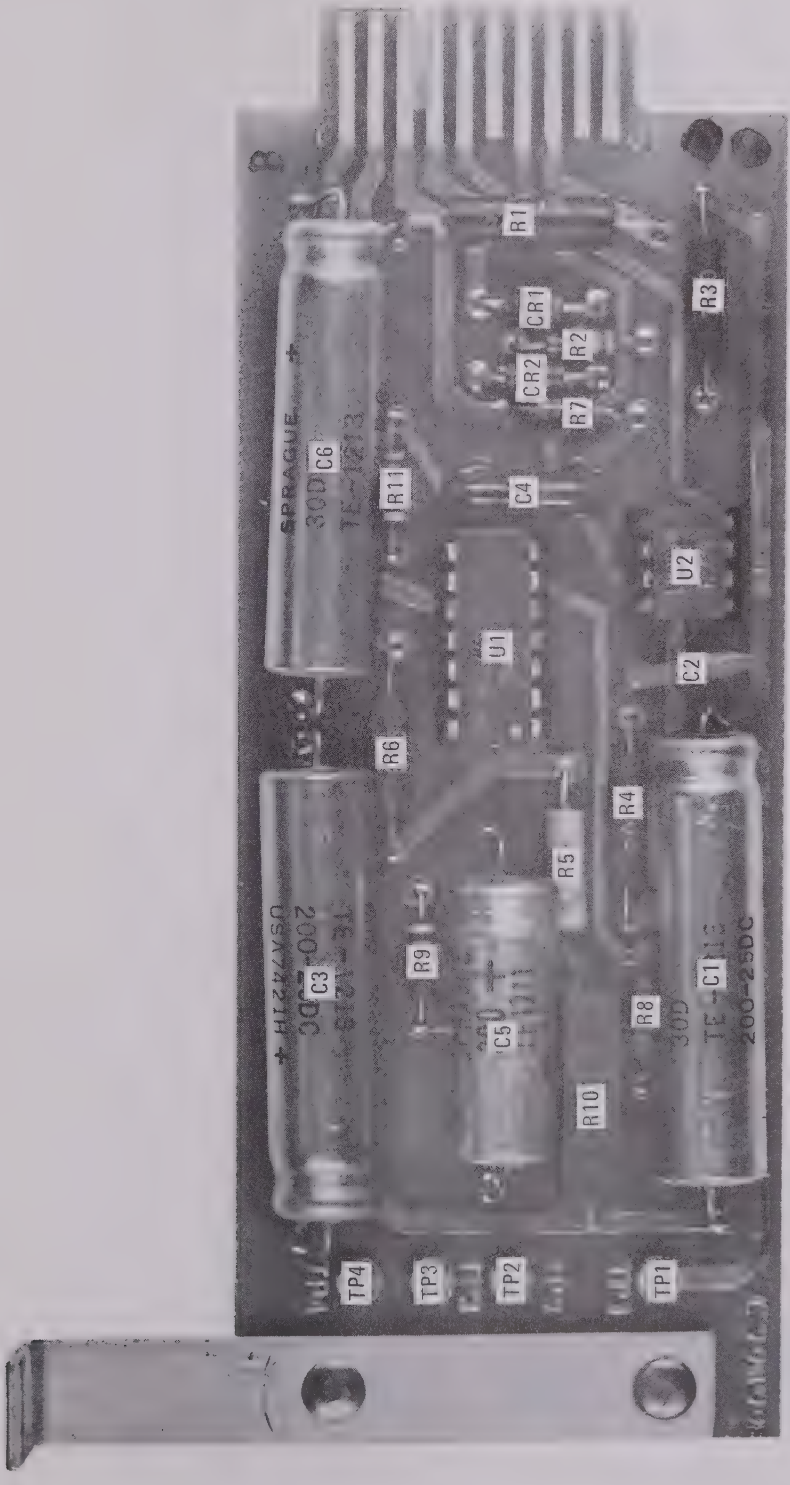

R1
CR1
R2
CR2
R7
R3
C6
R11
C4
U2
U1
C2
R6
R4
R5
R9
C3
C5
R8
R10
C1
TP4
TP3
TP2
TP1

Figure 6-53. 5.2 Volt Regulator A11A2, Component Locations

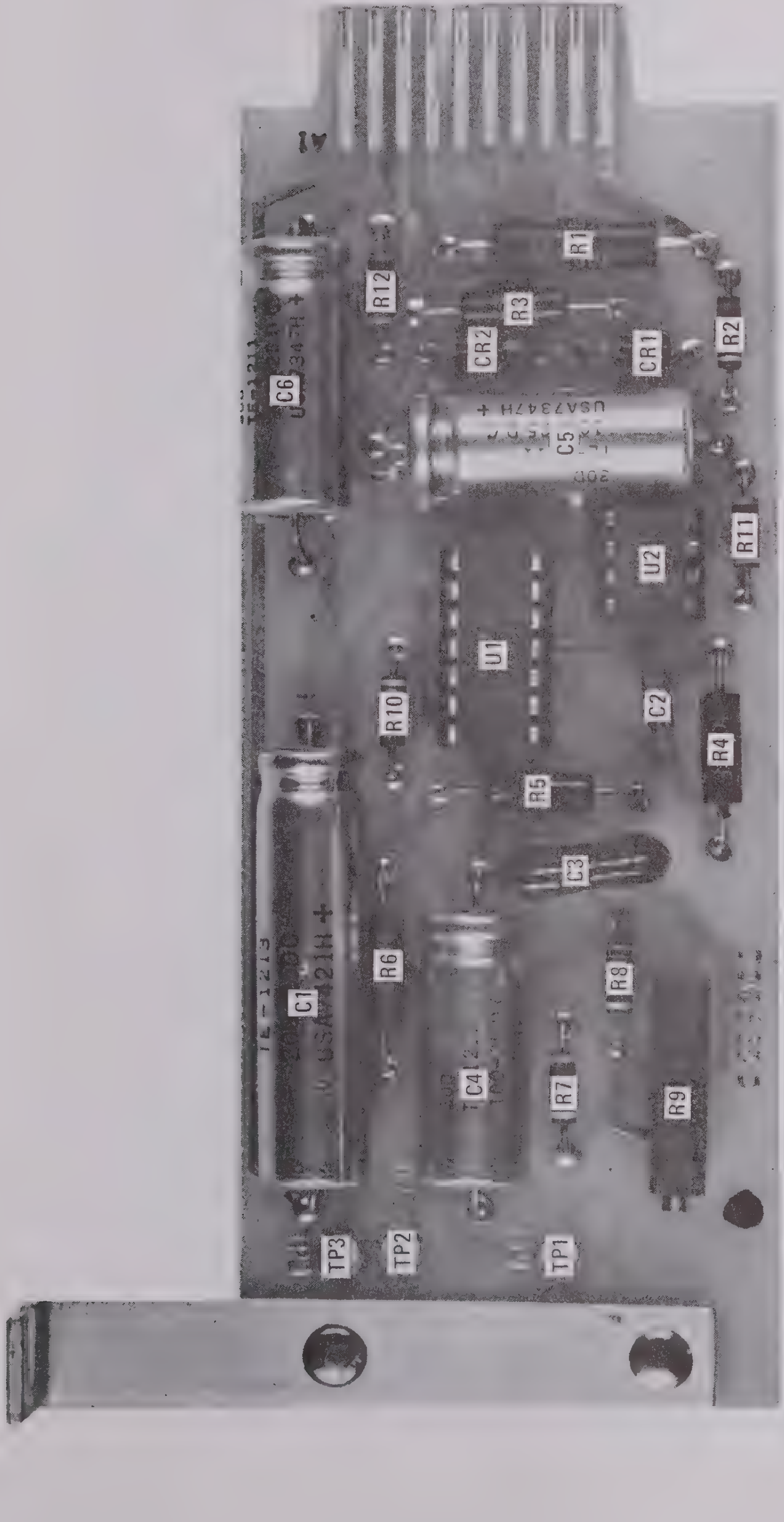
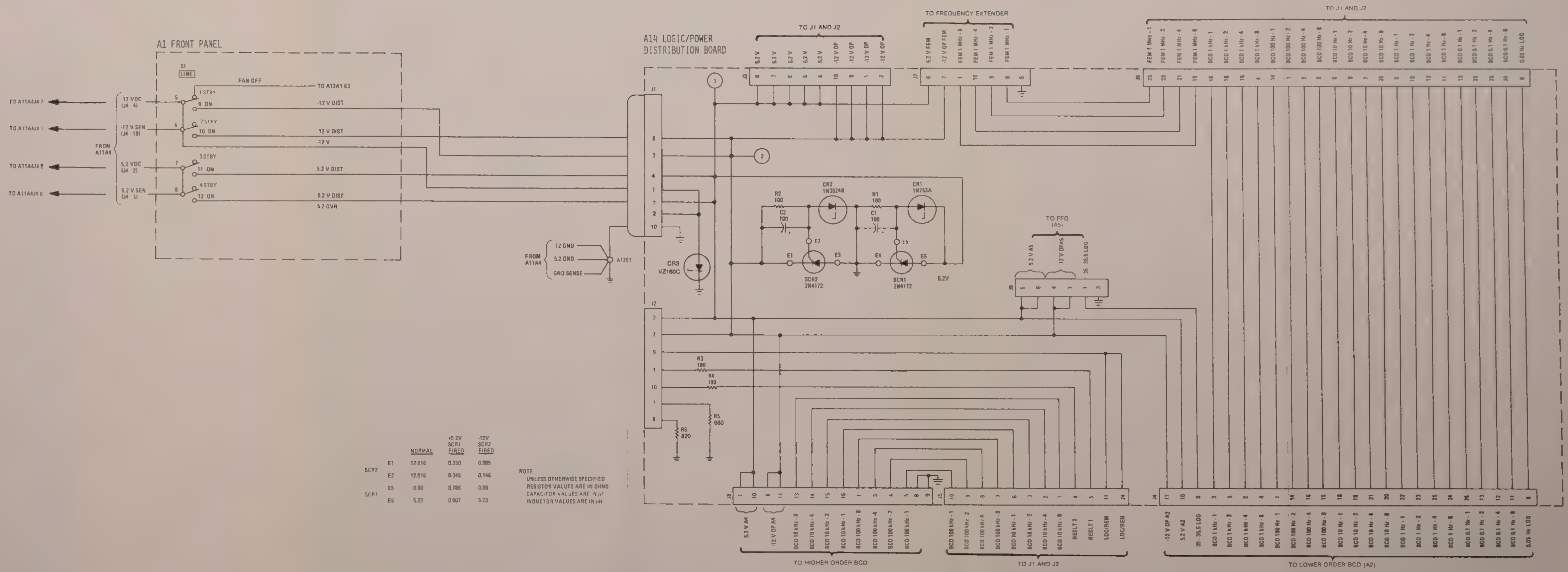

A1
R1
R12
R3
R2
CR2
CR1
C6
C5
R11
U2
U1
R10
C2
R4
R5
C3
R6
C1
R8
C4
R7
R9
TP3
TP2
TP1

Figure 6-54. -12 Volt Regulator A11A3, Component Locations


A1 FRONT PANEL
S1
LINE
FAN OFF
TO A12A1 E3
1 STBY
9 ON
-12 V DIST
2 STBY
10 ON
12 V DIST
-12 V
3 STBY
11 ON
5.2 V DIST
4 STBY
12 ON
5.2 V DIST
5.2 OVR
TO A11A4J4 7
TO A11A4J4 1
TO A11A4J4 9
TO A11A4J4 6
12 VDC (J4 4)
-12 V SEN (J4 - 10)
5.2 VDC (J4 2)
5.2 V SEN (J4 5)
FROM A11A4
5
6
7
8
A14 LOGIC/POWER DISTRIBUTION BOARD
TO J1 AND J2
5.2 V
5.2 V
5.2 V
5.2 V
5.2 V
-12 V OP
-12 V OP
-12 V OP
-12 V OP
J3
TO FREQUENCY EXTENDER
5.2 V FEM
-12 V OP FEM
FEM 1 MHz - 8
FEM 1 MHz - 4
FEM 1 MHz - 2
FEM 1 MHz - 1
J7
TO J1 AND J2
FEM 1 MHz - 1
FEM 1 MHz - 2
FEM 1 MHz - 4
FEM 1 MHz - 8
BCD 1 kHz - 1
BCD 1 kHz - 2
BCD 1 kHz - 4
BCD 1 kHz - 8
BCD 100 Hz - 1
BCD 100 Hz - 2
BCD 100 Hz - 4
BCD 100 Hz - 8
BCD 10 Hz - 1
BCD 10 Hz - 2
BCD 10 Hz - 4
BCD 10 Hz - 8
BCD 1 Hz - 1
BCD 1 Hz - 2
BCD 1 Hz - 4
BCD 1 Hz - 8
BCD 0.1 Hz - 1
BCD 0.1 Hz - 2
BCD 0.1 Hz - 4
BCD 0.1 Hz - 8
0.05 Hz LOG
J6
J1
①
②
FROM A11A4
12 GND
5.2 GND
GND SENSE
A12E1
CR3
VZ160C
R2
100
C2
100
CR2
1N3024B
R1
100
C1
100
CR1
1N753A
E1
E2
E3
E4
E5
E6
SCR2
2N4172
SCR1
2N4172
5.2V
TO FFG
(A5)
5.2 V A5
12 V OPA5
35 - 35.5 LOG
J9
J2
R3
100
R4
100
R5
680
R6
820
J8
5.2 V A4
-12 V OP A4
BCD 10 kHz - 8
BCD 10 kHz - 4
BCD 10 kHz - 2
BCD 10 kHz - 1
BCD 100 kHz - 8
BCD 100 kHz - 4
BCD 100 kHz - 2
BCD 100 kHz - 1
TO HIGHER ORDER BCD
J5
BCD 100 kHz - 1
BCD 100 kHz - 2
BCD 100 kHz - 4
BCD 100 kHz - 8
BCD 10 kHz - 1
BCD 10 kHz - 2
BCD 10 kHz - 4
BCD 10 kHz - 8
RESLT 2
RESLT 1
LOC/REM
LOC/REM
TO J1 AND J2
J4
-12 V OP A2
5.2 V A2
35 - 35.5 LOG
BCD 1 kHz - 1
BCD 1 kHz - 2
BCD 1 kHz - 4
BCD 1 kHz - 8
BCD 100 Hz - 1
BCD 100 Hz - 2
BCD 100 Hz - 4
BCD 100 Hz - 8
BCD 10 Hz - 1
BCD 10 Hz - 2
BCD 10 Hz - 4
BCD 10 Hz - 8
BCD 1 Hz - 1
BCD 1 Hz - 2
BCD 1 Hz - 4
BCD 1 Hz - 8
BCD 0.1 Hz - 1
BCD 0.1 Hz - 2
BCD 0.1 Hz - 4
BCD 0.1 Hz - 8
0.05 Hz LOG
TO LOWER ORDER BCD (A2)

		NORMAL	+5.2V SCR1 FIRED	-12V SCR2 FIRED
SCR2	E1	12.016	0.350	0.905
	E2	12.016	0.345	0.146
SCR1	E5	0.00	0.786	0.00
	E6	5.23	0.907	5.23

NOTE
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN µF
INDUCTOR VALUES ARE IN µH

Figure 6-55. Logic and Power Distribution Board A14, Schematic Diagram

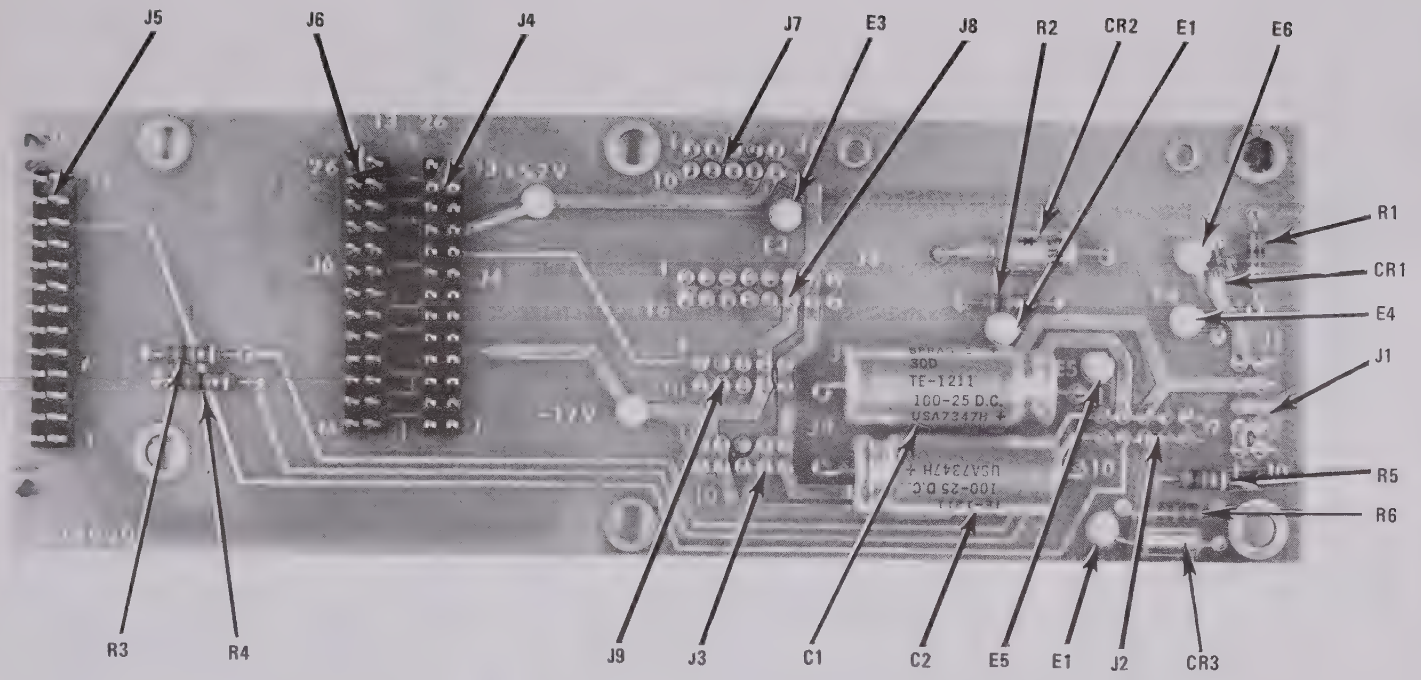

J5
J6
J4
J7
E3
J8
R2
CR2
E1
E6
R1
CR1
E4
J1
R5
R6
R3
R4
J9
J3
C1
C2
E5
E1
J2
CR3
30D
TE-1211
100-25 D.C.
USA7347H +
-12V

Figure 6-56. Logic and Power Distribution Board A14, Component Locations

it is necessary to protect the -12 volts. This is accomplished by diode CR3. If an SCR does fire due to an overvoltage condition, ac power must be removed from the unit. If the overvoltage condition has been eliminated, power may be reapplied to restore dc power. The ground return wires for +5.2 and -12 volts and the ground sense wire are each brought out and wired to the single point chassis ground.

6-115. MPU ASSEMBLY A15. (Figure 6-57).

6-116. The MPU assembly contains the MPU PC board assembly A15A1, Latch PC board assembly A15A2 and GPIB PC board assembly A15A3.

6-117. MPU BOARD ASSEMBLY A15A1. (Figures 6-58 through 6-60). The MPU board assembly contains the following major component blocks:

a. MPU (Microprocessor Unit)
b. DMA (Direct Memory Access)
c. Timing Generator
d. Address Decoders
e. RAM (Random Access Memory)
f. ROM (Read Only Memory)
g. PIC (Priority Interrupt Controller)
h. Data Latches for ATT and MPB Bits
i. The Power-Down Interrupt Generator

6-118. MICROPROCESSOR UNIT U20. Microprocessor Unit U20 performs the central control function of the 380 Synthesized Signal Generator. The MPU has the following features.

- Eight-bit parallel processing
- Bi-directional data bus
- Sixteen-bit address bus
- 72 instructions
- Seven addressing modes - Direct - Relative - Immediate, - Indexed, - Extended, - Implied, - Accumulator
- Variable length stack
- Vectored restart
- Maskable interrupt vector
- Separate non-maskable interrupt
- Six internal registers - two accumulators - Index register - Program counter, - Stock pointer, - Condition code register
- Direct memory addressing (DMA) capability

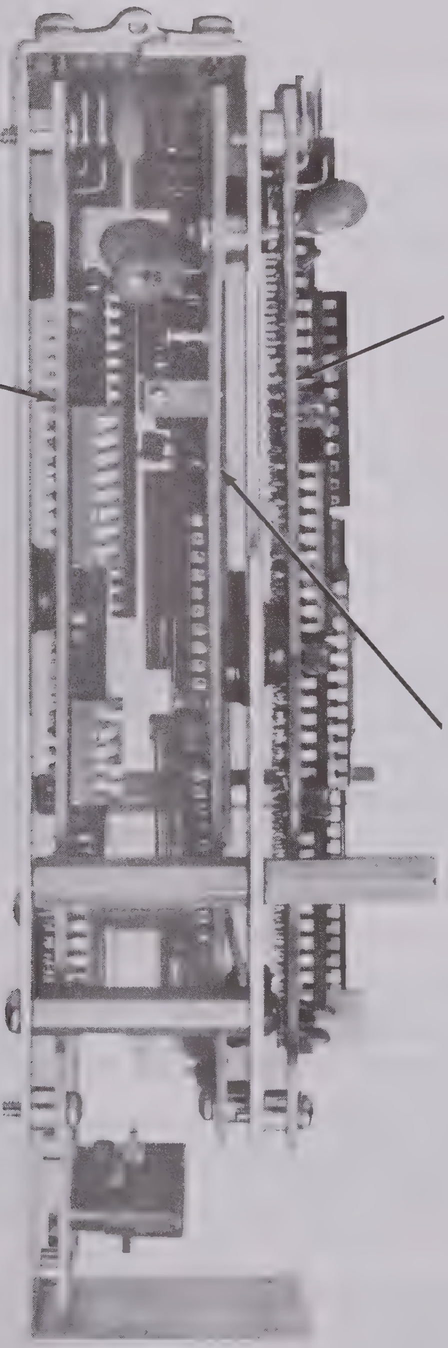


Figure 6-57. MPU Assembly A15

6-119. MPU SIGNAL DESCRIPTION. (Table 6-3). Proper operation of the MPU requires that certain control and timing signals be provided to accomplish specific functions and that other signal lines be monitored to determine the state of the processor.

6-120. DIRECT MEMORY ACCESS (DMA) U17. The Direct Memory Access Controller (DMAC) performs the function of transferring data directly between memory and peripheral device controllers, in this case the GPIA. It controls the address and data buses in place of the MPU in bus organized systems.

6-121. The bus interface of U17 includes select, read/write, interrupt, transfer request/grant, and bus interface logic to allow the data transfer over an 8-bit bidirectional data bus. The functional configuration of the DMAC is programmed via the data bus. The internal structure provides for control and handling of four individual channels, each of which is separately configured. Programmable control registers provide control for the transfer location and length, individual channel control and transfer mode configuration, priority of servicing, data chaining, and interrupt control. Status and control lines provide control to the peripheral controllers. The mode of transfer for each channel can be programmed as cycle-stealing or a burst transfer mode. The 380 utilizes only the cycle-stealing mode.

6-122. DEVICE OPERATION. The DMAC has fifteen addressable registers, eight of these are sixteen bits in length. Each channel has a separate address register and a byte count register, each of which is sixteen bits. There are also four channel control registers. The three general control registers common to all four channels are the priority control register, the interrupt control register, and the data chain register.

6-123. To prepare a channel for DMA, the address registers must be loaded with the starting memory address and the byte count register loaded with the number of bytes to be transferred. The bits in the channel control register establish the direction of the transfer, the mode, and the address increment or decrement after each cycle. Each channel can be set for one of three transfer modes: Three-state control (TSC) steal, halt steal, or halt burst. Two read-only status bits in the channel control register indicate when the channel is busy transferring data and when the DMA transfer is completed.

6-124. The priority control register enables the transfer requests from the peripheral controllers and establishes either a fixed priority or rotating priority scheme of servicing these requests.

6-125. When the DMA transfer for a channel is complete (the byte count register is zero), a DMA end signal is directed to the peripheral controller and an $\overline{\text{IRQ}}$ goes to the MPU. Enabling of these interrupts is done in the interrupt control register. The DMA end/$\overline{\text{IRQ}}$ flag bit is read from this register.

6-126. Chaining of data transfer is controlled by the data chain register. When enabled, the contents of the address and byte count registers for channel #3 are put into the registers of the channel selected for chaining when its byte count register becomes zero. This allows for repetitively reading or writing a block of memory.

6-127. During the DMA mode, the DMAC controls the address bus and data bus for the system as well as providing the R/$\overline{\text{W}}$ line and a signal to be used as VMA. When a peripheral device controller desires a DMA transfer, it is requested by a transfer request. Assuming this request is enabled and meets the test

TABLE 6-3. MPU SIGNAL DESCRIPTIONS

Signal Name	Description
CLOCKS PHASE ONE AND PHASE TWO (01, 01)	Two pins are used for a two-phase non-overlapping clock that runs at the VCC voltage level.
ADDRESS BUS (A0-A15)	Sixteen pins are used for the address bus. The outputs are three-state bus drivers capable of driving one standard TTL load and 90 pF. When the output is turned off, it is essentially an open circuit. This permits the MPU to be used in DMA applications. Putting TSC in its high state forces the address bus to go into the three-state mode.
DATA BUS (D0-D7)	Eight pins are used for the data bus. It is bi-directional, transferring data to and from the memory and peripheral devices. It also has three-state output buffers capable of driving one standard TTL load and 130 pF. Data bus is placed in the three-state mode when DBE is low.
DATA BUS ENABLE (DBE)	This input is the three-state control signal for the MPU data bus and will enable the bus drivers when in the high state. This input is TTL compatible; however, in normal operation, it would be driven by the phase two clock. During an MPU read cycle, the data bus drivers will be disabled internally. When it is desired that another device control the data bus such as in Direct Memory Access (DMA) applications, DBE should be held low. If additional data setup or hold time is required on an MPU write, the DBE down time can be decreased. The minimum delay from the trailing edge of DBE to the trailing edge of 01 is t_{DBED}. By skewing DBE with respect to E in this manner, data setup or hold time can be increased.
BUS AVAILABLE (BA)	The bus available signal will normally be in the low state; when activated, it will go to the high state indicating that the microprocessor has stopped and that the address bus is available. This will occur if the Halt line is in the low state or the processor is in the WAIT state as a result of the execution of a WAIT instruction. At such time, all three-state output drivers

TABLE 6-3. MPU SIGNAL DESCRIPTIONS (Cont)

Signal Name	Description
BUS AVAILABLE (BA) (Cont)	will go to their off state and other outputs to their normally inactive level. The processor is removed from the WAIT state by the occurence of a maskable (mask bit 1 = 0) or nonmaskable interrupt. This output is capable of driving one standard TTL load and 30 pF. If TSC is in the high state, bus available will be low.
READ/$\overline{\text{WRITE}}$ (R/$\overline{\text{W}}$)	This TTL compatible output signals the peripherals and memory devices whether the MPU is in a Read (high) or Write (low) state. The normal standby state of this signal is Read (high). Three-State Control going high will turn Read/$\overline{\text{Write}}$ to the off (high impedance) state. Also, when the processor is halted, it will be in the off state. This output is capable of driving one standard TTL load and 90 pF.
RESET	The reset input is used to reset and start the MPU from a power down condition resulting from a power failure or initial start-up of the processor. This input can also be used to reinitialize the machine at any time after startup. If a high level is detected in this input, this will signal the MPU to begin the reset sequence. During the reset sequence, the contents of the last two locations (FFFE, FFFF) in memory will be loaded into the program counter to point to the beginning of the reset routine. During the reset routine, the interrupt mask bit is set and must be cleared under program control before the MPU can be interrupted by IRQ. While reset is low (assuming a minimum of 8 clock cycles have occurred) the MPU output signals will be in the following states: VMA = low, BA = low, Data Bus = high impedance, R/$\overline{\text{W}}$ = high (read state), and the address bus will contain the reset address FFFE. After the power supply reaches 4.75 volts a minimum of eight clock cycles are required for the processor to stabilize in preparation for restarting. During these eight cycles, VMA will be in an indeterminate state so any devices that are enabled by VMA which could accept a false write during this time must be

TABLE 6-3. MPU SIGNAL DESCRIPTIONS (Cont)

Signal Name	Description
RESET (Cont)	disabled until VMA is forced low after eight cycles. $\overline{\text{Reset}}$ can go high asynchronously with the system clock any time after the eighth cycle.
$\overline{\text{INTERRUPT REQUEST}}$ ($\overline{\text{IRQ}}$)	This level sensitive input requests that an interrupt sequence be generated within the machine. The processor will wait until it completes the current instruction that is being executed before it recognizes the request. At that time, if the interrupt mask bit in the Condition Code Register is not set, the machine will begin an interrupt sequence. The Index Register, Program Counter, Accumulators, and Condition Code Register are stored away on the stack. Next the MPU will respond to the interrupt request by setting the interrupt mask bit high so that no further interrupts may occur. At the end of the cycle, a 16-bit address will be loaded that points to a vectoring address which is located in memory locations FFF8 and FFF9. An address loaded at these locations causes the MPU to branch to an interrupt routine in memory. The $\overline{\text{Halt}}$ line must be in the high state for interrupts to be serviced. Interrupts will be latched internally while $\overline{\text{Halt}}$ is low. The $\overline{\text{IRQ}}$ has a high impedance pullup device internal to the chip; however a 3 k external resistor to V_{CC} should be used for wire-OR and optimum control of interrupts.
$\overline{\text{NON-MASKABLE INTERRUPT}}$ ($\overline{\text{NMI}}$) and WAIT FOR INTERRUPT (WAI)	The MC6800 is capable of handling two types of interrupts: maskable ($\overline{\text{IRQ}}$) as described earlier, and non-maskable ($\overline{\text{NMI}}$). $\overline{\text{IRQ}}$ is maskable by the interrupt mask in the condition code register while $\overline{\text{NMI}}$ is not maskable. The handling of these interrupts by the MPU is the same except that each has its own vector address. The behavior of the MPU when interrupted is shown in Figure 6-61 which details the MPU response to an interrupt while the MPU is executing the control program. The interrupt shown could be either $\overline{\text{IRQ}}$ or $\overline{\text{NMI}}$ and can be asynchronous with respect to 02. The interrupt is shown going low

TABLE 6-3. MPU SIGNAL DESCRIPTIONS (Cont)

Signal Name	Description
NON-MASKABLE INTERRUPT (NMI) and WAIT FOR INTERRUPT (WAI) (Cont)	at time t_{PCS} in cycle #1 which precedes the first cycle of an instruction (OP code fetch). This instruction is not executed but instead the Program Counter (PC), Index Register (IX), Accumulators (ACCX), and the Condition Code Register (CCR) are pushed onto the stack. The Interrupt Mask bit is set to prevent further interrupts. The address of the interrupt service routine is then fetched from FFFC, FFFD for an $\overline{\text{NMI}}$ interrupt and from FFF8, FFF9 for an $\overline{\text{IRQ}}$ interrupt. Upon completion of the interrupt service routine, the execution of RTI will pull the PC, IX, ACCX, and CCR off of the stack; the Interrupt Mask bit is restored to its condition prior to Interrupts.
THREE STATE CONTROL (TSC)	When the three-state control (TSC) line is a logic 1, the address bus and the $\text{R}/\overline{\text{W}}$ line are placed in a high impedance state. VMA and BA are forced low when TSC = 1 to prevent false reads or writes on any device enabled by VMA. It is necessary to delay program execution while TSC is held high. This is done by insuring that no transitions of 01 (or 02) occur during this period. (Logic levels of the clocks are irrelevant so long as they do not change). Since the MPU is a dynamic device, the 01 clock can be stopped for a maximum time PW_{0H} without destroying data within the MPU. TSC then can be used in a short Direct Memory Access (DMA) application.
VALID MEMORY ADDRESS (VMA)	This output indicates to peripheral devices that there is a valid address on the address bus. In normal operation, this signal should be utilized for enabling peripheral interfaces such as the PIA and ACIA. This signal is not three-state. One standard TTL load and 90 pF may be directly driven by this active high signal.
$\overline{\text{HALT}}$	When this input is in the low state, all activity in the machine will be halted. This input is level sensitive.

TABLE 6-3. MPU SIGNAL DESCRIPTIONS (Cont)

Signal Name	Description
$\overline{\text{HALT}}$ (Cont)	The $\overline{\text{Halt}}$ line provides an input to the MPU to allow control of program execution by an outside source. If $\overline{\text{Halt}}$ is high, the MPU will execute the instructions; if it is low, the MPU will go to a halted or idle mode. A response signal, Bus Available (BA) provides an indication of the current MPU status. When BA is low, the MPU is in the process of executing the control program; if BA is high, the MPU has halted and all internal activity has stopped. When BA is high, the Address Bus, Data Bus, and R/$\overline{\text{W}}$ line will be in a high impedance state, effectively removing the MPU from the system bus. VMA is forced low so that the floating system bus will not activate any device on the bus that is enabled by FMA.

of highest priority, the DMAC will issue a DMA request. When the DMAC receives the DMA grant, it gives a transfer acknowledge to the peripheral device controller, at which time the data is transferred. When the channels byte count register equals zero, the transfer is complete and a DMA end is given to the peripheral device controller, and an $\overline{\text{IRQ}}$ is given to the MPU.

6-128. **Initialization.** During a power-on sequence, the DMAC is reset via the $\overline{\text{RES}}$ input. All registers, with the exception of the address and byte count registers, are set to a logic 0 state. This disables all request and the data chain function while masking all interrupts. The address, byte count, and channel control registers must be programmed before the respective transfer request bit is enabled in the priority control register.

6-129. **Transfer Mode.** There are three ways in which a DMA transfer may be done. The one used is determined by the data transfer rate required, the number of channels attached, and the hardware complexity allowable. The 380 utilizes the TSC steal. See Figure 6-62.

6-130. The three-state control (TSC) steal-mode is initiated by the DMAC bringing the $\overline{\text{DRQT}}$ line LOW. This line goes to the system clock driver which returns a HIGH on DGRNT on the rising edge of the system 01 clock. The DGRNT signal must cause the address control and data lines to go to the high impedance state. The DMAC now supplies the address from the address register of the channel requesting service. It also supplies the R/$\overline{\text{W}}$ signal as determined from the channel control register. After one byte is transferred, control is restored to the MPU. This method stretches the 01 and 02 clocks while the DMAC uses the memory.

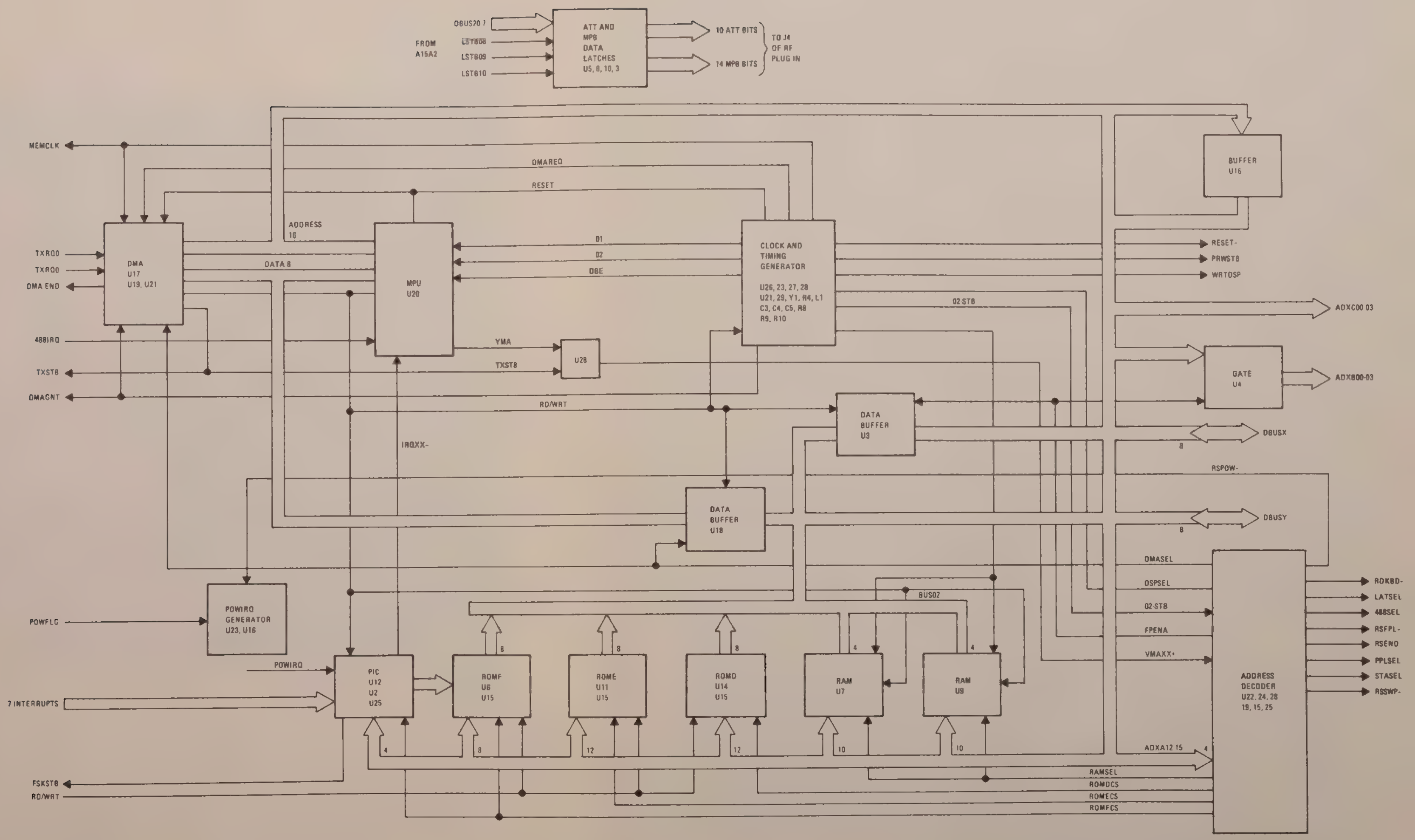

DBUS20 7
FROM A15A2
LST808
LST809
LST810
ATT AND MPB DATA LATCHES U5, 8, 10, 3
10 ATT BITS
14 MPB BITS
TO J4 OF RF PLUG IN
MEMCLK
DMAREQ
RESET
ADDRESS 16
TXRQ0
TXRQ0
DMA END
DMA U17 U19, U21
DATA 8
MPU U20
01
02
DBE
CLOCK AND TIMING GENERATOR U26, 23, 27, 28 U21, 29, Y1, R4, L1 C3, C4, C5, R8 R9, R10
BUFFER U16
RESET-
PRWSTB
WRTDSP
02 STB
ADXC00 03
488IRQ
YMA
U28
TXSTB
TXSTB
GATE U4
ADX800-03
DMAGNT
RD/WRT
DATA BUFFER U3
DBUSX
8
IRQXX-
RSPOW-
DATA BUFFER U18
DBUSY
8
DMASEL
DSPSEL
02 STB
FPENA
VMAXX+
BUS02
POWIRQ GENERATOR U23, U16
POWFLG
POWIRQ
PIC U12 U2 U25
7 INTERRUPTS
ROMF U6 U15
ROME U11 U15
ROMD U14 U15
RAM U7
RAM U9
8
8
8
4
4
4
8
12
12
10
10
ADDRESS DECODER U22, 24, 28 19, 15, 25
ADXA12 15
4
RDKBD-
LATSEL
488SEL
RSFPL-
RSEND
PPLSEL
STASEL
RSSWP-
FSKSTB
RD/WRT
RAMSEL
ROMDCS
ROMECS
ROMFCS

Figure 6-58. MPU PC Board Block Diagram

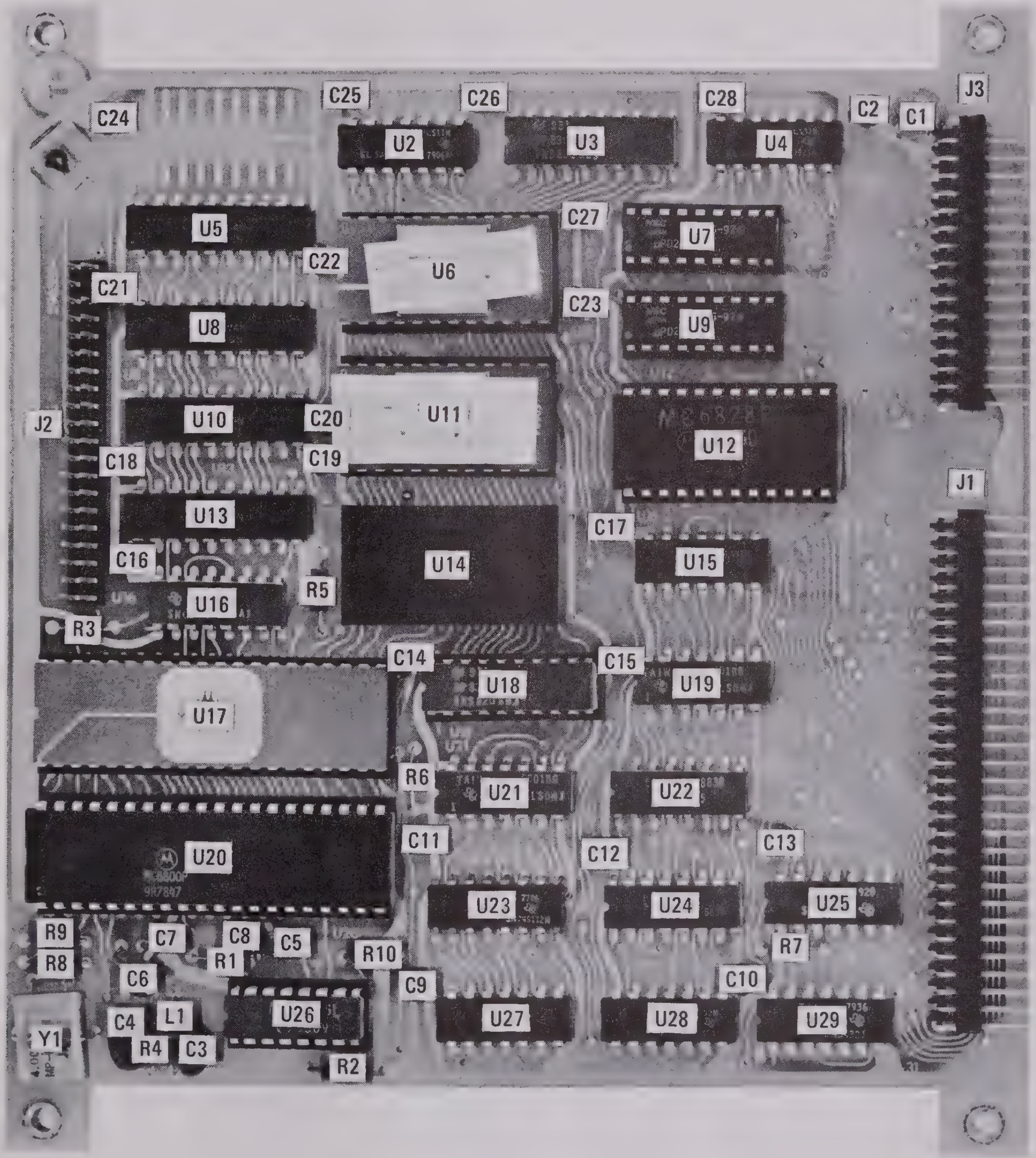

C24
C25
U2
C26
U3
C28
U4
C2
C1
J3
U5
C22
U6
C27
U7
C21
U8
C23
U9
J2
U10
C20
U11
U12
C18
C19
J1
U13
C17
C16
U14
U15
R5
U16
R3
C14
U18
C15
U19
U17
R6
U21
U22
C11
C12
C13
U20
U23
U24
U25
R9
C7
C8
C5
R8
R1
R10
R7
C6
C9
C10
Y1
C4
L1
U26
U27
U28
U29
R4
C3
R2

Figure 6-59. MPU PC Board A15A1, Component Location

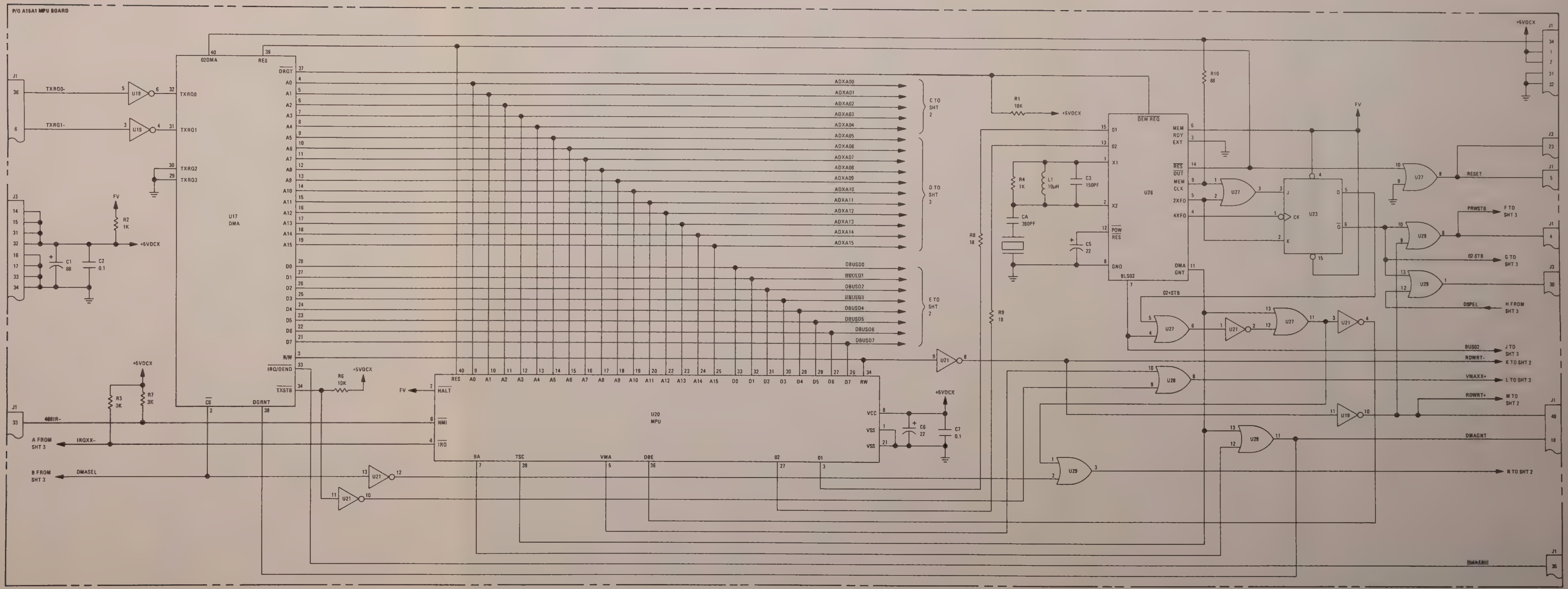

P/O A15A1 MPU BOARD
J1
TXRQ0-
TXRQ1-
U19
TXRQ0
TXRQ1
TXRQ2
TXRQ3
J3
FV
R2
1K
+5VDCX
C1
68
C2
0.1
U17
DMA
02DMA
RES
DRQT
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
D0
D1
D2
D3
D4
D5
D6
D7
R/W
IRQ/DEND
TXSTB
CS
DGRNT
R3
3K
R7
3K
488IR-
A FROM SHT 3
IRQXX-
B FROM SHT 3
DMASEL
R6
10K
U21
ADXA00
ADXA01
ADXA02
ADXA03
ADXA04
ADXA05
ADXA06
ADXA07
ADXA08
ADXA09
ADXA10
ADXA11
ADXA12
ADXA13
ADXA14
ADXA15
C TO SHT 2
D TO SHT 3
DBUS00
DBUS01
DBUS02
DBUS03
DBUS04
DBUS05
DBUS06
DBUS07
E TO SHT 2
U20
MPU
HALT
NMI
IRQ
VCC
VSS
BA
TSC
VMA
DBE
02
01
RW
C6
22
C7
0.1
R1
10K
R4
1K
L1
10μH
C3
150PF
CA
390PF
C5
22
R8
18
R9
18
U26
DEM REQ
X1
X2
POW RES
GND
BLS02
MEM RDY
EXT
RES OUT
MEM CLK
2XFO
4XFO
DMA GNT
R10
68
U27
U23
J
CK
K
Q
U28
U29
RESET
PRWSTB
F TO SHT 3
02-STB
G TO SHT 3
DSPEL
H FROM SHT 3
02+STB
BUS02
J TO SHT 3
RDWRT-
K TO SHT 2
VMAXX+
L TO SHT 3
RDWRT+
M TO SHT 2
DMAGNT
N TO SHT 2

Figure 6-60. MPU PC Board A15A1, Schematic Diagram (Sheet 1 of 4)

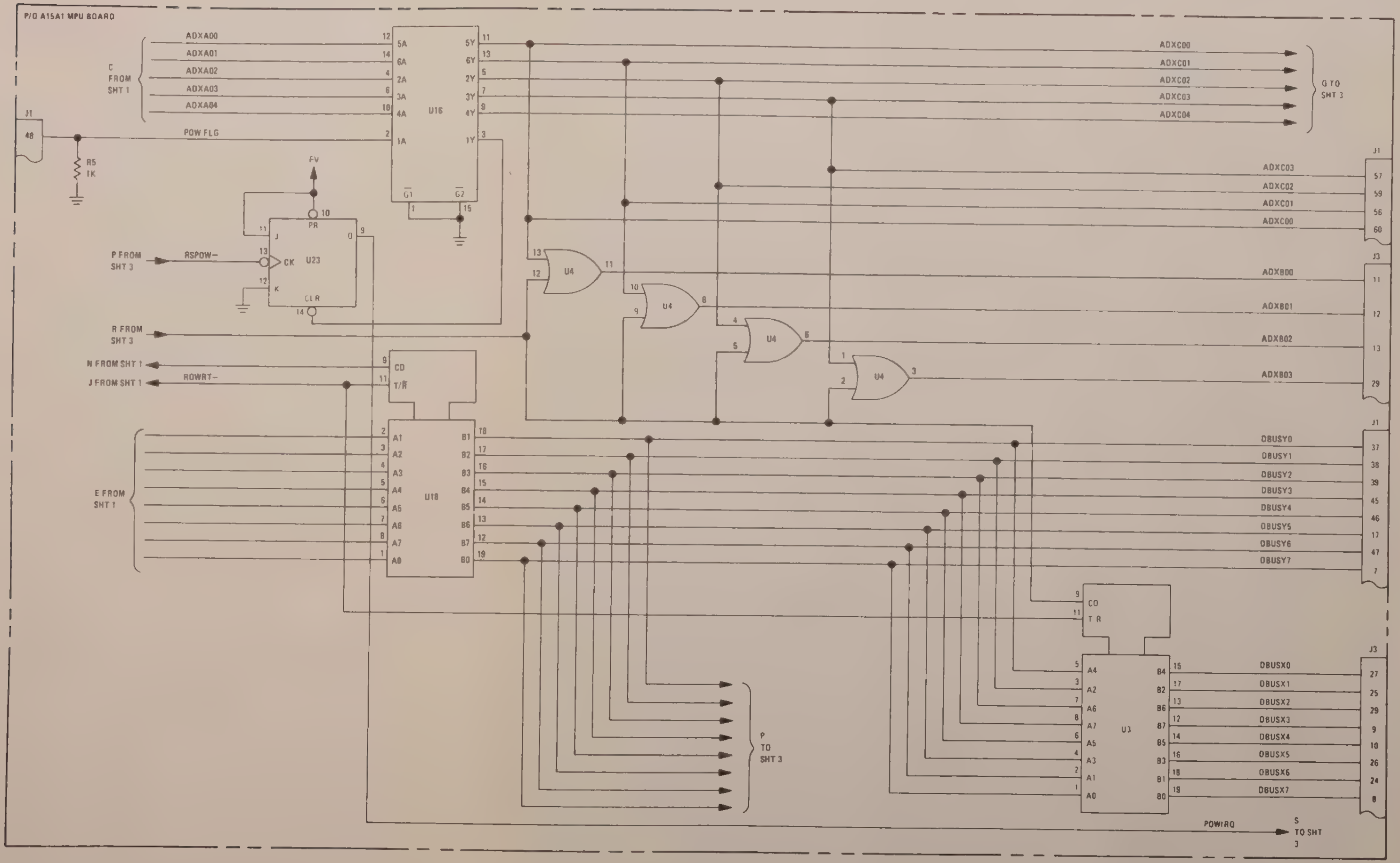

P/O A15A1 MPU BOARD
C FROM SHT 1
ADXA00
ADXA01
ADXA02
ADXA03
ADXA04
J1
48
POW FLG
R5
1K
U16
5A
6A
2A
3A
4A
1A
5Y
6Y
2Y
3Y
4Y
1Y
G1
G2
FV
U23
PR
J
CK
K
CLR
Q
P FROM SHT 3
RSPOW−
R FROM SHT 3
N FROM SHT 1
J FROM SHT 1
ROWRT−
CD
T/R
U18
A1
A2
A3
A4
A5
A6
A7
A0
B1
B2
B3
B4
B5
B6
B7
B0
E FROM SHT 1
U4
ADXC00
ADXC01
ADXC02
ADXC03
ADXC04
Q TO SHT 3
J1
ADXC03
ADXC02
ADXC01
ADXC00
57
59
56
60
J3
ADXB00
ADXB01
ADXB02
ADXB03
11
12
13
29
J1
DBUSY0
DBUSY1
DBUSY2
DBUSY3
DBUSY4
DBUSY5
DBUSY6
DBUSY7
37
38
39
45
46
17
47
7
CO
T R
U3
A4
A2
A6
A7
A5
A3
A1
A0
B4
B2
B6
B7
B5
B3
B1
B0
J3
DBUSX0
DBUSX1
DBUSX2
DBUSX3
DBUSX4
DBUSX5
DBUSX6
DBUSX7
27
25
29
9
10
26
24
8
P TO SHT 3
POWIRQ
S TO SHT 3

Figure 6-60. MPU PC Board A15A1 Schematic Diagram (Sheet 2 of 4)

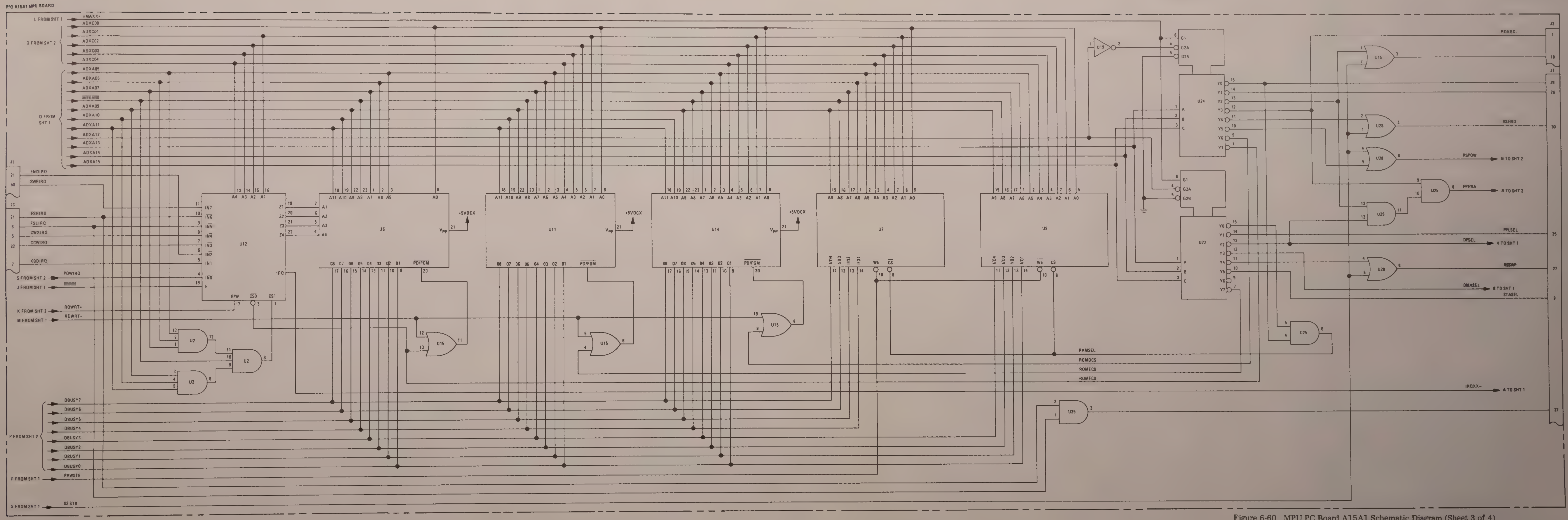

P/O A15A1 MPU BOARD
L FROM SHT 1
VMAXX+
Q FROM SHT 2
ADXC00
ADXC01
ADXC02
ADXC03
ADXC04
D FROM SHT 1
ADXA05
ADXA06
ADXA07
ADXA09
ADXA10
ADXA11
ADXA12
ADXA13
ADXA14
ADXA15
J1
ENDIRQ
SWPIRQ
J3
FSHIRQ
FSLIRQ
CWXIRQ
CCWIRQ
KBDIRQ
S FROM SHT 2
POWIRQ
J FROM SHT 1
K FROM SHT 2
ROWRT+
M FROM SHT 1
ROWRT-
U12
U2
U6
U11
U14
U7
U8
U15
U19
U24
U22
U25
U28
+5VDCX
PD/PGM
WE
CS
RAMSEL
ROMDCS
ROMECS
ROMFCS
P FROM SHT 2
DBUSY7
DBUSY6
DBUSY5
DBUSY4
DBUSY3
DBUSY2
DBUSY1
DBUSY0
F FROM SHT 1
PRWSTB
G FROM SHT 1
Q2 STB
RDKBD-
RSEND
RSPOW
N TO SHT 2
FPENA
R TO SHT 2
PPLSEL
DPSEL
H TO SHT 1
RSSWP
DMASEL
B TO SHT 1
STASEL
IRQXX-
A TO SHT 1

Figure 6-60. MPU PC Board A15A1 Schematic Diagram (Sheet 3 of 4)

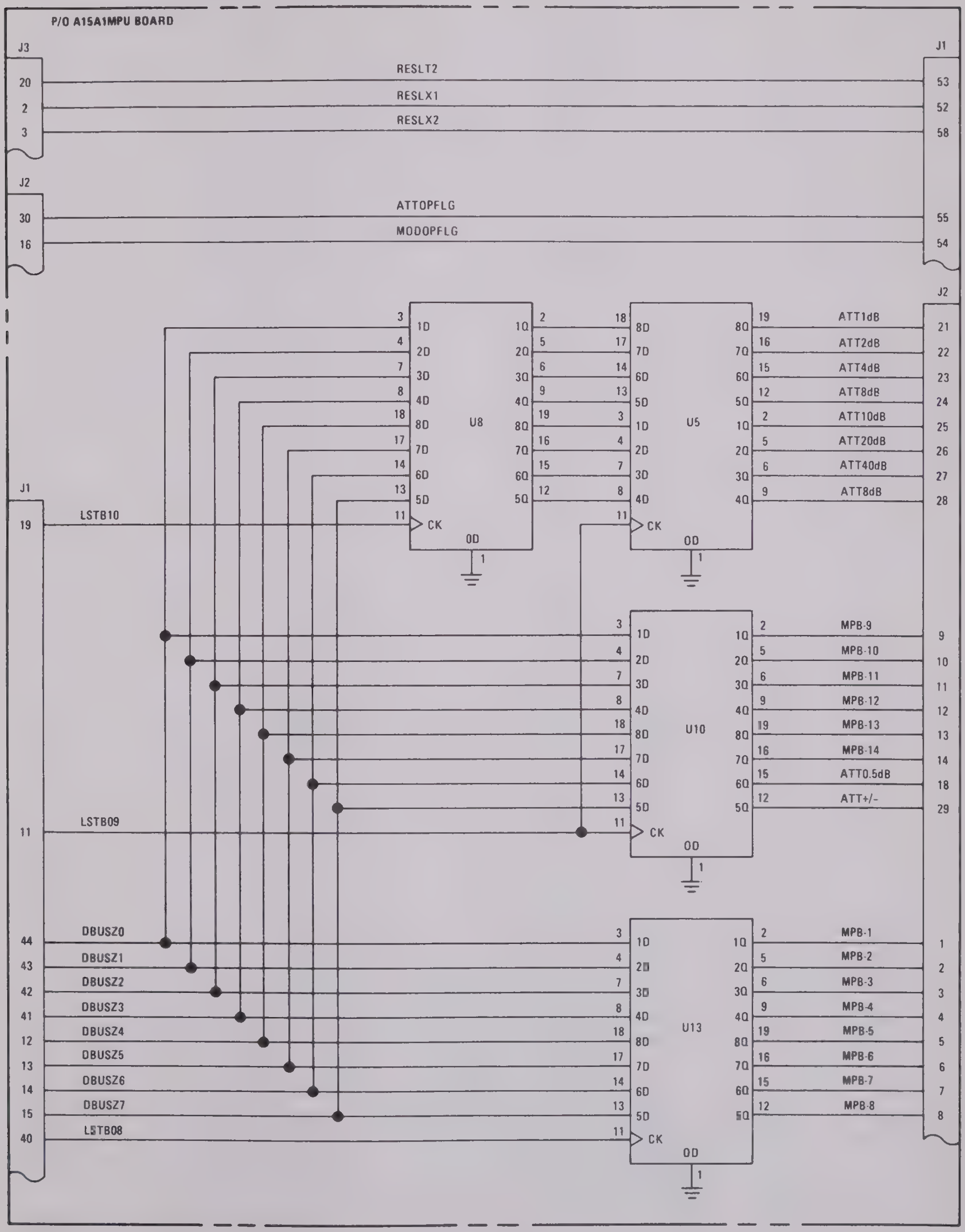

P/O A15A1MPU BOARD
J3
J1
J2
RESLT2
RESLX1
RESLX2
ATTOPFLG
MODOPFLG
U8
U5
U10
U13
LSTB10
LSTB09
LSTB08
DBUSZ0
DBUSZ1
DBUSZ2
DBUSZ3
DBUSZ4
DBUSZ5
DBUSZ6
DBUSZ7
ATT1dB
ATT2dB
ATT4dB
ATT8dB
ATT10dB
ATT20dB
ATT40dB
ATT8dB
MPB-9
MPB-10
MPB-11
MPB-12
MPB-13
MPB-14
ATT0.5dB
ATT+/-
MPB-1
MPB-2
MPB-3
MPB-4
MPB-5
MPB-6
MPB-7
MPB-8
CK
OD

Figure 6-60. MPU PC Board A15A1 Schematic Diagram (Sheet 4 of 4)

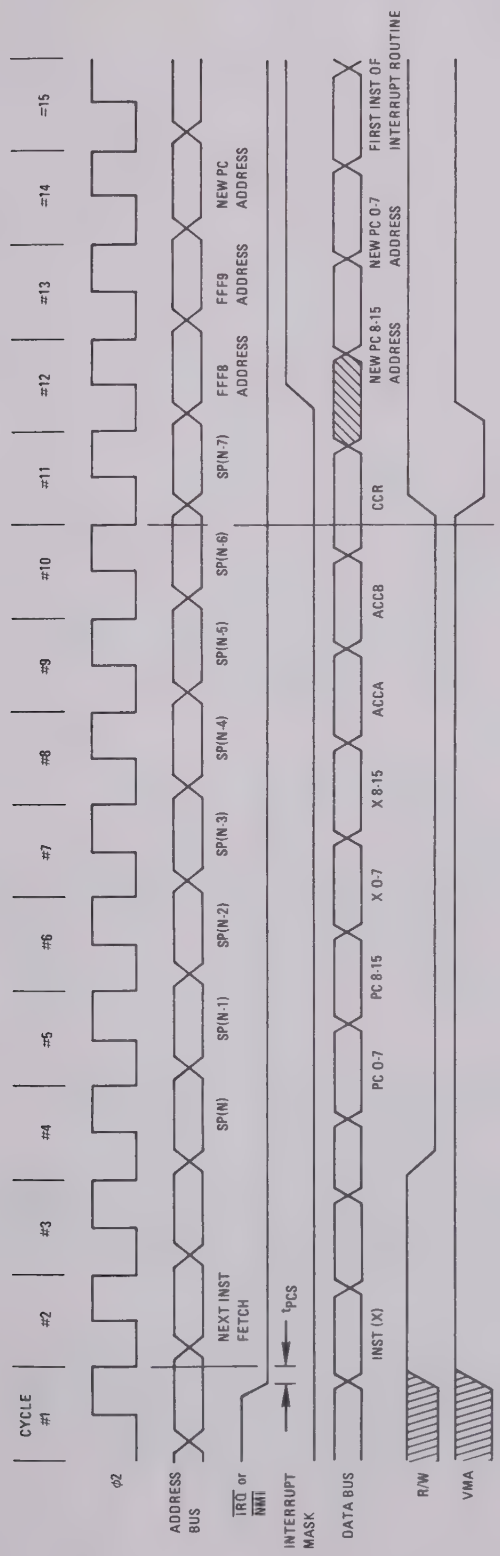

CYCLE #1
#2
#3
#4
#5
#6
#7
#8
#9
#10
#11
#12
#13
#14
#15
φ2
ADDRESS BUS
IRQ or NMI
INTERRUPT MASK
DATA BUS
R/W
VMA
NEXT INST FETCH
SP(N)
SP(N-1)
SP(N-2)
SP(N-3)
SP(N-4)
SP(N-5)
SP(N-6)
SP(N-7)
FFF8 ADDRESS
FFF9 ADDRESS
NEW PC ADDRESS
t_PCS
INST (X)
PC 0-7
PC 8-15
X 0-7
X 8-15
ACCA
ACCB
CCR
NEW PC 8-15 ADDRESS
NEW PC 0-7 ADDRESS
FIRST INST OF INTERRUPT ROUTINE

Figure 6-61. Interrupt Timing

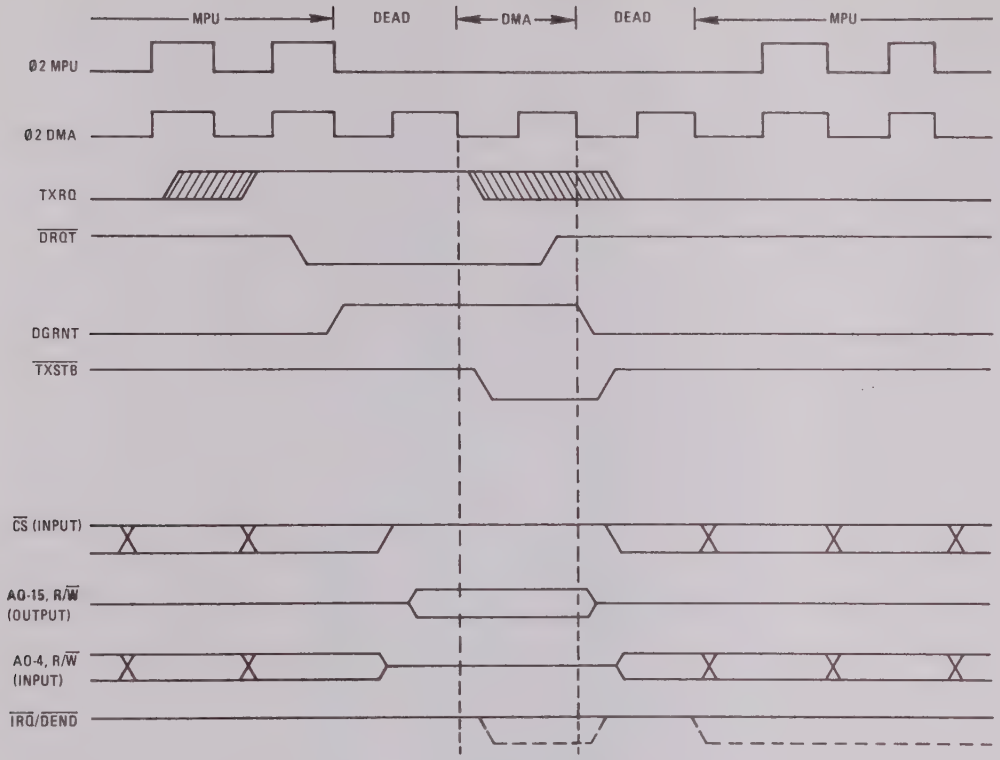


Figure 6-62. TSC Steal Mode

6-131. Input/Output Functions. (Table 6-4). The DMAC interfaces with the MPU through the eight-bit bidirectional data bus, the chip select line, five address lines, an interrupt request line, the read/write line, and the reset line. These signals, in conjunction with the VMA output, permit the MPU to have access to the DMAC. Four other lines associated with the MPU and the clock driver are the $\overline{\text{DRQT}}$, $\overline{\text{DRQH}}$, DGRNT, and the 02 DMA.

6-132. DMAC Registers. (Table 6-5). All of the fifteen registers in the DMAC are Read/$\overline{\text{Write}}$ registers, although some of the bits are read only status bits.

6-133. Timing Generator. The timing generator supplies the non-overlapping two phase clock signals required by the microprocessor. It also generates the power-on system reset signal and other signals used to synchronize the entire system. The major components of the timing generator are U26 and U23. (Table 6-6).

6-134. Oscillator. (Y1, C4, R4, L1, C3) A tank circuit tuned to 4 MHz connected between terminals X1 and X2 is used to prevent the oscillator from starting at other than the desired frequency. The 1000 ohm capacitor resistor (R4) reduces the Q sufficiently to maintain stable crystal control. C4 is used in series with the crystal for optimum performance at series resonance.

TABLE 6-4. INPUT/OUTPUT FUNCTIONS

Signal Name	Description
DMAC INTERFACE SIGNALS FOR THE MPU	
BIDIRECTIONAL DATA (D0-D7)	The bidirectional data lines (D0-D7) allow for data transfer between the DMAC and the MPU. The data bus output drivers are three-state devices that remain in the high impedance state except when the MPU performs DMAC read operations.
$\overline{\text{CHIP SELECT}}$/TRANSFER ACKNOWLEDGE B ($\overline{\text{CS}}$/TxAKB)	This line is multiplexed, serving both as an input and an output. $\overline{\text{CS}}$/TxAKB is an output in the four-channel mode during the DMA transfer. At all other times, it is a high impedance TTL compatible input used to address the DMAC. The DMAC is selected when $\overline{\text{CS}}$/TxAKC is LOW. VMA must be used in generating this input to insure that false selects will not occur. Transfers of data to and from the DMAC are then performed under the control of the 02 DMA, Read/$\overline{\text{Write}}$, and A0-A4 address lines. In the four-channel mode when TxAKB is needed, the $\overline{\text{CS}}$ gate must have an open-collector output (a pull-up resistor should not be used). In the two channel mode, $\overline{\text{CS}}$/TxAKB is always an input.
ADDRESS LINES (A0-A4)	Address lines A0-A4 are both input and output lines. In the MPU mode, these are high impedance inputs used to address the DMAC registers. In the DMA mode, these lines are outputs which are set to the contents of the Address Register of the channel being processed.
$\overline{\text{INTERRUPT REQUEST}}$/$\overline{\text{DMA END}}$ ($\overline{\text{IRQ}}$/$\overline{\text{DEND}}$)	$\overline{\text{Interrupt Request}}$/$\overline{\text{DMA End}}$ is a TTL compatible, active LOW output that is used to interrupt the MPU and to signal the peripheral controller that the data block transfer has ended. If the Interrupt has been enabled, the $\overline{\text{IRQ}}$/$\overline{\text{DEND}}$ line will go LOW after the last DMA cycle of a transfer. An open collector gate must be connected to DGRNT and $\overline{\text{IRQ}}$/$\overline{\text{DEND}}$ to prevent false interrupts from the DEND signal when interrupts are not enabled.

TABLE 6-4. INPUT/OUTPUT FUNCTIONS (Cont)

Signal Name	Description
READ/$\overline{\text{WRITE}}$ (R/$\overline{\text{W}}$)	Read/$\overline{\text{Write}}$ is a TTL compatible line that is a high impedance input in the MPU mode and an output in the DMA mode. In the MPU mode, it is used to control the direction of data flow through the DMACs input/output data bus interface. When Read/$\overline{\text{Write}}$ is HIGH (MPU read cycle) and the chip is selected, DMAC data output buffers are turned on and a selected register is read. When it is LOW, the DMAC output drivers are turned off and the MPU writes into a selected register. In the DMA mode, Read/$\overline{\text{Write}}$ is an output to drive the memory and peripheral controllers. Its stats is determined by bit 0 of the channel control register for the channel being serviced. When Read/$\overline{\text{Write}}$ is HIGH, the memory is read and the data from the memory is written into the peripheral controller. When it is LOW, the peripheral controller is read and its data stored in the memory. In the DMA mode, the DMAC data buffers are off, so data is not available on the Data Bus (D0-D7).
$\overline{\text{RESET}}$ ($\overline{\text{RES}}$)	The $\overline{\text{RES}}$ input provides a means of resetting the DMAC from an external source. In the LOW state, the $\overline{\text{RES}}$ input causes all registers, with the exception of the address and byte count registers, masks all interrupts, disables the data chain function, and puts each channel control register into the condition of memory write, halt steal transfer mode, and address increment.
TRANSFER SIGNALS TO THE MPU	
	Two DMA request output lines and a DMA Grant input line, together with the system clock, synchronize the DMAC with the MPU system.
$\overline{\text{DMA REQUEST THREE-STATE}}$ $\overline{\text{CONTROL STEAL}}$ ($\overline{\text{DRQT}}$)	This active LOW output requests a DMA transfer for a channel configured for the TSC steal transfer mode. This line is connected to the system clock driver, requesting a 01 clock stretch.

TABLE 6-4. INPUT/OUTPUT FUNCTIONS (Cont)

Signal Name	Description
DMA REQUEST HALT STEAL ($\overline{\text{DRQH}}$)	This active LOW output requests a DMA transfer for a channel programmed for the halt steal or halt burst mode transfer. This line is connected directly to the MPU $\overline{\text{HALT}}$ input and remains low until the last byte has begun to be transferred. This output is not used on the 380 Synthesized Signal Generator.
DMA GRANT (DGRNT)	This is a high impedance input to the DMAC, giving it control of the system busses. For the TSC Steal mode, the signal comes from the system clock drive circuit (DMA Grant), indicating that the clock is being stretched. For either of the halt modes, this signal is the bus available from the MPU, indicating that the MPU has halted and turned control of its busses over to the DMAC. For a design involving TSC steal and halt mode transfers, this input must be the OR of the clock driven DMA grant and the MPU BA.

TABLE 6-5. DMAC REGISTERS

Signal Name	Description
ADDRESS REGISTERS	
	Each channel has its own individual 16-bit address register. Before a DMA transfer is begun, the starting address for the transfer must be loaded into the address register. Depending on the state of the channel control register, bit 3, the address register will be decremented or incremented after each byte of transfer.
BYTE COUNT REGISTERS	
	Each channel also has its own byte count register. Before the DMA transfer, this register must be loaded with the number of bytes to be transferred. Since it is 16 bits in length, the transfer can be up to 65,536 bytes of data. The byte count register is decremented at the beginning of each DMA cycle.

TABLE 6-5. DMAC REGISTERS (Cont)

Signal Name	Description
CHANNEL CONTROL REGISTERS	
	The control of each channel's DMA transfer is programmed into its channel control register. Bits 4 and 5 are unused.
READ/$\overline{\text{WRITE}}$ (R/$\overline{\text{W}}$), Bit 0	The direction of the DMA transfer is controlled by this bit. When it is HIGH, the peripheral controller reads the memory. When it is LOW, the transfer will be in the opposite direction, thus writing into the memory. The system R/$\overline{\text{W}}$ line is in the same state as this R/$\overline{\text{W}}$ bit in the DMA mode. The device controller must change the sense of its R/$\overline{\text{W}}$ input during the DMA mode.
BURST/STEAL, Bit 1	This bit, along with bit 2, selects the mode of the DMA transfer. With bit 1 HIGH, the burst mode is selected. A LOW selects the steal mode.
TSC/HALT, Bit 2	Bit 2 helps select the mode of DMA transfer. When this bit is HIGH, the TSC mode is selected. When LOW, the halt mode is selected.
ADDRESS UP/DOWN, Bit 3	Bit 3 controls the change in the address register for each DMA cycle. If this bit is LOW, the address register will be incremented each time the byte count register decrements. If the bit is HIGH, the address register will decremented.
BUSY/READY FLAG, Bit 6	The Busy/Ready Flag is a read only status bit that indicates a DMA transfer is in process on that channel. This bit goes HIGH at the beginning of the transfer and remains so until the $\overline{\text{IRQ}}$/$\overline{\text{DEND}}$ has been LOW for one cycle (DMA End). The bit is then reset and the channel can again be configured for a transfer.
DMA END FLAG (DEND), Bit 7	The DEND bit indicates a DMA block transfer has ended. This bit is set at the same time the Busy/Ready Flag is reset. The DEND bit is reset by the MPU reading the channel control register. This bit causes an interrupt if enabled in the interrupt control register.

TABLE 6-5. DMAC REGISTERS (Cont)

Signal Name	Description
PRIORITY CONTROL REGISTER	
	The enabling and prioritizing of the transfer request (TxRQs) are done in the priority control register. Bits 4 through 6 are unused.
REQUEST ENABLE (RE0-3), BIT 0-3	The four channels are individually enabled by setting the respective RE bit HIGH. A LOW on any of these bits disables recognition of the transfer request for that channel. The bit number equals the channel number (i.e., bit 2 equals channel #2).
ROTATE CONTROL, Bit 7	The DMAC priority service routine is selected by this rotate control bit. When it is LOW, the fixed mode is selected. Channel #0 has the highest priority, channel #1 the next highest, and so on down. When this bit is HIGH, a rotating routine is used. This routine states that initially it will be the same as in the fixed mode. But once a channel has been serviced, it moves to the lowest priority and those that were below it advance to the next highest priority.
INTERRUPT CONTROL REGISTER	
	An interrupt is caused by a channel completing its DMA block transfer. DEND (channel control register, bit 7) flags this condition for each channel. Bits 4 through 6 are unused.
DEND IRQ ENABLE (DIE0-3) Bits 0-3	Each channel is separately enabled to cause the interrupt. A HIGH enables an interrupt from the channel; a LOW masks the interrupt. The bit number equals the channel number (i.e., bit 2 equals channel #2).
DEND IRQ FLAG, Bit 7	This read only bit indicates an IRQ is requested of the MPU when it is HIGH. If the interrupt is enabled (DIE = 1) when a channel's DEND flag (channel control register, bit 7) goes HIGH, the DEND IRQ Flag bit also goes HIGH. It is reset by the MPU reading the channel control register that caused the interrupt.

TABLE 6-5. DMAC REGISTERS (Cont)

Signal Name	Description
DATA CHAIN REGISTER	
	Repetitive reading or writing of a block of memory can be done in the data chain function. A DMA transfer cannot be active on channel #3 during the data chain. Bits 4 through 7 are unused.
DATA CHAIN ENABLE (DCE), Bit 0	The data chain function is enabled when this bit is HIGH.
DATA CHAIN CHANNEL SELECTS A, B (DCA, DCB), Bits 1 and 2	The channel updated by data chaining is selected by bits 1 and 2. The data chain function is performed by transferring the contents of channel #3 address and byte count registers into the respective registers of the channel selected by bits 1 and 2. This transfer is done during the cycle of 02 DMA following the byte count register having decremented to zero.
TWO/FOUR CHANNEL SELECT (2/4), Bit 3	The DMAC is configured to handle two or four channels by bit 3. This bit HIGH selects the 4-channel mode. In this mode, the $\overline{\text{CS}}$/TxAKB becomes a chip select in the MPU mode and a transfer acknowledge B for the DMA mode. With bit 3 LOW, the 2-channel mode is selected, and the $\overline{\text{CS}}$/TxAKB line is always a chip select, both for the MPU and the DMA mode.
02 DMA	Transferring in and out of the DMAC registers, sampling of channel request lines and gating of other control signals to the system is done internally in conjunction with the 02 DMA high impedance input. This input must be the system memory clock (non-stretched 02 clock).
TRANSFER SIGNALS FROM THE PERIPHERAL CONTROLLER	
TRANSFER REQUEST (TxRQ0-3)	Each of the four channels has its own high impedance input request for transfer line. The peripheral controller requests a transfer by setting its TxRQ line HIGH (a logic 1). The lines

TABLE 6-5. DMAC REGISTERS (Cont)

Signal Name	Description
TRANSFER REQUEST (TxRQ0-3) (Cont)	are sampled according to the priority and enabling established in the priority control register. In the steal mode and the first byte of the halt burst mode, the TxRQ signals are tested on the positive edge of 02 DMA and the highest priority channel is strobed. Once strobed, the TxRQs are not tested until that channel's data transfer is finished. In the succeeding bytes of the halt burst mode transfer, the TxRQ is tested on the negative edge of 02 DMA, and data is transferred on the next 02 DMA cycle if TxRQ is HIGH.
$\overline{\text{TRANSFER STROBE}}$ ($\overline{\text{TxSTB}}$)	The $\overline{\text{Transfer Strobe}}$ causes acknowledgement to be given to the peripheral controller and transfers the data to or from the memory. This line is also intended to be the VMA signal for the system in the DMA mode.
$\overline{\text{INTERRUPT REQUEST}}$/$\overline{\text{DMA END}}$ ($\overline{\text{IRQ/DEND}}$)	In the DMA mode, this dual purpose line is LOW for the last byte of transfer, indicating a DMA end. This occurs when the byte counter register decrements to zero. This line, through the decode of TxAKA and $\overline{\text{CS}}$/TxAKB, can be used to strobe a DMA end to each device controller.
ADDRESS LINES TO THE MEMORY	
ADDRESS LINES A0-A15 (A0-A15)	These output lines are in the high impedance state during the MPU mode. In the DMA mode, these lines are outputs which are set to the contents of the address register of the channel being processed.

6-135. Signals 02+STB, 02-STB. These signals are used as enable strobes which have the proper timing relation to MPU 02 and MEM CLK so that data and the various reset signals will be strobed at the proper time. These signals have been designed to provide the proper amount of both setup time and hold time required by the various data latches in the system.

TABLE 6-6. TIMING GENERATOR U26 PIN FUNCTIONS

Pin Functions	Description
4xfo	A free running oscillator at four times the MPU clock rate of 1 MHz.
2xfo	A free running oscillator at two times the MPU clock rate.
DMA REQ	An asynchronous input used to freeze the MPU clocks in the 01 high 02 low state for cycle steal DMA.
DMA GNT	A synchronous output used to synchronize the DMA operation of the MPU.
MEM RDY	This input is not used.
01, 02	Capable of driving the 01 and 02 inputs on MPU U20 at 1 MHz.
BUS 02	An output nominally in phase with MPU 02.
MEM CLK	An output nominally in phase with MPU 02 which free runs even during the DMA request cycle.
POW RES	A Schmitt trigger input which controls RES OUT. The internal 50 k resistor to Vcc and the external 22 μf capacitor sets the time constant of about 1 second.
RES OUT	The power-on reset output used to initialize the system at power-on.
X1, X2	Provision to attach the 4 MHz series resonant oscillator.
EXT	This input is not used.

6-136. These signals are generated by the 4xfo, 2xfo and MEM CLK outputs of U26. The falling edge of 4xfo triggers the transition of the Q and $\overline{Q}$ outputs of U23. The generation of these signals is illustrated in Figures 6-63 and 6-64. The timing relation between 2xfo, 4xfo and MEM CLK are shown in Figure 6-64. The state table of the actual circuit is shown in Figure 6-64.

6-137. **Data Bus Enable (DBE).** The 02+STB is used specifically to generate the rising edge of the DBE (U20-36) (Data Bus Enable) signal. This is done to provide the long data setup time (140 nanoseconds) required by the CMOS latches located on the latch board and elsewhere. The falling edge of DBE is governed by the falling edge of bus 02, which occurs approximately 100 nanoseconds after the falling edge of 02+STB, thereby supplying ample data hold time for the various latches.

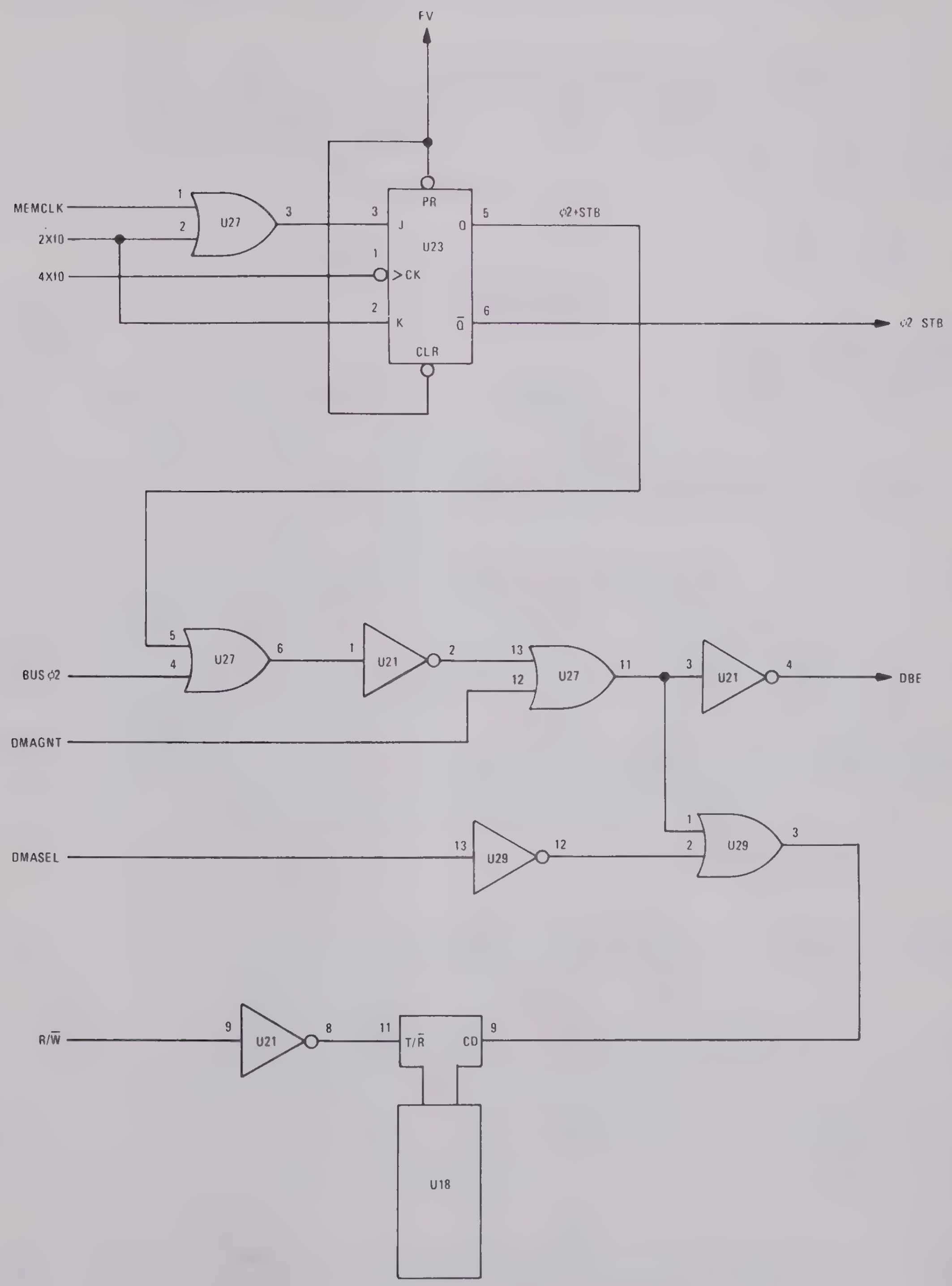
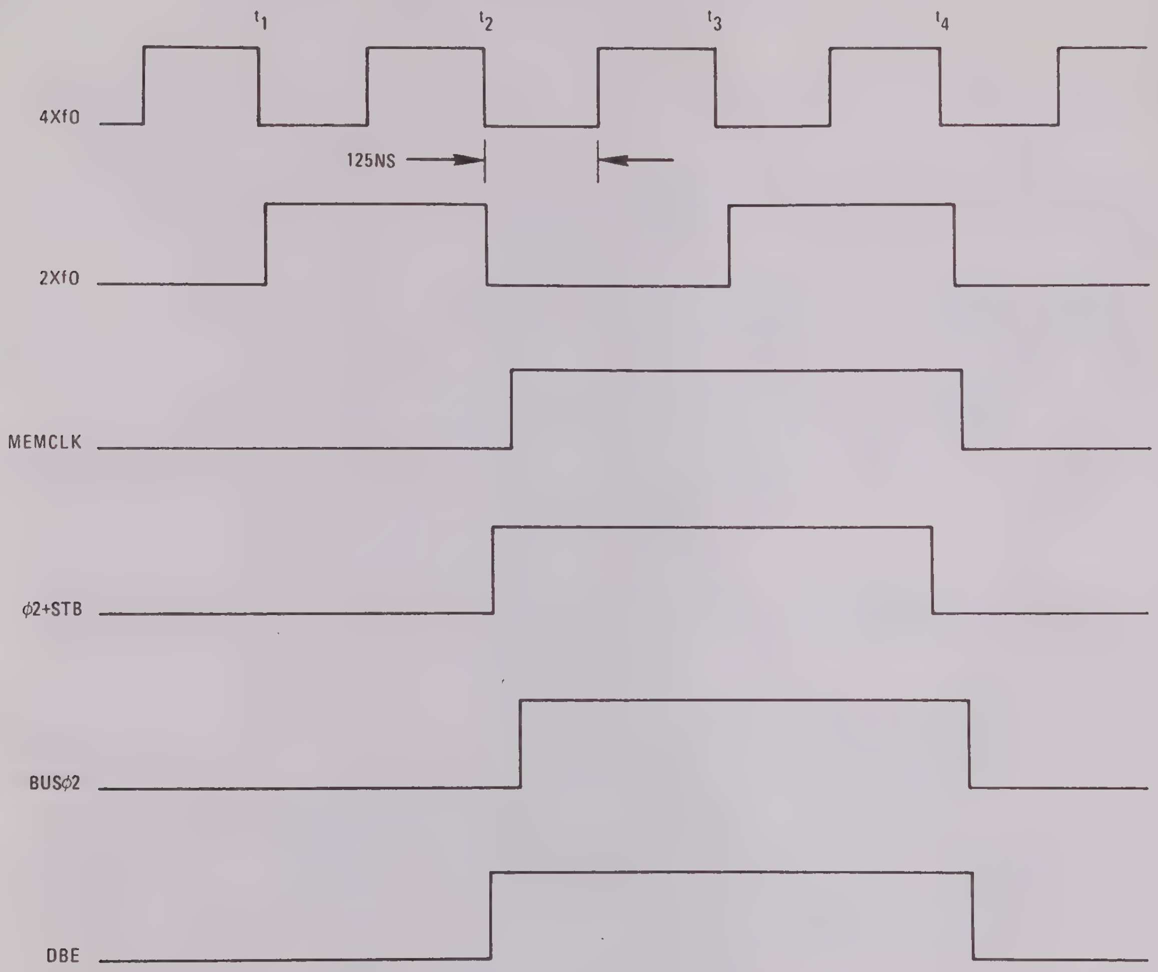

FV
MEMCLK
2X10
4X10
U27
U23
PR
J
Q
> CK
K
Q̄
CLR
φ2+STB
φ2 STB
BUS φ2
DMAGNT
DMASEL
U21
U27
DBE
U29
R/W̄
T/R̄
CD
U18

Figure 6-63. DBE Generator


t1
t2
t3
t4
4XfO
125NS
2XfO
MEMCLK
ϕ2+STB
BUSϕ2
DBE

	J	K	Q	Q′
t_1	0	0	0	0
t_2	1	1	0	1
t_3	1	0	1	1
t_4	1	1	1	0

WHERE J = MEMCLK+2XfO
K = 2XfO
Q = THE VALUE OF ϕ2+STB AT TN
Q′ = THE VALUE OF ϕ2+STB AT TN+TD
TD = THE PROPOGATION DELAY FROM THE CLOCK INPUT TO THE Q OUTPUT.

Figure 6-64. Time Relations

6-138. If the DMAGNT (DMA Grant) signal U26-11 goes high, the DBE signal will not be allowed to go high. This is done to prevent the MPU from enabling its data bus at the same time as the DMA is enabling its data bus.

6-139. The inverse of DBE U27-11, is used to enable the octal transceiver U18 which buffers the data lines of the MPU and DMA from the rest of the system. When the DMASEL line is active (low), U18 will be forced into the disabled mode (U18-9 high). This is done to prevent the bus conflict which would otherwise occur when the MPU is reading data from the DMA. This conflict would be between the DMA data outputs and the U18 receiver drivers.

6-140. **CLOCK OSCILLATOR BOARD P/O A15A1.** (Figures 6-65 and 6-66). The 1 MHz clock oscillator board was designed to substitute for the MC6875 MPU clock generator.

6-141. The major component on the 1 MHz clock oscillator board is the two-phase microprocessor clock driver U1. This device contains the 4 MHz crystal, the oscillator circuit, the NMOS and TTL drivers, and the waveshaping and interface circuitry; all the components necessary to provide the critical non-overlapping two-phase waveforms used by the MPU. It provides the BOS 02, MEMCLK and 4xfc outputs as well. Additional circuitry is used to provide the 2xfc, $\overline{\text{DMAREQ}}$, DMAGNT and the RESET- signals.

6-142. The $\overline{\text{RESET OUT}}$ signal which comes out on pin 14 will be low for about 2 seconds when power is initially applied to the circuit. When capacitor C3 charges sufficiently through resistor R2, the output will go high. The diode CR1 will permit fast discharging of capacitor C3 when power is removed.

6-143. The 2xfc signal is not an output of U1 and had to be created externally. In order to provide the 2xfc signal, two timing requirements had to be met.

a. 2xfc must transition (high to low, or low to high) on the falling edge of the 4xfc signal.

b. 2xfc must be low following the falling edge of the MEMCLK signal.

6-144. These requirements are met by the circuitry. When 4xfc goes high the output of U4-2 goes low thereby toggling Q output U2-5 (2xfc).

6-145. When the 02 TTL UNGATED output (same as MEMCLK) goes low, U3-3 output goes high causing one-shot output U5-1 to pulse low for about 130 nanoseconds. This will force 2xfc low.

6-146. The $\overline{\text{DMAREQ}}$ and DMAGNT functions are also created with external circuitry. The status of the $\overline{\text{DMAREQ}}$ line is sampled on the 2xfc signal, allowing the clock output signals to be stretched in full clock period increments. If the $\overline{\text{DMAREQ}}$ line is low during a falling edge of 2xfc and remains low beyond a second falling edge of 2xfc, clock signal stretching will occur which holds 01 high and 02 and BUS 02 low. The clock signals will remain stretched for a maximum of one clock period after the $\overline{\text{DMAREQ}}$ line returns high. The MEMCLK output maintains a normal period.

6-147. **Address Decoder.** The function of the address decoder is to decode the four high order address lines from the MPU to create the various chip-selects and reset pulses used in the system.

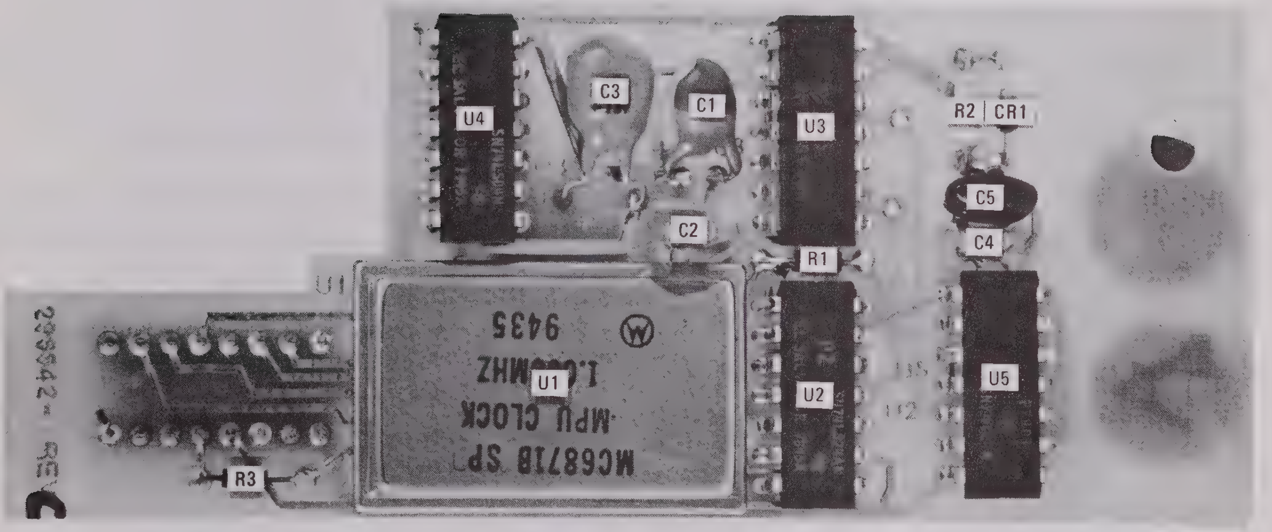

U4
C3
C1
U3
R2
CR1
C5
C2
C4
R1
U1
U2
U5
R3

Figure 6-65. 1 MHz Clock Oscillation P/O A15A1, Component Locations

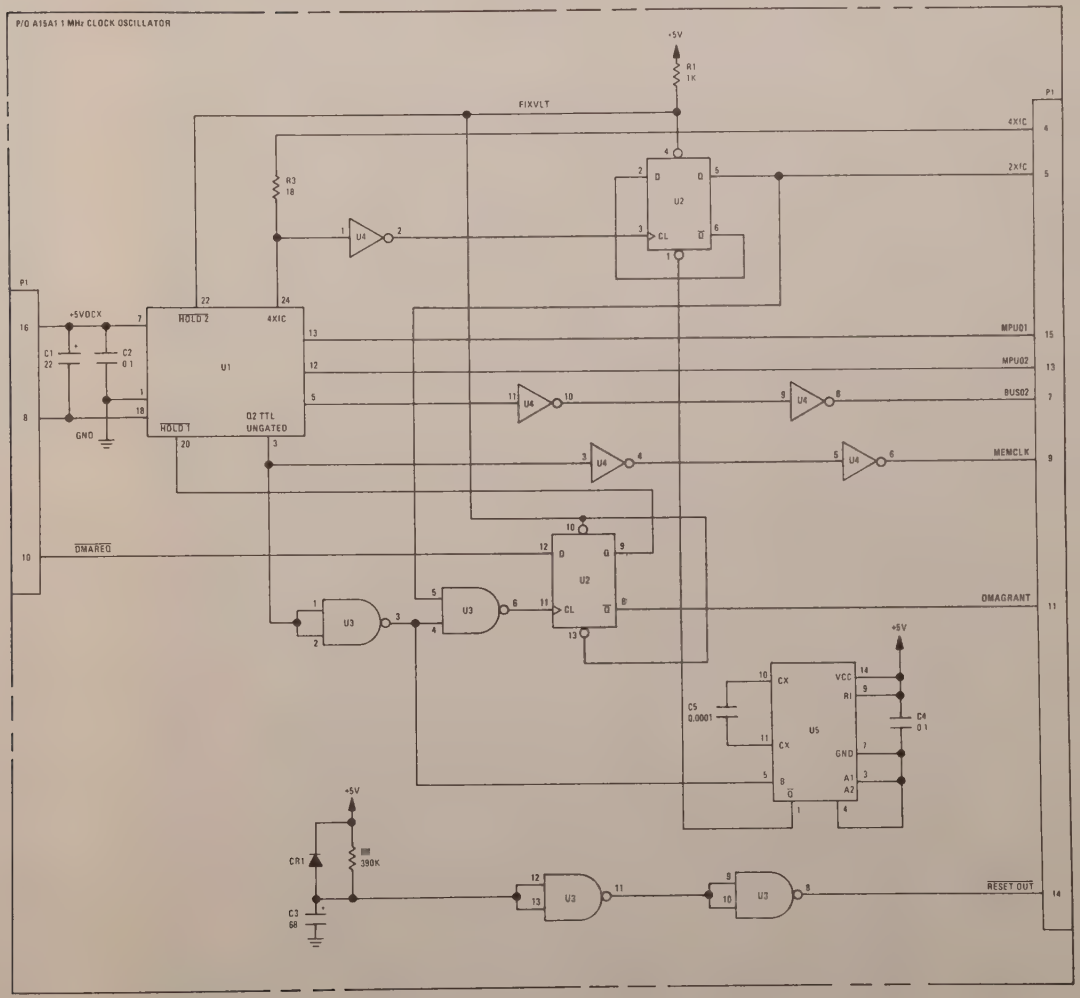

P/O A15A1 1 MHz CLOCK OSCILLATOR
+5V
R1
1K
FIXVLT
P1
4XIC
4
2XIC
5
U2
D
Q
CL
$\overline{Q}$
R3
18
U4
P1
16
8
10
+5VDCX
C1
22
C2
0 1
GND
U1
$\overline{HOLD\ 2}$
4XIC
$\overline{HOLD\ 1}$
Q2 TTL
UNGATED
MPU01
15
MPU02
13
BUS02
7
MEMCLK
9
$\overline{DMAREQ}$
U3
DMAGRANT
11
U5
CX
VCC
RI
GND
A1
A2
B
$\overline{Q}$
C5
0.0001
C4
0 1
+5V
CR1
390K
C3
68
$\overline{RESET\ OUT}$
14

Figure 6-66. 1 MHz Clock Oscillator P/O A15A1, Schematic Diagram

6-148. The inputs to the decoder are ADXA12 through ADXA15 (the high order address lines), VMAXX+ and 02-STB (Figure 6-67). The VMAXX+ is the logical OR of VMA from the MPU and TXSTB+ (the inverted $\overline{\text{TXSTB}}$ from the DMA). This signal, when high, enables the two 3-to-8 decoders, U24 and U22. The A, B and C inputs of each of these decoders are driven by ADXA13, ADXA14 and ADXA15 respectively. ADXA12 is used for active low enable of U22-4 directly and the inverted ADXA12 is used for an active low enable of U24-4. This provides a total of 16 decoded outputs of which 15 are used. Most of these outputs are used directly as chip-selects and enables, some must be gated with 02-STB. This is done for all the reset signals (RSFPL-, RSEND-, RSPOW-, RSSWP). This is done to prevent spikes which could be generated between cycles, from strobing these reset lines at the wrong time.

6-149. The FPENA- signal U25-8 goes active (low) when any one of three front panel activities are being enabled. These three activities are RDKBD- U24-12 read keyboard, RSFPL- U24-13 reset front panel, and DSPSEL U22-13 display select. The FPENA- signal, when active, will enable the front panel address buffer U4, and the front panel data buffer U3. The front panel address and data buffers are only enabled at these times in order to keep those lines static when there is no communications with the front panel. This was done to minimize noise propagation to the RF systems.

6-150. The RAMSEL signal is generated when either U22-15 or U24-15 go active (low). U22-15 is the normal address decode for RAM. U24-15 is the LATSEL signal (Latch Select) and is made active when data is being written into the data latches. Therefore, whenever data is written into the data latches, it is also written into RAM. This is done to allow the MPU to have access to the data which was written into the frequency data latches during a fast learn sequence.

6-151. **Random Access Memory (RAM).** The RAM used in the 380 consists of two packages which contain 4096 bits of static read/write memory organized in 1024 words by 4 bits.

6-152. The ten address lines (A0-A9) (Figure 6-60) as well as the CS (chip select) and $\overline{\text{WE}}$ (write enable) are commonly bussed to each chip, thereby allowing parallel access to the 1024 words of each chip. The three-state data outputs of U7 are tied to DBUSY4 through DBUSY7 and the three state data outputs of U9 are tied to DBUSY0 through DBUSY3. This arrangement gives a full eight bit byte for each address. The chip select ($\overline{\text{CS}}$) input allows selection of the RAM for reading or writing.

6-153. The write enable ($\overline{\text{WE}}$) must pulse low during the write cycle. This signal is the PRWSTB (peripheral write strobe) which is the logical OR of RD/WRT and 02-STB. Therefore, this pulse will occur during every cycle in which RD/WRT is low (write mode). Data will only be written, however, when the $\overline{\text{CS}}$ input is active (low). The $\overline{\text{WE}}$ input remains high during the read cycle.

6-154. **Read Only Memory (ROM).** The system has been designed to utilize three EPROMS. The EPROM is a 32,768 bit, ultraviolet light erasable, electrically programmable read-only memory. The data outputs are three-state, which allows OR- tying multiple devices on a common bus. Each of the three ROM's contain 4096 bytes of program memory, for a total of 12,288 bytes. Standard systems will have only two ROM's installed with the third available for optional expansion.

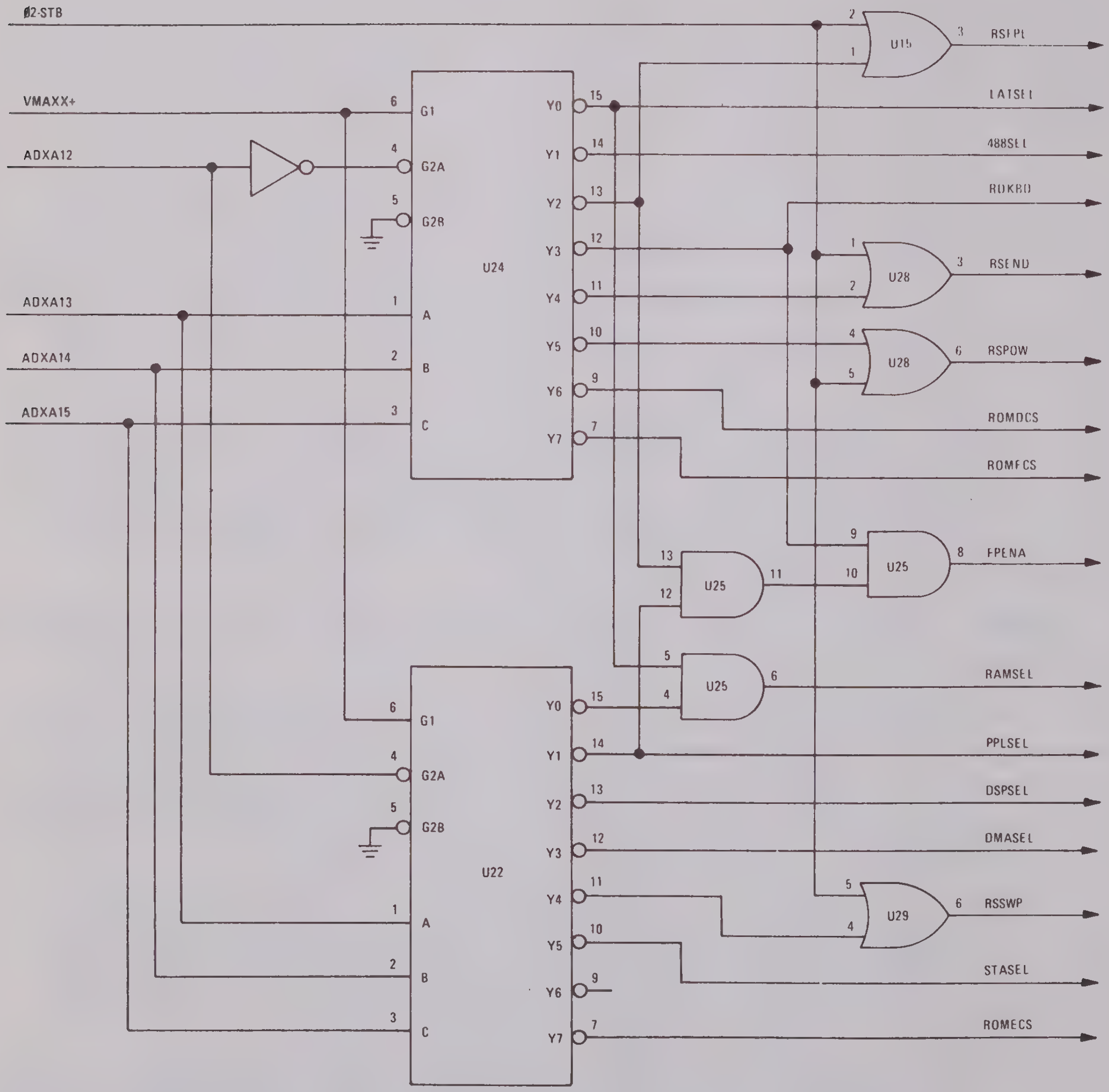

Ø2-STB
VMAXX+
ADXA12
ADXA13
ADXA14
ADXA15
U24
G1
G2A
G2B
A
B
C
Y0
Y1
Y2
Y3
Y4
Y5
Y6
Y7
U15
U28
U28
U25
U25
U25
U22
U29
RSFPL
LATSEL
488SEL
RDKBD
RSEND
RSPOW
ROMDCS
ROMFCS
FPENA
RAMSEL
PPLSEL
DSPSEL
DMASEL
RSSWP
STASEL
ROMECS

Figure 6-67. Address Decoder Diagram

6-155. The outputs of the ROM are enabled when a low level signal is applied to the PD/$\overline{\text{PGM}}$ (pin 20) input. When this pin goes high, the outputs in a high-impedance state and the active power dissipation of the device is cut by 80%.

6-156. The U14 ROM is selected when the four high order address lines code a hexadecimal D. The U11 ROM is selected with an E and the U6 ROM is selected with an F. The selected ROM outputs will be enabled only if the $\overline{\text{RD/WRT}}$ signal is low (read mode).

6-157. The address inputs and data outputs of all three ROM's are commonly bussed, with the exception of A1 through A4 of the U6 ROM. These address inputs come from the priority interrupt controller.

6-158. **Priority Interrupt Controller (PIC).** The priority interrupt controller (PIC-U12) is used to add prioritized response to system interrupts and eliminates the need of polling routines to determine which interrupt has been enabled. The primary purpose of the priority interrupt controller (PIC) is to generate a modified address to ROM in response to prioritized interrupts. With the PIC, each interrupting device is assigned a unique ROM location which contains the starting address of the appropriate service routine. After the MPU detects and responds to an interrupt the PIC directs the MPU to the proper memory location.

6-159. The basic functions of the PIC are shown in Figure 6-68. The 8-bit request register is an edge clocked D-type register with internal 6 k pullup resistors on the interrupt inputs ($\overline{\text{IN0}}$ through $\overline{\text{IN7}}$). Note the inputs are active low. The interrupt register is loaded on the falling edge of the enable when the PIC is not selected.

6-160. The 1-of-8 priority encoder enables a vector corresponding to the stored interrupt with the highest priority and places it on the vector input port of a data selector. In addition an interrupt request signal $\overline{\text{IRQ}}$ is generated to signal the MPU that an interrupt has been detected. The mask location register overides all interrupts with priority below the mask level. The mask can be thought of as a movable partition allowing responses to inputs equal to or greater than the mask value. For example, if the stored mask level was 4, inputs $\overline{\text{IN0}}$, $\overline{\text{IN1}}$, $\overline{\text{IN2}}$, and $\overline{\text{IN3}}$ would not generate an interrupt to the MPU system. The input request register is not affected by the mask, and if the mask is cleared (by loading it with zeros) any previously stored inputs will generate an $\overline{\text{IRQ}}$ signal.

6-161. The chip select and decode circuitry controls all internal functions of the PIC. The selected mode is defined as the logical AND function $\overline{\text{A1}} \bullet \overline{\text{A2}} \bullet \text{A3} \bullet \text{A4} \bullet \overline{\text{CS0}} \bullet \text{CS1} \bullet \text{R/W}$. When the device is not in the selected mode the request register clock is enabled and the address inputs A_i pass directly through the data selector to the Z_i outputs. When the MPU responds to interrupt request $\overline{\text{IRQ}}$ and the PIC decodes the select address, the request register is inhibited and the data selector places the vector on the Z outputs.

6-162. The decode logic controls the loading of the mask location register. This register will be loaded on the falling edge of the enable pulse when enabled by the logical AND function $\overline{\text{CS0}} \bullet \text{CS1} \bullet \overline{\text{R/W}}$. Contrary to normal read/write operations in MPU systems, the data written into the mask register are bits A1 through A4 of the address bus. This means that in the load mask mode the data on the data bus is a don't care. However, in this mode the ROM will also be accessed and both the ROM and MPU will be driving the data bus. Therefore, the read/write line is used as an active high chip select or enable signal for ROM decoding.

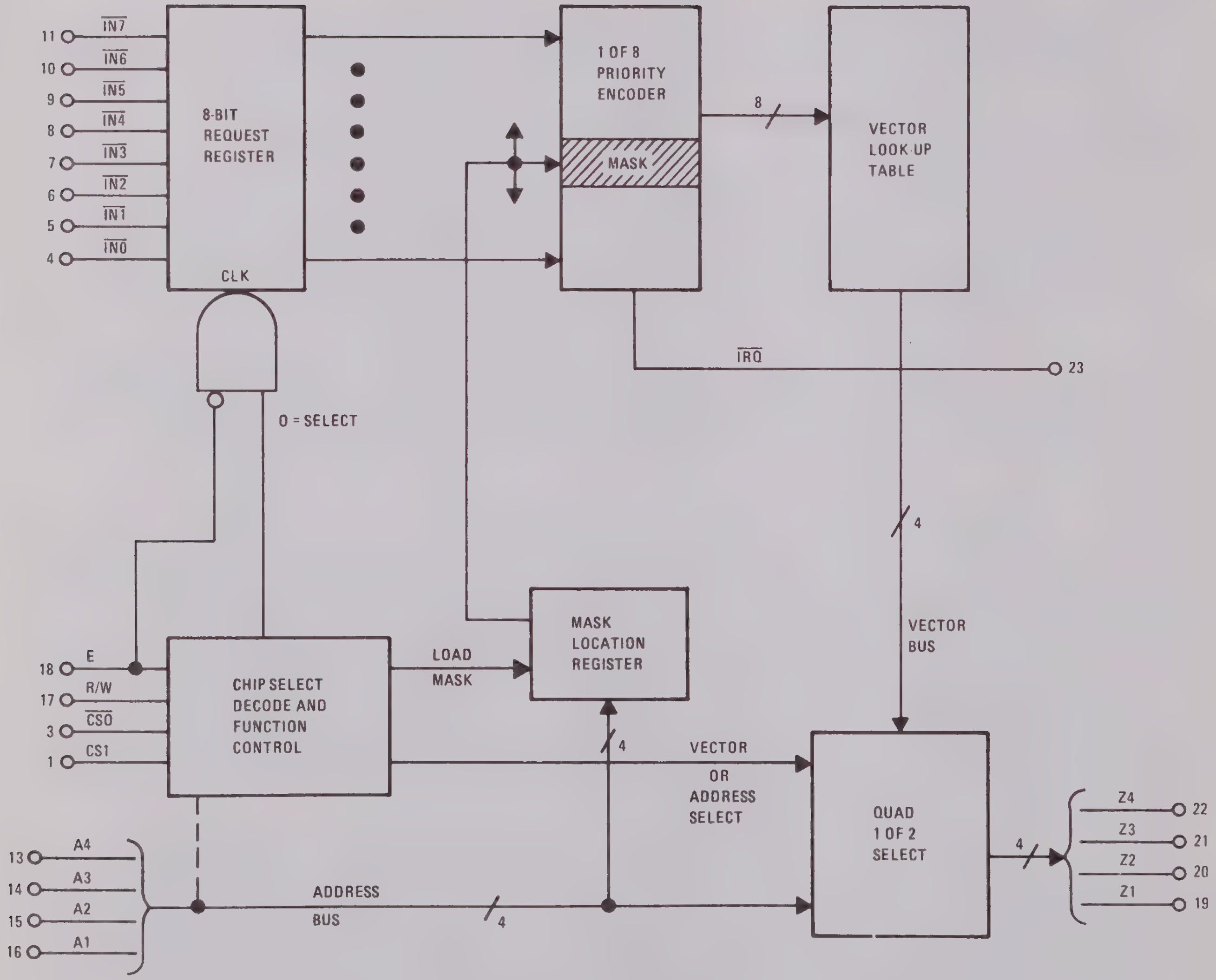

11
$\overline{IN7}$
10
$\overline{IN6}$
9
$\overline{IN5}$
8
$\overline{IN4}$
7
$\overline{IN3}$
6
$\overline{IN2}$
5
$\overline{IN1}$
4
$\overline{IN0}$
8-BIT REQUEST REGISTER
CLK
1 OF 8 PRIORITY ENCODER
MASK
8
VECTOR LOOK-UP TABLE
$\overline{IRQ}$
23
0 = SELECT
4
VECTOR BUS
E
18
R/W
17
$\overline{CS0}$
3
CS1
1
CHIP SELECT DECODE AND FUNCTION CONTROL
LOAD MASK
MASK LOCATION REGISTER
4
VECTOR OR ADDRESS SELECT
QUAD 1 OF 2 SELECT
4
Z4
22
Z3
21
Z2
20
Z1
19
13
A4
14
A3
15
A2
16
A1
ADDRESS BUS
4

Figure 6-68. Priority Interrupt Controller

6-163. When the MPU responds to an interrupt, it attempts to read locations FFF8 and FFF9 (Hex) of ROM to get the vector to the interrupt service routine. The function of the PIC is to modify the FFF8 and FFF9 addresses to the ROM according to which interrupt has caused the $\overline{\text{IRQ}}$ (pin 23) to activate. The only bits which are modified are the A1 through A4 bits. The A5 through A15 bits will enable the PIC when all are high (as when addresses FFF8 and FFF9 are active). Bits A12 through A15 are decoded by the address decoder. An F code on these bits will activate $\overline{\text{CSO}}$ (pin 3). The A05 through A11 are ANDed together (via U2) to create the CS1 enable (pin 1).

6-164. Data Latches for Attenuation and Modulation Programming Bits. The data latches for the ten attenuation and fourteen modulation programming bits, resides on the MPU board. The data inputs to these latches come in on the 8 bit bus (DBUS Z0 through DBUS Z7) from the latch board. The latch strobes LSTB08, LSTB09 and LSTB10 also come from the latch board.

6-165. These latches are the CM0S type, 8 bit storage elements with three-state outputs. These outputs have been permanently enabled by tying the OUTPUT DISABLE input (pin 1) to ground. This latch is an 8-bit D type positive edge triggered flip-flop. Data at the D inputs, meeting the set-up and hold time requirements, is transferred to the Q outputs on positive going transitions of the CLOCK input (pin 11).

6-166. In order to insure that all ten attenuation bits change simultaneously, the ATT 1dB through ATT 80 dB bits have a two stage latch. When it is desired to change the output amplitude, the MPU first loads the ATT 1 through 80 dB bits into the first stage latch U8 (LSTB10 is activated). The MPU will then load U10 (by activating LSTB09) with the required ATT +1-, ATT 0.5 dB and MPB-9 through 14 bits. When LSTB09 activates, both U5 and U10 will be loaded, thus loading all ATT bits simultaneously. Due to the slower switching speed of the RF modulator, it is not required to program all MPB bits simultaneously.

6-167. Power-Down Interrupt Generator. The power-down interrupt generator (Figure 6-69) will create an interrupt whenever the POWFLG signal is turned off (low). This may happen in one of two ways. The first is when the system is switched into standby, by the front panel line switch. The second is when the 50 pin D-type connector from the latch board J2 to the RF plug-in J5 is disconnected. The 1 k ohm resistor R5 insures that the signal is pulled low when the POWFLG signal is disconnected.

6-168. When POWFLG goes low, the clear input of the flip-flop U23-14 goes low. Note that the POWFLG signal is buffered by the hex 3-state buffer U16, which is permanently enabled by the grounding of $\overline{\text{G1}}$ and $\overline{\text{G2}}$. When the clear input goes low, the Q output U23-9 is forced low. This is the POWIRQ (power interrupt) signal to the PIC. The MPU will service this interrupt by blanking the front panel and then attempt to reset the interrupt by activating RSPOW-. If the POWFLG signal is still low, the POWIRQ will not be reset but if it has gone high it will be reset. The MPU will continuously attempt to reset the POWIRQ as long as it is active.

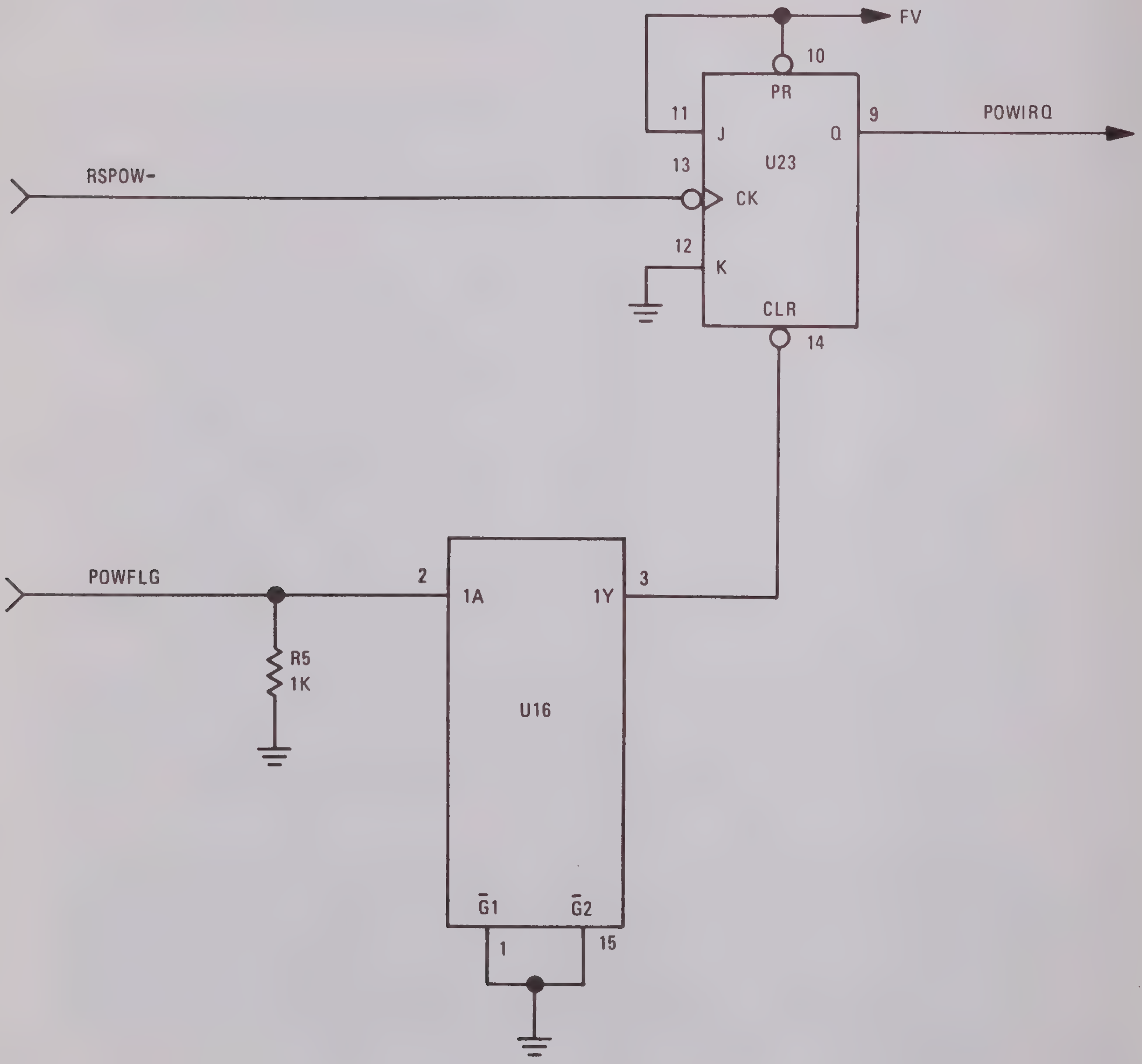

FV
10
PR
11
J
9
Q
POWIRQ
13
U23
RSPOW-
CK
12
K
CLR
14
POWFLG
2
1A
1Y
3
R5
1K
U16
$\overline{G}1$
$\overline{G}2$
1
15

Figure 6-69. Power-Down Interrupt Generator, Logic Diagram

6-169. LATCH BOARD ASSEMBLY A15A2. (Figures 6-70 through 6-72). The primary function of the latch board is to store the 42 frequency bits which are used to drive the remote frequency inputs of the RF plug-in. This board also contains the sweep control circuitry, the sweep voltage generator and the 8 bit system status buffer.

6-170. Latch Data Buffer. The latch data buffer U16 serves to buffer the MPU data bus (DBUSY0-7) from the latch data bus (DBUSZ0-7). The U16 component is an octal buffered latch. The latch is needed to supply sufficient data hold time for the data latches. The outputs of U16 are each tied to a 10K pull-up resistor. This is done to insure that the VIH of the CMOS data latches is met. These outputs parallel feed the data inputs of each primary data latch and the primary sweep latch. The latch enable (G) input is driven by the inverted PRWSTB signal. When PRWSTB pulses low, G pulses high and the input data becomes latched at the Q outputs.

6-171. Latch Address Decoder (U22). The function of the latch address decoder is to create the various clock strobes for the primary data latches. The A, B, C and D inputs to this decoder are driven by the ADXC00 through ADXC03 buffered address lines. The $\overline{G2}$ enable input is driven by the LATSEL (latch select) signal from the address decoder on the MPU board. These signals are normally generated by the MPU, but will be generated by the DMA during a fast-learn transfer sequence. The $\overline{G1}$ enable input is the PRWSTB signal. This signal will pulse low after the ADX and LATSEL signals have settled to their desired levels. This will cause the selected output to pulse low, clocking the appropriate data latch.

6-172. Primary Frequency Latches (U1, 3, 9, 7, 11, 14). These latches are the CMOS 8 bit, D type, positive edge triggered flip-flop. Data at the D inputs, meeting the set-up and hold time requirements, is transferred to the Q outputs on the positive-going transition of the clock input. Each latch, in turn, is loaded with the packed BCD frequency bits. The clock inputs are driven by the outputs of the U22 latch address decoder. The data inputs are driven by the U16 latch data buffer. The OD (output disable) of each latch is wired to ground for continuous operation.

6-173. Secondary Frequency Latches (U2, 4, 8, 10, 12, 15). These Latches are of the same type as the primary latches. The data inputs are driven by the Q outputs of the primary latches. The clock inputs of the secondary latches are all driven by the STORAGE signal from U21-13. This results in the simultaneous loading of all 42 frequency bits when this strobe is activated.

6-174. Primary Sweep Latch (U17). This latch is the CMOS hex D flip-flop with a positive-edge clock input. The CLEAR input is not used and pulled-up to VCC. This latch is the first rank of the double rank register for the six most significant bits of the digital sweep voltage.

6-175. Secondary Sweep Latch (U19). This is the same type of device as U17 and serves as the final rank of the double rank sweep register. Its Q outputs drive the 6 MSB inputs of the digital to analog converter (DAC) U23. The other 4 bits of this 10 bit DAC come from U8, a secondary frequency latch. Since the clock input of U19 is driven by the same signal as that of U8, all ten bits are loaded simultaneously into the DAC.

6-176. Sweep Voltage Generator (U23, U5). The sweep voltage generator consists of the ten bit DAC U23, the operational amplifier, U5 and various discrete resistors, inductors, and capacitors. During a sweep, the digital inputs of the DAC will be stepped from all zeroes, corresponding to the start frequency of the sweep, to binary one thousand, corresponding to the stop frequency of the sweep. The step size will be determined by the contents of the step-size register in RAM.

6-177. The I_o1 output of the DAC supplies a negative current (into the DAC) which is proportional to the binary value of the input lines, drives the inverting input of the operational amplifier U5-2. The output of the operational amplifier is the SWPOUT (sweep voltage output) and is tied to the 10K feed back resistor which is internal to the DAC. A 150 pF capacitor is used to suppress output overshoots. The reference voltage for the DAC of -2.046 volts is obtained by the R2, R5 voltage divider fed by the -12VDC input. The R6 potentiometer is the offset trim pot. The 5.2VDCA and the -12VDC voltage inputs are filtered with a series 10μH inductor and a 68μF capacitor to ground.

6-178. Strobe Generator. This circuit will generate the STROBE signal used to clock the secondary frequency latches and the secondary sweep latch. The STROBE signal will be generated by one of three sources. They are the

a. 1 ms Timer
b. FSKSTB
c. LSTB07

6-179. The 1 ms timer (U20) is wired to operate in the astable (free run) mode. The timing components selected (R3, R5, C1) will create a 1 ms pulse train with a duty cycle of 98%. The timer output will do two things. The falling edge will create the rising edge of the STROBE (U21-13) signal, which will clock the data previously loaded in the primary stage to the outputs of the secondary stage. The rising edge of the timer output will generate the active low SWPIRQ signal. This is the sweep interrupt signal which tells the MPU to update the frequency and sweep voltage data in the primary latches. This new data will be transferred to the secondary latch outputs on the falling edge of the next timer pulse output.

6-180. These two events, the creation of the STROBE pulse and SWPIRQ by the timer, will only occur when U6-8 is low. It is made low by the MPU placing a 1 on DBUSZ0 and addressing LSTB13, and will be done when the sweep mode is entered. Note that in the fast-learn mode, when FASTL- goes low, U6-8 is forced high. This insures that the sweep mode is not active when fast-learn transfers are occurring. The SWPIRQ signal is reset by the MPU after the frequency and sweep voltage data has been updated. This is done by the activation of RSSWP-(reset sweep) by the MPU.

6-181. The FSKSTB signal will also generate the STROBE signal. The FSKSTB will go low when either the FSHIRQ or FSLIRQ interrupt signals go active. When FSKSTB pulses low, the STROBE will pulse high, thereby clocking the secondary latches on the leading edge. The data which is in the primary latches was placed there by the MPU when it received the previous FSK interrupt.

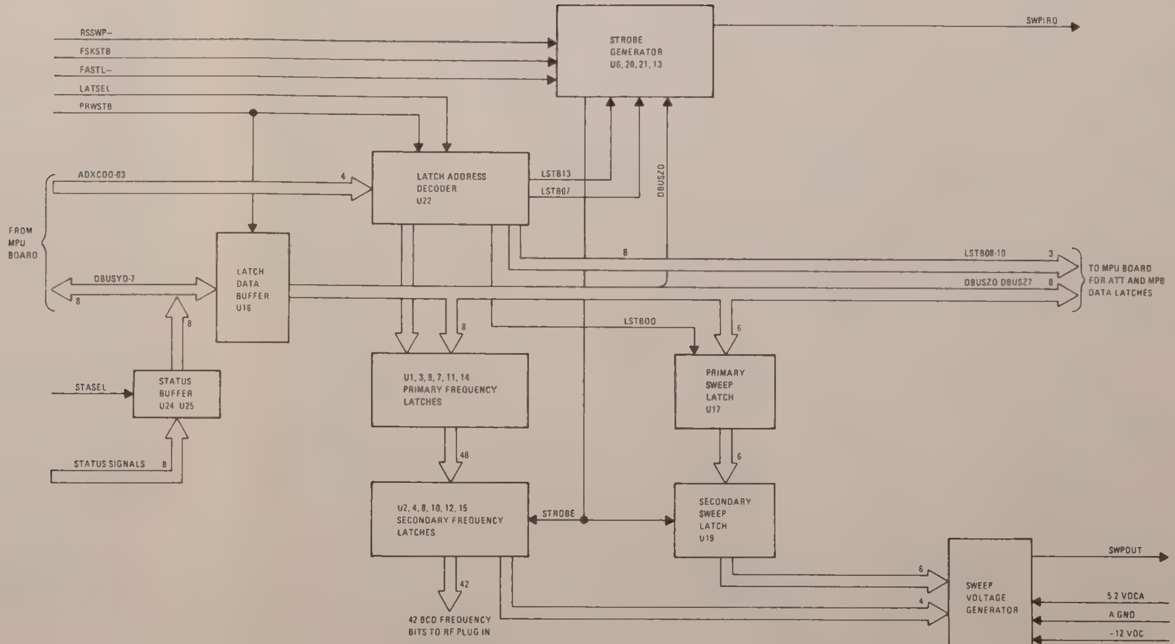

RSSWP–
FSKSTB
FASTL–
LATSEL
PRWSTB
STROBE GENERATOR U6, 20, 21, 13
SWPIRQ
ADXC00-03
4
LATCH ADDRESS DECODER U22
LSTB13
LSTB07
DBUSZ0
FROM MPU BOARD
DBUSY0-7
8
LATCH DATA BUFFER U16
8
LSTB08-10
3
DBUSZ0 DBUSZ7
8
TO MPU BOARD FOR ATT AND MPB DATA LATCHES
8
LSTB00
6
STASEL
STATUS BUFFER U24 U25
STATUS SIGNALS
8
U1, 3, 9, 7, 11, 14 PRIMARY FREQUENCY LATCHES
PRIMARY SWEEP LATCH U17
48
6
U2, 4, 8, 10, 12, 15 SECONDARY FREQUENCY LATCHES
STROBE
SECONDARY SWEEP LATCH U19
42
42 BCD FREQUENCY BITS TO RF PLUG IN
6
4
SWEEP VOLTAGE GENERATOR
SWPOUT
5 2 VDCA
A GND
-12 VDC

Figure 6-70. Latch PC Board A15A2, Block Diagram

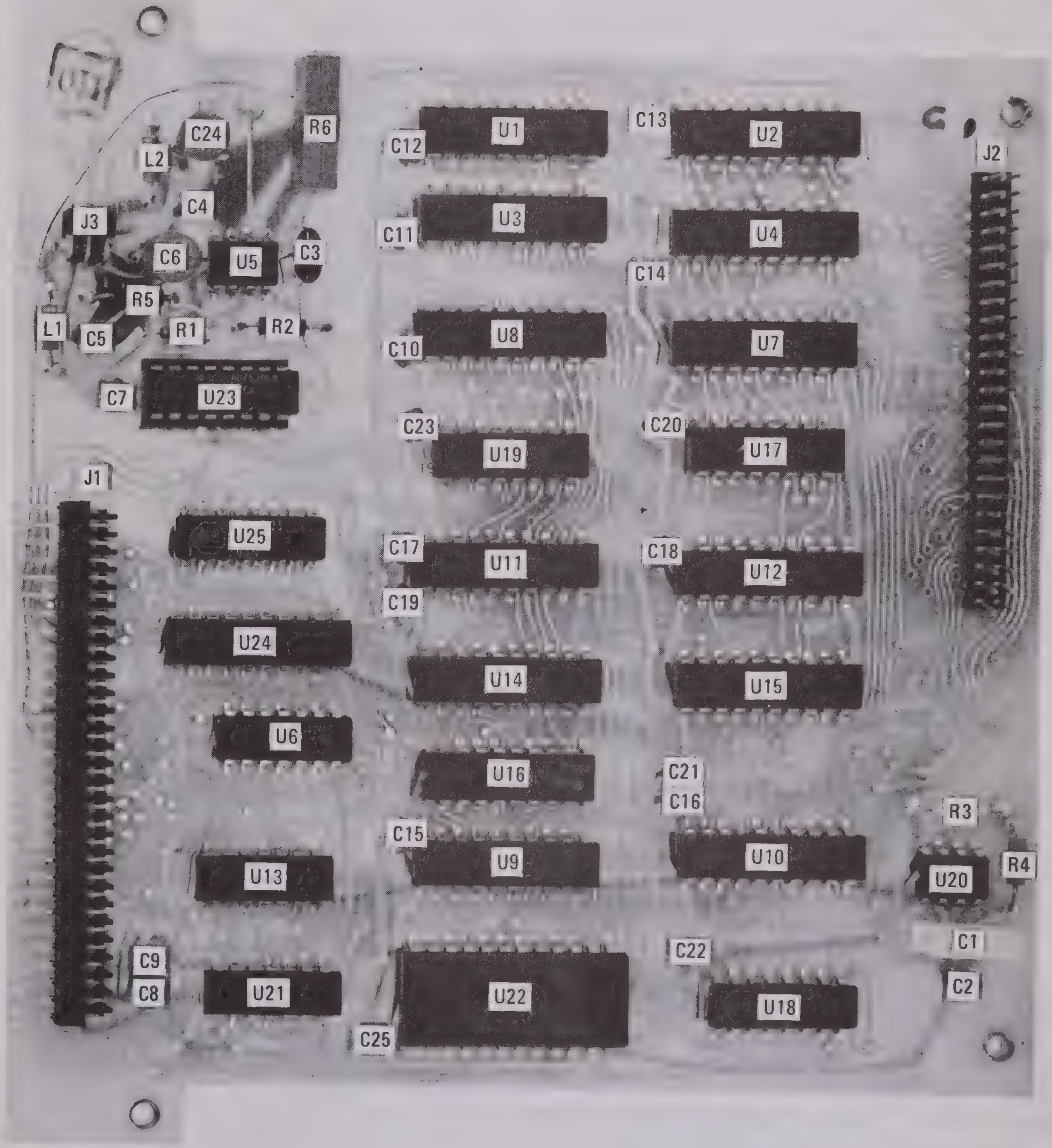

U1
U2
U3
U4
U5
U6
U7
U8
U9
U10
U11
U12
U13
U14
U15
U16
U17
U18
U19
U20
U21
U22
U23
U24
U25
C1
C2
C3
C4
C5
C6
C7
C8
C9
C10
C11
C12
C13
C14
C15
C16
C17
C18
C19
C20
C21
C22
C23
C24
C25
R1
R2
R3
R4
R5
R6
L1
L2
J1
J2
J3

Figure 6-71. Latch PC Board A15A2, Component Location

6-182. When the FSHIRQ is received, the MPU will load the contents of the frequency register into the primary frequency latches. When the FSLIRQ is received, the MPU will load the contents of the frequency register plus (+) the contents of the increment register into the primary frequency latches. This data will be transferred to the outputs of the secondary latches on the leading edge of the next FSK interrupt signal.

6-183. The LSTB07 (latch strobe 07) signal is generated by the latch address decoder. When the MPU (or the DMA in fast-learn mode) wishes to load a frequency or sweep voltage into the secondary output latches, it will activate the address of the LSTB07 signal. This will, in turn, generate the STROBE pulse and clock the secondary latches.

6-184. The status buffer is an 8 bit 3-state noninverting buffer. This buffer allows the MPU to read the state of the 8 status signals wired to the inputs of the buffer. The buffer is enabled by the STASEL signal which is generated by the address decoder on the MPU board. The outputs of the buffer are tied into the MPU data bus (DBUSY0 through DBUSY7).

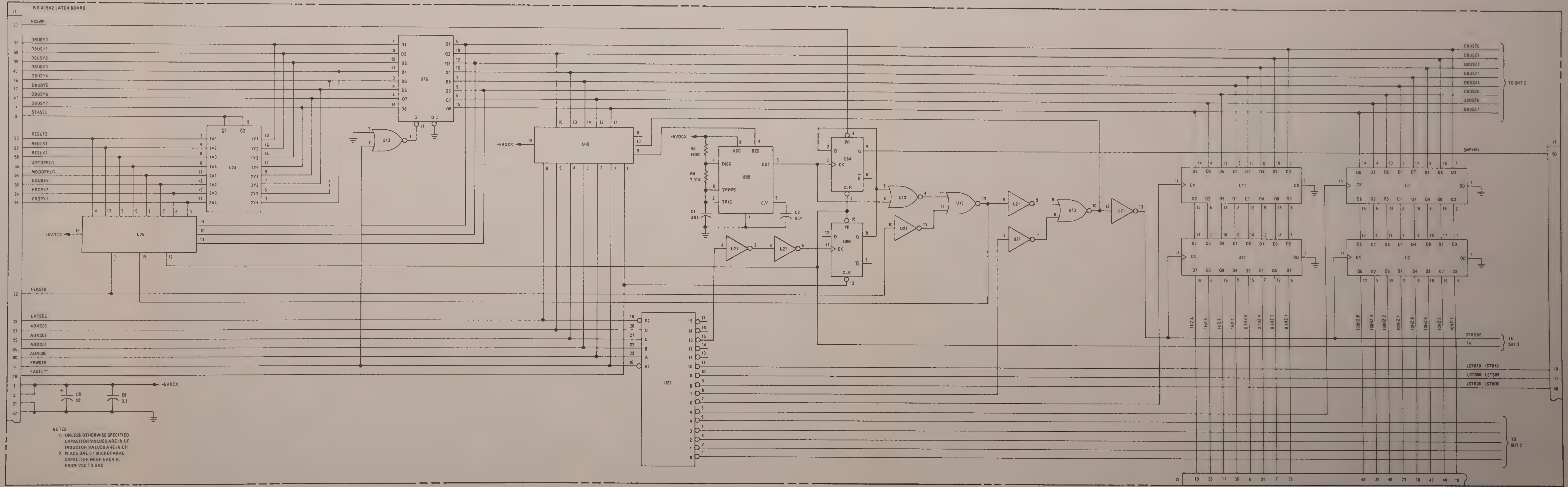
The eight status inputs are:

1. RESLT2 } From resolution
2. RESLX1 } control circuit on
3. RESLX2 } encoder board

4. ATTOPFLG — From plug-in. Will be low when attenuator is installed.
5. MODOPFLG — From plug-in. Will be low when modulator is installed.
6. DOUBLE — From plug-in. Will be low when frequency doubler is installed.
7. FROPX1, — From plug-in. Used to indicate the maximum frequency the
8. FROPX2 plug-in is capable of being programmed to. These lines are coded as follows:

FROPX1	FROPX2	MAX. FREQUENCY
0	0	26 GHz
0	1	2 GHz
1	0	1800 MHz
1	1	180 MHz

6-185. All inputs of the buffer are tied to a 10K pull-up resistor in the U25 resistor network package. This is done to insure that the inputs go high when the ground jumpers are not installed on these status input signals. If the ATTOPFLG and/or the MODOPFLG signal are high, those functions will not be programmable. If both of these signals are high, the RF panel mode will not be entered when prompted from the keyboard. When the DOUBLE signal is low, the frequency programmed will be displayed as entered but will output to the frequency latches at half the entered value.


P/O A15A2 LATCH BOARD
J1
RSSWP
DBUSY0
DBUSY1
DBUSY2
DBUSY3
DBUSY4
DBUSY5
DBUSY6
DBUSY7
STASEL
RESLT2
RESLX1
RESLX2
ATTOPFLG
MODOPFLG
DOUBLE
FROPX2
FROPX1
FSKSTB
LATSEL
ADXC03
ADXC02
ADXC01
ADXC00
PRWSTB
FASTL
+5VDCX
C8 22
C9 0.1
U24
U25
U16
U13
U18
U20
R3 143K
R4 3 01K
C1 0 01
C2 0.01
VCC
RES
DISC
OUT
THRES
TRIG
C.V
U21
U6A
U6B
PR
CLR
CK
U22
G2
D
C
B
A
G1
U11
U12
U1
U2
OD
1HZ 8
1HZ 4
1HZ 2
1HZ 1
0 1HZ 8
0 1HZ 4
0 1HZ 2
0 1HZ 1
100HZ 8
100HZ 4
100HZ 2
100HZ 1
10HZ 8
10HZ 4
10HZ 2
10HZ 1
DBUSZ0
DBUSZ1
DBUSZ2
DBUSZ3
DBUSZ4
DBUSZ5
DBUSZ6
DBUSZ7
TO SHT 2
SWPIRQ
STROBE
FV
TO SHT 2
LSTB10 LSTB10
LSTB09 LSTB09
LSTB08 LSTB08
TO SHT 2
J2

NOTES
1. UNLESS OTHERWISE SPECIFIED CAPACITOR VALUES ARE IN UF INDUCTOR VALUES ARE IN UH
2. PLACE ONE 0 1 MICROFARAD CAPACITOR NEAR EACH IC FROM VCC TO GND

Figure 6-72. Latch PC Board A15A2, Schematic Diagram (Sheet 1)

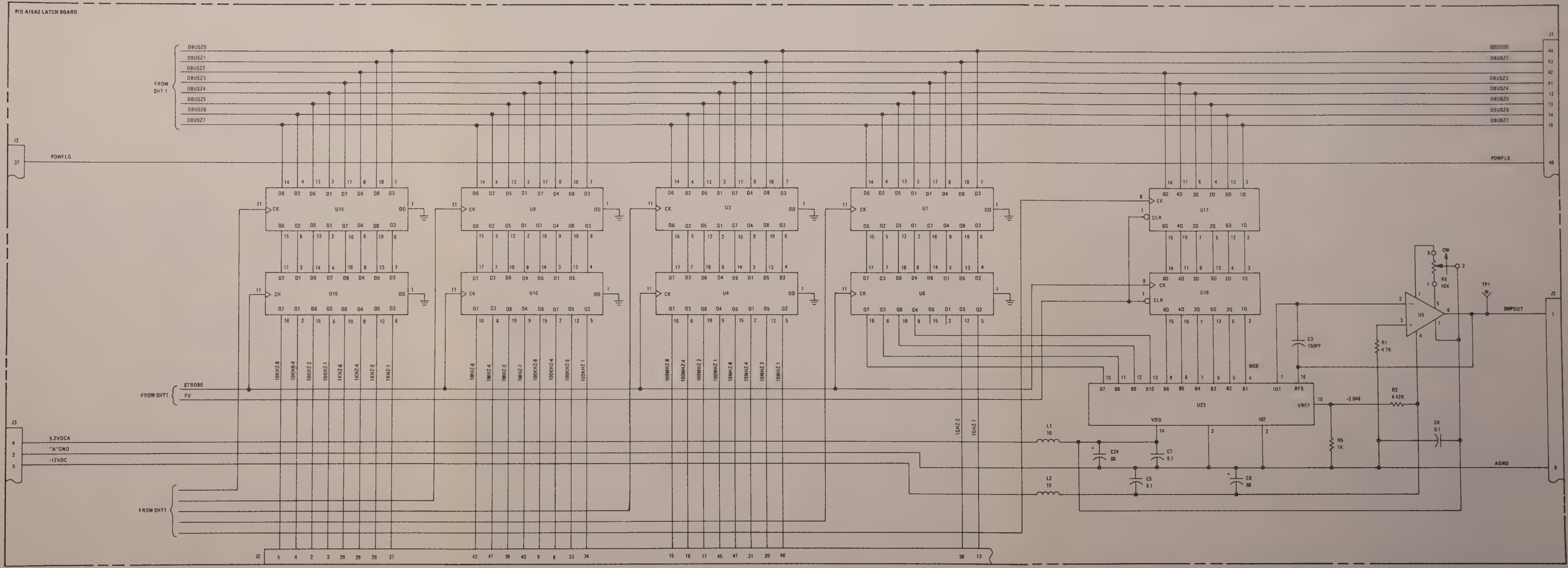

P/O A15A2 LATCH BOARD
FROM SHT 1
DBUSZ0
DBUSZ1
DBUSZ2
DBUSZ3
DBUSZ4
DBUSZ5
DBUSZ6
DBUSZ7
J1
J2
J3
POWFLG
POWFLG
U14
U9
U3
U7
U17
U15
U10
U4
U8
U18
U23
U5
CK
OD
CLR
10KHZ-8
10KHZ-4
10KHZ-2
10KHZ-1
1KHZ-8
1KHZ-4
1KHZ-2
1KHZ-1
1MHZ-8
1MHZ-4
1MHZ-2
1MHZ-1
100KHZ-8
100KHZ-4
100KHZ-2
100KHZ-1
100MHZ-8
100MHZ-4
100MHZ-2
100MHZ-1
10MHZ-8
10MHZ-4
10MHZ-2
10MHZ-1
1GHZ-2
1GHZ-1
FROM SHT1
STROBE
FV
5.2VDCA
"A"GND
-12VDC
FROM SHT1
MSB
IO1
RFB
VREF
VDD
IO2
-2.046
L1 10
L2 10
C24 68
C7 0.1
C5 0.1
C6 68
C3 150PF
C4 0.1
R1 4.7K
R2 4.42K
R5 1K
R6 10K
CW
TP1
SWPOUT
AGND

Figure 6-72. Latch PC Board A15A2, Schematic Diagram (Sheet 2)

The resolution signals are coded as follows:

RESLX2	RESLX1	RESLT2	Resolution Hz
0	0	0	1000.0
0	0	1	100.0
0	1	0	100.0
0	1	1	10.0
1	0	0	10.0
1	0	1	1.0
1	1	0	1.0
1	1	1	0.1

6-186. GPIB BOARD ASSEMBLY A15A3. (Figures 6-73 through 6-75). The GPIB board provides the means for interfacing the 380 to the IEEE 488 standard instrument bus, also referred to as the General Purpose Interface Bus (GPIB). Below is a brief description of the GPIB as specified by IEEE Standard 488-1978. For a detailed description of the bus operation, refer to this standard published by The Institute of Electrical and Electronics Engineers, Inc., 345 East 47th Street, New York, N.Y., 10017. To understand the circuit description of the GPIB board, a full understanding of the Standard 488-1978 is not required but would be useful.

6-187. General Description of the IEEE Standard 488-1978. The IEEE Standard 488-1978 is a byte-serial, bit-parallel interface system primarily defined for programmable measurement instrument systems. The general system defines all circuits, cables, connectors, control protocol and message repertoire to ensure unambiguous data transfer between devices.

a. The system has the following defined constraints:

  1. No more than 15 devices can be interconnected by a single contiguous bus.
  2. Total transmission length is not to exceed 20 meters, or 2 meters times the number of devices, whichever is less.
  3. Data rate through any signal line must be less than, or equal to, 1 Megabit/second.
  4. All data exchange is to be digital (as opposed to analog).

b. The 488-1978 system is structured with 16 transmission lines. They consist of:

  1. 8 data bus lines, permitting transmission of ASCII characters. Data is asynchronous and generally bidirectional.
  2. 3 data byte transfer control lines (handshake).
  3. 5 general interface management signal lines.

c. These lines may employ either open-collector (not open-collector in an absolute sense due to the termination networks on the bus) or three-state drive elements with certain constraints on the SRQ, NRFD, DIO1-8 and NDAC lines.

6-188. **Bus Line Callouts.** Table 6-7 lists the 16 lines defined in the 488 bus and their definitions.

TABLE 6-7. BUS LINE CALLOUTS

Signal Name	Description
DIO1-DIO8 Data Input/Output	These are the message lines for carrying data in a bit-parallel, byte-serial form. Data is asynchronous and generally bidirectional. These lines carry either data or address information, depending on the condition of the ATN line.
DAV (Data Valid)	One of the three handshake lines used to indicate availability and validity of information on the DIO lines. DAV indicates to the acceptor(s) that data is available on the DIO lines.
NRFD (Not Ready for Data)	Another handshake line used to indicate that all devices are or are not ready to accept data.
NDAC (Not Data Accepted)	The final handshake line used to indicate the acceptance of data by all devices.
ATN (Attention)	One of five bus management lines used to specify how data on the data lines are to be interpreted and which devices must respond to the data. When ATN is true the DIO1-8 lines carry addresses or commands. When false they carry data. (controller driven).
IFC (Interface Clear)	A bus management line which is used to place the interface system in a known quiescent state. All interconnected devices contain some portions of the interface system. IFC puts talkers, listeners into their idle states. (controller driven).
SRQ (Service Request)	A bus management line used by a device to indicate a need for service and to request an interrupt of the current events sequence.

TABLE 6-7. BUS LINE CALLOUTS (Cont)

Signal Name	Description
REN (Remote Enable)	This management line in conjunction with other messages selects between two alternate sources of device programming data. (example: front panel control or interface control). (controller driven).
EOI (End or Identify)	The final management line used to indicate the end of multiple byte transfer sequences or with ATN to perform a parallel polling sequence

6-189. General Purpose Interface Adapter (GPIA) U13. U13 is an NMOS General Purpose Interface Adapter (GPIA) that provides the means to interface between the IEEE-488 standard instrument bus and an 8-bit MPU bus. It implements the necessary IEEE state diagrams for the acceptor and source handshake, talker, talker extended*, listener, listener extended*, service request, parallel poll, device trigger*, remote local, and device clear functions. In addition, the part implements other non-IEEE defined functions such as: single and dual primary address recognition*, RFD handshake hold-off, DMA, talk only*, listen only* and programmable interrupts. Many instrument bus protocol features are automatically handled by the device and require minimal MPU response due to the internal registers conveying information on the state of the U13 and GPIB. The device is configured for the desired operation by programming it from the MPU data bus or the IEEE-488 bus.

6-190. The GPIA is designed to be used with an MPU. The MPU is used to program the part (e.g., initialization or changing operations within the context of the IEEE-488 protocol), to transfer device-dependent messages to and from the part (and hence the TPIB), and to monitor status of the GPIA and GPIB. U13 responds to GPIB controller commands and conveys current status to an MPU through either an interrupt or polling sequence. U13 will automatically handle all handshake protocol on the GPIB.

6-191. GPIB/GPIA Interface Bus Signals. The GPIA provides a set of eighteen interface signal lines between itself and the IEEE standard 488 bus. Refer to Table 6-8.

6-192. GPIA/MPU Interface Signals. The GPIA interfaces to the MPU with an eight-bit bidirectional data bus, a chip select, read/write line, reset line, three register select lines, an interrupt request line, two DMA control lines and an address switch enable line. Refer to Table 6-9.

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*These features are not supported by the 380 software.

TABLE 6-8. GPIB INTERFACE SIGNALS

Signal Name	Description
SIGNAL LINES ($\overline{\mathrm{IB0}}$-$\overline{\mathrm{IB7}}$)	These bidirectional lines allow for the flow of seven bit ASCII interface messages and device dependent messages. Data appears on these lines in a bit-parallel byte-serial form. These lines are buffered by transceivers and applied to the 488 bus (DIO1-DIO8).
BYTE TRANSFER LINES (DAC, RFD, $\overline{\mathrm{DAV}}$)	These lines allow for proper transfer of each data byte on the bus between sources and acceptors. RFD goes passively true indicating that all acceptors are ready for data. A source will indicate the data is valid by pulling $\overline{\mathrm{DAV}}$ low. Upon the reception of valid data by all acceptors, DAC will go passively true indicating that the data has been accepted by all acceptors.
BUS MANAGEMENT LINES ($\overline{\mathrm{ATN}}$, $\overline{\mathrm{IFC}}$, $\overline{\mathrm{SRQ}}$, $\overline{\mathrm{EOI}}$, $\overline{\mathrm{REN}}$)	These lines are used to manage an orderly flow of information across the interface lines.
ATTENTION ($\overline{\mathrm{ATN}}$)	Is sent true over the interface to disable current talkers and listeners freeing the signal lines ($\overline{\mathrm{IB0}}$-$\overline{\mathrm{IB7}}$). During the ATN active state devices monitor the signal path for addressing or an interface command. Data Flows on the signal lines when $\overline{\mathrm{ATN}}$ is inactive (high).
INTERFACE CLEAR ($\overline{\mathrm{IFC}}$)	Is used to put the interface system into a known quiescent state.
SERVICE REQUEST ($\overline{\mathrm{SRQ}}$)	Is used to indicate a need for attention in addition to requesting an interruption in the current sequence of events. This indicates to the controller that a device on the bus is in need of service.
REMOTE ENABLE ($\overline{\mathrm{REN}}$)	Is used to select one of two alternate sources of device programming data, local or remote control.
END OR IDENTIFY ($\overline{\mathrm{EOI}}$)	Is used to signal the end of a multiple byte transfer sequence and in conjunction with $\overline{\mathrm{ATN}}$ executes a parallel polling sequence.

TABLE 6-8. GPIB INTERFACE SIGNALS (Cont)

Signal Name	Description
TRANSMIT/RECEIVE CONTROL SIGNALS (T/$\overline{R}$, T/$\overline{R}$2)	These two signals are used to control the quad transceivers which drive the interface bus. Each transceiver has a separate transmit/$\overline{\text{receive}}$ control pin. These pins can support one TTL load each. The transmit/$\overline{\text{receive}}$ inputs of $\overline{\text{REN}}$, $\overline{\text{IFC}}$, and $\overline{\text{ATN}}$ are hardwired low to receive. $\overline{\text{EOI}}$ is controlled by T/$\overline{R}$ 1 through the transceiver allowing it to transmit or receive. T/$\overline{R}$ 1 operates exactly as T/$\overline{R}$ 2 except during the parallel polling sequence. During parallel poll $\overline{\text{EOI}}$ will be made an input by T/$\overline{R}$ 1 while $\overline{\text{DAV}}$ and $\overline{\text{IB0}}$/$\overline{\text{IB7}}$ lines are outputs. During Serial Poll T/$\overline{R}$ 1 will make $\overline{\text{EO1}}$ an input with $\overline{\text{DAV}}$ and the $\overline{\text{IB0}}$/$\overline{\text{IB7}}$ bus as outputs.

TABLE 6-9. GPIA/MPU INTERFACE SIGNALS

Signal Name	Description
$\overline{\text{D0}}$-$\overline{\text{D7}}$	The bidirectional data lines allow the transfer of data between the MPU and the GPIA. The data bus output drives are three state devices that remain in the high impedance (off) state except when the MPU performs a GPIA read operation. The Read/$\overline{\text{Write}}$ line is in the read state when the GPIA is selected for a read operation.
GPIA CHIP SELECT ($\overline{\text{CS}}$)	This input signal is used to select the GPIA. $\overline{\text{CS}}$ must be low for selection of the device. Chip select decoding is accomplished with logic external to the chip.
GPIA READ/$\overline{\text{WRITE}}$ LINE (R/$\overline{\text{W}}$)	This signal is generated by the MPU to control register access and direction of data transfer on the data bus. A low state on the GPIA Read/$\overline{\text{Write}}$ allows

TABLE 6-9. GPIA/MPU INTERFACE SIGNALS (Cont)

Signal Name	Description
GPIA READ/$\overline{\text{WRITE}}$ LINE (R/$\overline{\text{W}}$) (Cont)	for the selection of one of seven write only registers when used in conjunction with the register select lines; RS0, RS1, RS2. A high state on the GPIA Read/$\overline{\text{Write}}$ allows for the selection of one of eight read only registers when used in conjunction with register select lines RS0, RS1, RS2.
GPIA REGISTER SELECT (RS0, RS1, RS2)	The three register select lines are used to select the various registers inside the GPIA. These three lines are used in conjunction with the Read/$\overline{\text{Write}}$ line to select a particular register that is to be written or read.
INTERRUPT REQUEST ($\overline{\text{IRQ}}$)	The $\overline{\text{IRQ}}$ output goes to the NMI interrupt of the MPU. This is an open drain output. The $\overline{\text{IRQ}}$ is set false (low) when an enabled interrupt occurs and stays false until the MPU reads from the interrupt status register.
$\overline{\text{RESET}}$	The active low $\overline{\text{Reset}}$ line is used to initialize the chip during power-on start up. Reset is driven by an external power-up reset circuit.
DMA CONTROL LINES (DMA GRANT, DMA REQUEST)	The DMA request line is used to signal waiting data when BI is set high for a DMA controller. The DMA request line is set high if the BI interrupt flag is set in the interrupt status register (R0W). The DMA request line is cleared when the DMA grant is made true. The DMA grant line is used to signal the GPIA that the DMA controller has control of the MPU data and address lines.
ADDRESS SWITCH ENABLE ($\overline{\text{ASE}}$)	The $\overline{\text{ASE}}$ output is used to enable the device address switch three state buffers to allow the instrument address switch to be read on the MPU bus.
CLOCK INPUT (Clk)	The Clk input is a derivative of the MPU 02 clock. It is designed to operate at clock rates up to 1 MHz.

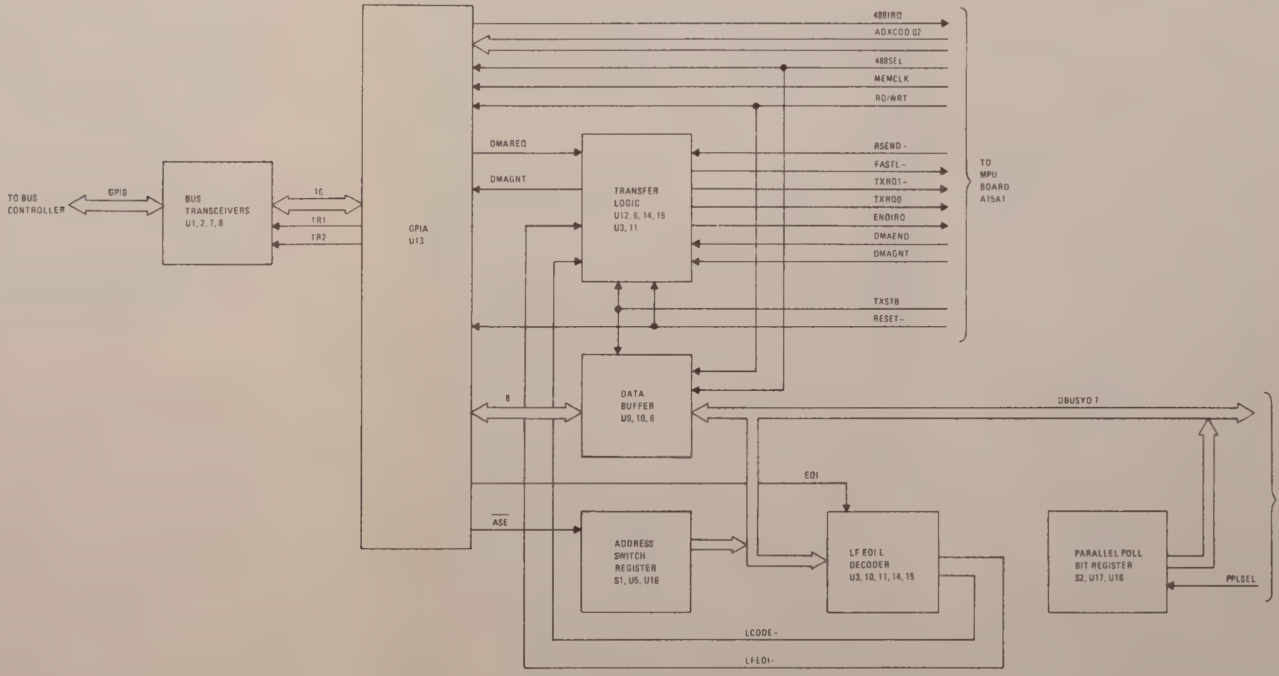

TO BUS CONTROLLER
GPIB
BUS TRANSCEIVERS U1, 2, 7, 8
16
TR1
TR2
GPIA U13
488IRQ
ADXCOD 02
488SEL
MEMCLK
RD/WRT
DMAREQ
DMAGNT
TRANSFER LOGIC U12, 6, 14, 15 U3, 11
RSEND -
FASTL–
TXRQ1–
TXRQ0
ENDIRQ
DMAEND
DMAGNT
TXSTB
RESET–
TO MPU BOARD A15A1
8
DATA BUFFER U9, 10, 6
DBUSYO 7
EOI
$\overline{\text{ASE}}$
ADDRESS SWITCH REGISTER S1, U5, U16
LF EOI L DECODER U3, 10, 11, 14, 15
PARALLEL POLL BIT REGISTER S2, U17, U16
PPLSEL
TO MPU BOARD A15A1
LCODE -
LFEOI-

Figure 6-73. GPIB PC Board A15A3, Block Diagram

6-193. Transfer Logic. All data transferred over the GPIB to the 380 is transferred via the GPIA to a 25 byte buffer location in RAM. This transfer to RAM is controlled by the DMA controller (Direct Memory Access). The DMA controller itself is located on the MPU board, although the transfer request logic is located on the GPIB board.

6-194. Transfer Request Logic (U12, 6, 14, 15, 3, 11). At power-up, the request flip-flops U12 are initialized by the RESET- signal via U6. U12A is cleared, which disables the TXRQ1- signal U3-2 and forces the FASTL- signal to be inactive high. U12B is preset which, together with FASTL- being high, enables the TXRQO- signal U14-6. The ENDIRQ signal U12-5 is disabled high at this time. When a byte of data is accepted by the GPIA, the DMAREQ (DMA request) signal goes high causing the TXRQO- signal to go active low. This signals the DMA controller that there is a byte of data to be transferred on channel O. The DMA controller will go through the request sequence for channel O. It will place the appropriate address on the address bus and the R/W signal in the write mode. It will then activate the TXSTB- (transfer strobe).

6-195. The TXSTB- signal, activates the DMA grant input of the GPIA. The DMA grant input is used to signal the GPIA that the DMA controller has control of the MPU address lines. The DMAREQ line is cleared when the DMA grant is made true. The U9 data buffer is enabled by the TXSTB- signal and allows the DMA transfer of the data present in the GPIA at this time. This data and all succeeding data bytes will be transferred to the 25 byte memory block in RAM. Each byte will be loaded in sequential memory locations. The memory address is determined by the contents of the address registers, and the total number of bytes to be transferred is determined by the contents of the byte count register. These two registers are internal registers of the DMA.

6-196. When the 25th byte is transferred, the DMAEND and the DMAGNT signals both go active low, causing U12B to be cleared. This results in the activation of the ENDIRQ signal U12-5. This is the END interrupt to the MPU. When this interrupt is received, the MPU knows that the final byte of the data string has been received. The MPU will then read the contents of the 25 byte RAM buffer and take the appropriate actions. When the MPU completes processing the data string received, it will activate the RSEND U6-2 signal. This will preset U12B and deactivate the ENDIRQ interrupt signal. It should be noted that while the ENDIRQ signal is active low, the TXRQO- signal is disabled by U14-3 low. This is done to prevent data transfers while the MPU is busy processing the previous data string. If the GPIB controller attempts to transfer more data while the ENDIRQ is active, the DMAREQ will not be recognized on channel O until ENDIRQ is reset by RSEND-.

6-197. The use of the DMAEND as described above, is the default terminator and not the normal means of terminating a data string. Normally, the final character of the data string is sent concurrently with an EOI uniline signal or the terminating character may be the ASCII code for line feed. When either of these messages are received, the LFEOI- signal goes low. When TXSTB- goes low, U11-8 goes low, thereby clearing flip-flop U12A. This is done to terminate the fast-learn mode if it were being utilized. When TXSTB- returns high, a 0 is clocked to ENDIRQ U12-5, which activates this interrupt signal and prevents channel O transfers until ENDIRQ is reset by RSEND-.

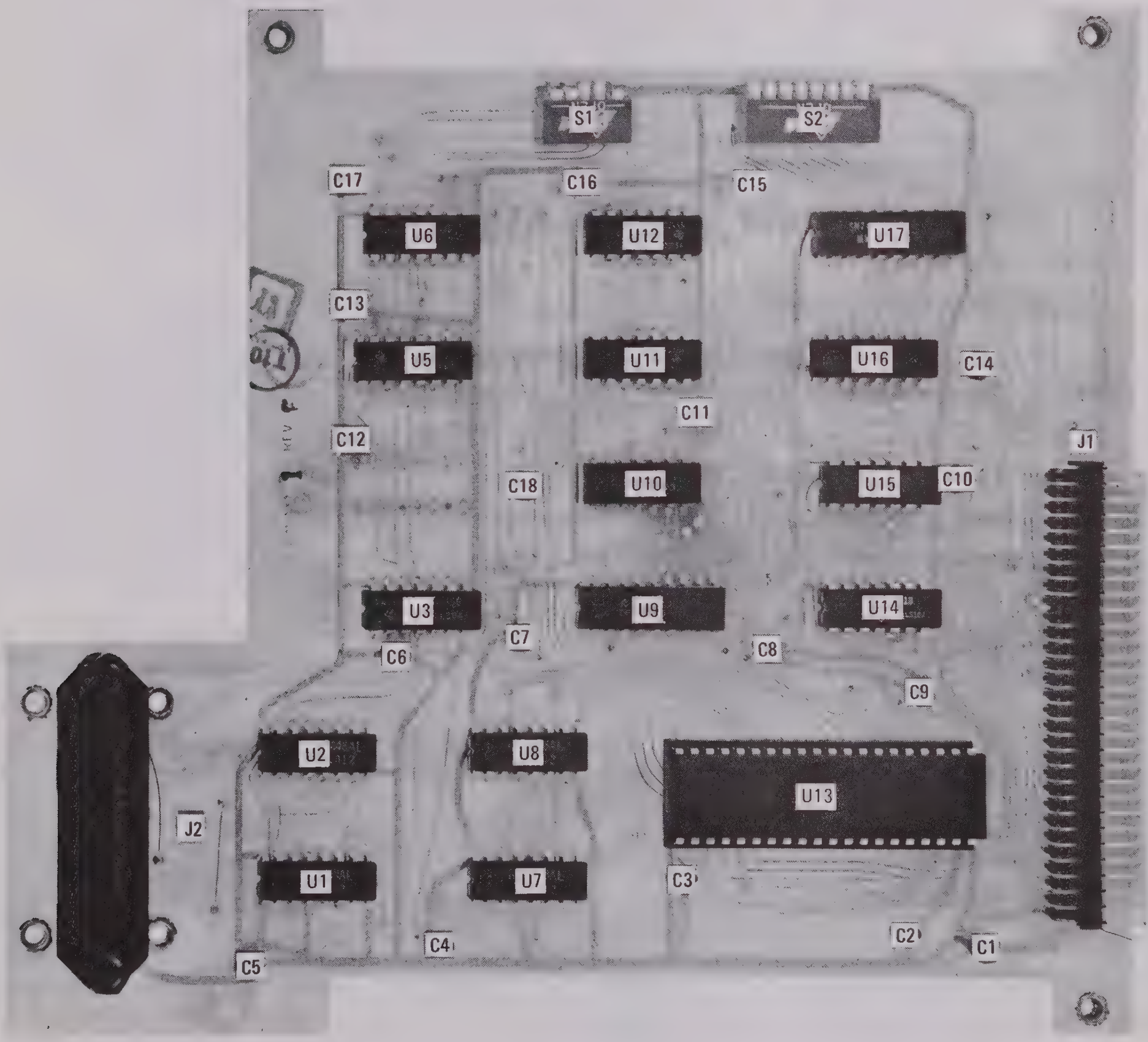
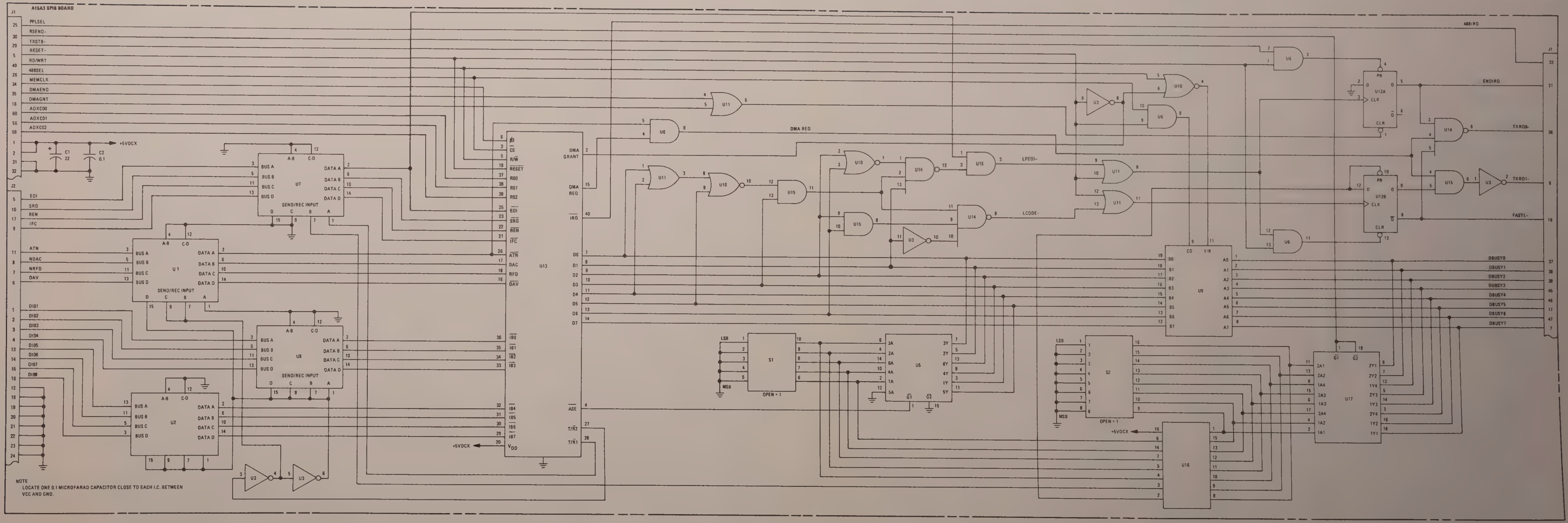
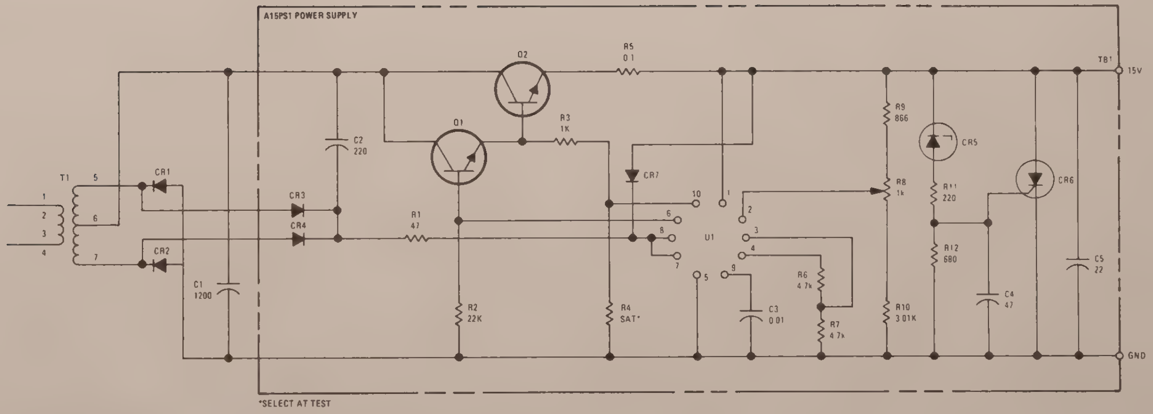

S1
S2
C17
C16
C15
U6
U12
U17
C13
U5
U11
U16
C14
C11
J1
C12
C18
U10
U15
C10
U3
U9
U14
C7
C6
C8
C9
U2
U8
U13
J2
U1
U7
C3
C2
C1
C4
C5

Figure 6-74. GPIB PC Board A15A3, Component Locations


A15A3 GPIB BOARD
J1
PPLSEL
RSEND-
TXSTB-
RESET-
RD/WRT
488SEL
MEMCLK
DMAEND
DMAGNT
ADXC00
ADXC01
ADXC02
+5VDCX
C1 22
C2 0.1
J2
EOI
SRQ
REN
IFC
ATN
NDAC
NRFD
DAV
DIO1
DIO2
DIO3
DIO4
DIO5
DIO6
DIO7
DIO8
U7
U1
U8
U2
SEND/REC INPUT
BUS A
BUS B
BUS C
BUS D
DATA A
DATA B
DATA C
DATA D
U13
DMA GRANT
DMA REQ
IRQ
ASE
T/R2
T/R1
VDD
U3
U6
U10
U11
U14
U15
DMA REQ
LFEOI-
LCODE-
488IRQ
U12A
U12B
PR
CLR
CLK
ENDIRQ
TXRQ0-
TXRQ1-
FASTI-
U9
U5
U16
U17
S1
S2
LSB
MSB
OPEN = 1
DBUSY0
DBUSY1
DBUSY2
DBUSY3
DBUSY4
DBUSY5
DBUSY6
DBUSY7

NOTE
LOCATE ONE 0.1 MICROFARAD CAPACITOR CLOSE TO EACH I.C. BETWEEN VCC AND GND.

Figure 6-75. GPIB PC Board A15A3, Schematic Diagram


A15PS1 POWER SUPPLY
T1
1
2
3
4
5
6
7
CR1
CR2
CR3
CR4
C1
1200
C2
220
Q1
Q2
R5
0 1
R3
1K
R1
47
R2
22K
R4
SAT*
CR7
U1
10
1
2
6
8
3
4
7
5
9
C3
0 01
R6
4 7k
R7
4 7k
R9
866
R8
1k
R10
3 01K
CR5
R11
220
R12
680
C4
47
CR6
C5
22
TB1
15V
GND

*SELECT AT TEST

NOTE
UNLESS OTHERWISE SPECIFIED
RESISTOR VALVES ARE IN OHMS
CAPACITOR VALUES ARE IN μF

Figure 6-76. Power Supply A15PS1 Schematic Diagram

6-198. If the ASCII character L is being transmitted to initiate the fast learn mode, the LCODE- signal U11-13 will go low and the DMAREQ will go high, activating the TXRQO- signal. The DMA controller will, in turn, activate the TXSTB- signal. On the rising edge of this signal the outputs of U12A will be $\overline{Q}$ U12-8 low and Q U12-9 high. The $\overline{Q}$ output is the FASTL- signal and is used on the latch board to signal the occurrence of the fast-learn mode. This signal is also used to disable the TXRQO-. This is done because in the fast-learn mode all data, except the first character (L), are transferred via channel 1 of the DMA. The Q output of U12 which has gone high, enables the TXRQ1- signal for the next byte transfer. TXRQ1- invokes a channel 1 transfer.

6-199. Channel 1 of the DMA is configured to perform what is called a data chain transfer. The data chain function permits repetitive writing into a block of memory. This allows the GPIB controller to send groups of 7 bytes each after sending an ASCII L. Each of these groups will represent a frequency coded in packed-BCD. The seven byte block of memory is actually the six frequency data latches on the latch board. The seventh byte serves to strobe the frequency to the secondary latch stage for a simultaneous output of all bits. There is a corresponding block of memory in RAM which is simultaneously loaded with the same data that is going into the latches.

6-200. After the seven bytes have been transferred, the DMA will automatically reset its byte count and address register so that the next byte to be transferred will go into the first location of the block. The DMAEND will not be activated as it is with channel O. This technique of directly loading the packed-BCD frequency codes into the latches greatly increases the rate at which frequencies can be loaded into the 380 from the GPIB. The overhead time required to process the data by the MPU is totally eliminated.

6-201. **Linefeed, EOI and L Decode Circuit.** This circuit performs a hardware decode of a LF, EOI and an ASCII L character being transferred via the GPIA.

a. The EOI message, which is generated by the GPIB controller and is an active low uniline message, is detected at U7-2. This signal is used to indicate the end of a data string and is activated concurrently with the last byte of data.

b. The LF or linefeed message is also generated by the GPIB controller but differs from the EOI in that is is a multiline message which means that it is conveyed by the eight data lines. When the LF message is received by the GPIA, it is placed on the D0-D7 data lines and its presence is detected by the decoding logic of U10, 11, 14, 15, 3.

c. When LF or EOI is detected, the $\overline{\text{LFEOI}}$- signal is generated by U15-3. This signal in conjunction with the TXSTB- will cause the ENDIRQ signal. This is the interrupt to the MPU to signal the end of a data string.

d. $\overline{\text{LFEOI}}$- in conjunction with TXSTB- will also terminate the fast learn mode by setting FAST L- to logic 1.

e. The ASCII character L is also decoded in the same manner as the LF. When it is detected, the LCODE- signal goes low and when the TXSTB- is activated, the FAST L- signal goes active low. The U12-9 (Q) output enables the channel 1 transfer request TXRQ1- and the U12-8 ($\overline{Q}$) output disables the channel 0 transfer request TXRQO-. Therefore, the subsequent data bytes following the L will be transferred via channel 1 of the DMA.

6-202. Bus Transceivers (U1, 2, 7, 8). Quad bus transceivers provide the electrical interface between the GPIB signal lines and U13. It is with these transceivers that the electrical specifications (drive requirements, logic levels, receiver hysteresis, terminations, etc.) are met. The primary reason for using a separate chip for the electrical interface is to provide the 48 mA drive capability per signal line required by the IEEE standard. The drive chips are coupled directly to U13, and the proper direction of data flow is automatically selected by T/R1 and T/R2. The signals on T/R1 and T/R2 are generated by U13. All outputs are driven open collector mode.

6-203. Address Switch Register (S1, U5, U16). The five pole switch S1 is used to select the talk/listen address of the 380. An open switch will select a 1 for that bit position. The five poles permit selection of 31 valid talk/listen codes (the UNL/UNT code of five 1's may not be used). One end of each switch pole is tied to ground. The other end is tied to a 10K pull-up resistor in U16 and to one of the data inputs of the U5 hex 3 state buffer. The outputs of the buffer tie into 6 data lines (D0-D5) from the GPIA. This buffer is enabled by the $\overline{\text{ASE}}$ output of the GPIA. The $\overline{\text{ASE}}$ output is pulsed low when the address switch register of the GPIA is ready by the MPU. When this occurs, the D0-D7 data outputs of the GPIA are in the high impedance mode and the U9 octal transceiver is enabled and set to receive data from the address switch.

6-204. Parallel Poll Bit Register. The eight poll switch S2 is used to select the 380's poll bit for the parallel polling function. An open switch selects a 1 and a closed switch selects a 0 just as with the address switch. One end of each switch pole is tied to ground and the other is pulled up with a 10K resistor in U16. This end is also tied to one of the data inputs of the U17 octal 3 state buffer. The outputs of this buffer are tied to the DBUSY0-DBUSY7 data lines to the MPU. This buffer is enabled by the active low PPLSEL signal which is generated by the address decoder. When the MPU wishes to read the parallel poll bit, it can activate the PPLSEL and read the data bus.

6-205. Data Buffer. The U9 data buffer is an octal transceiver whose function is to buffer the GPIA, address switch register and the LF, EOI, L decoder from the MPU data bus. The transceiver is enabled when the 488SEL signal is active (low) or when the TXSTB- signal is active. The 488SEL signal is activated by the MPU whenever a read or write to the GPIA is desired. The TXSTB- signal is activated by the DMA whenever a data transfer via DMA is desired.

6-206. The T/$\overline{\text{R}}$ (Transmit/Receive) input to the buffer is controlled by the RD/WRT (Read/Write) signal and by the TXSTB- signal. During an MPU operation, when the TXSTB- signal is inactive, the RD/WRT controls the T/$\overline{\text{R}}$ directly. A high on RD/WRT, which corresponds to the read mode, will drive T/$\overline{\text{R}}$ to a low, the receive mode. Since the buffer is oriented to the MPU, this is the correct mode for a read operation. A low on RD/WRT puts the buffer in the transmit mode. During a DMA transfer, the RD/WRT signal is placed in the write mode (low) by the DMA. This is done in order to insure that the data is written into the RAM and data latches. This presents a problem for the U9 buffer however, in that it needs to be in the receive mode which is contrary to what the RD/WRT signal is telling it to do. This problem is overcome by forcing the T/$\overline{\text{R}}$ input of U9 into receive (low) when TXSTB- is active.

6-207. 24/33 MHz OSCILLATOR A6. (Figures 6-77 and 6-78).

6-208. The 24/33 MHz oscillator generates ten discrete frequencies in 1-MHz steps controlled by the frequency extender module logic. Two isolated outputs are provided.

6-209. 24/33 MHz OSCILLATOR A6A1. (Figures 6-77 and 6-79). This board generates ten discrete frequencies from 24 to 33 MHz in 1-MHz increments. The board contains logic decoder U1, ten oscillators, and divide-by-three circuit U2. The oscillators operate from 72 to 99 MHz in 3-MHz increments so that after the divide-by-three circuit they produce the required 24-33 MHz. The logic decoder is a BCD-to-decimal decoder. The output frequency is given for each input BCD code in Table 6-10.

6-210. Each of the ten oscillators are identical, therefore, only one is described. When the four logic inputs are in a low state (0000 programmed), pin 1 of U1 is low. When pin 1 is low, PNP transistor Q10 goes on raising the collector to approximately 4.5V. This turns on 72-MHz NPN oscillator Q9, which is a Colpitts oscillator with the resonant circuit formed by C17, C18, and C19 resonating with L10. The output of the oscillator is formed by common coupling line L9 coupling energy from resonating inductor L10.

6-211. If another BCD code appears at the input, for example, a 1001, then pin 1 is high. The collector of Q10 goes to -12 Vdc. This condition provides no bias for Q9 and, therefore, this oscillator is off. However, pin 9 is low turning on Q16 which turns on oscillator Q15. The output of this oscillator also is coupled into the common coupling line L9.

6-212. Coupling line L9 feeds amplifier driver Q21 which boosts the signal level to drive U2. U2 is a dual Schottkey flip-flop wired as a frequency divide-by-three to generate the required 24 to 33 MHz. The output of this board drives the power divider on A6A2. The output level is a 2-volt peak-to-peak squarewave.

6-213. ISOLATOR DIVIDER A6A2 (Figures 6-77 and 6-80). The output from the 24/33 MHz oscillator is applied to power divider A1 which splits the signal into two outputs each feeding an isolation amplifier and bandpass filter. One amplifier isolator is on A6A3, the other is formed by Q1 on A6A2. The amplifier is followed by a 24- to 33-MHz bandpass filter which provides isolation to frequencies not in the passband and filters the harmonics of 24 to 33 MHz appearing at the filter input. The filter is formed by C4 through C8 and L2 through L6. The output is a sine wave at a level of about -6 dBm.

TABLE 6-10. BCD OUTPUT FREQUENCIES

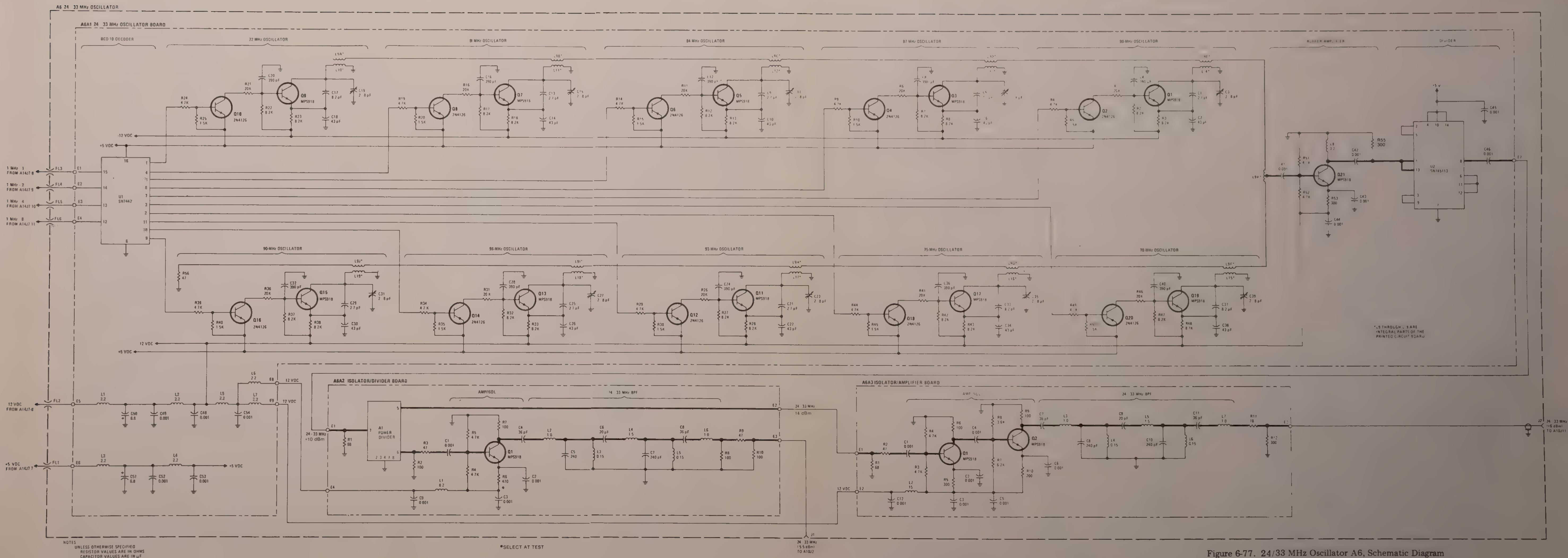
BCD Code					
1 MHz-1	1 MHz-2	1 MHz-4	1 MHz-8	Oscillator Frequency	Output Frequency
0	0	0	0	72	24 MHz
1	0	0	0	75	25
0	1	0	0	78	26
1	1	0	0	81	27
0	0	1	0	84	28
1	0	1	0	87	29
0	1	1	0	90	30
1	1	1	0	93	31
0	0	0	1	96	32
1	0	0	1	99	33

6-214. **ISOLATOR/AMPLIFIER A6A3.** (Figures 6-77 and 6-81). Q1 and Q2 form an isolator/amplifier for the 24- to 33-MHz signal. The output of the amplifier feeds a bandpass filter consisting of C7 through C11 and L3 through L7. This filter filters out harmonics which appear at its input as well as providing isolation to signals outside the filter bandpass. The level out of this board is a +6 dBm sine wave.

6-215. **366 MHz FILTER CONVERTER A7.** (Figures 6-82 and 6-83).

6-216. The 366 MHz filter converter mixes the 337-MHz output from A10J1 with the 29-MHz from the modulation unit producing 366 MHz. The 366-MHz signal is selected by the bandpass filter. The output of the filter is mixed with the 35-36 MHz output from the higher order BCD producing 330-331 MHz for further processing in filter amplifier A9.

6-217. **366 MHz FILTER/CONVERTER INPUT BOARD A7A1.** (Figures 6-82 and 6-84). This board mixes the 337-MHz signal from the 337-MHz oscillator isolator with 29 MHz. The 29 MHz comes from an optical modulator in the RF plug-in or from 29-MHz converter A10A6. Mixer M1 produces the sum and difference of these two frequencies. The sum frequency is 366 MHz. L3 and C1 are a resonant circuit tuned to 366 MHz which select the proper input frequency for the two-stage amplifier formed by Q1 and Q2. This amplifier provides a gain of 11 dB, after the mixer conversion loss of 7 dB yielding an overall gain of 4 dB.


A6 24 33 MHz OSCILLATOR
A6A1 24 33 MHz OSCILLATOR BOARD
BCD-10 DECODER
72 MHz OSCILLATOR
90-MHz OSCILLATOR
87 MHz OSCILLATOR
75-MHz OSCILLATOR
78-MHz OSCILLATOR
BUFFER AMPLIFIER
DIVIDER
U1 SN7447
U2 SN74S113
Q10 2N4126
Q9 MPS918
Q21 MPS918
R55 300
-12 VDC
+5 VDC
1 MHz 1 FROM A14J7 8
1 MHz 2 FROM A14J7 9
1 MHz 4 FROM A14J7 10
1 MHz 8 FROM A14J7 11
12 VDC FROM A14J7-6
+5 VDC FROM A14J7 7
FL1
FL2
FL3
FL4
FL5
FL6
A6A2 ISOLATOR/DIVIDER BOARD
AMP/ISOL
24 33 MHz BPF
A1 POWER DIVIDER
24 - 33 MHz +10 dBm
24 33 MHz 14 dBm
A6A3 ISOLATOR/AMPLIFIER BOARD
J1 24 33 MHz 15.5 dBm TO A10J2
J2 24 33 MHz (+6 dBm) TO A10J11
*L9 THROUGH L9 ARE INTEGRAL PARTS OF THE PRINTED CIRCUIT BOARD

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN µF
INDUCTOR VALUES ARE IN µH

*SELECT AT TEST

Figure 6-77. 24/33 MHz Oscillator A6, Schematic Diagram

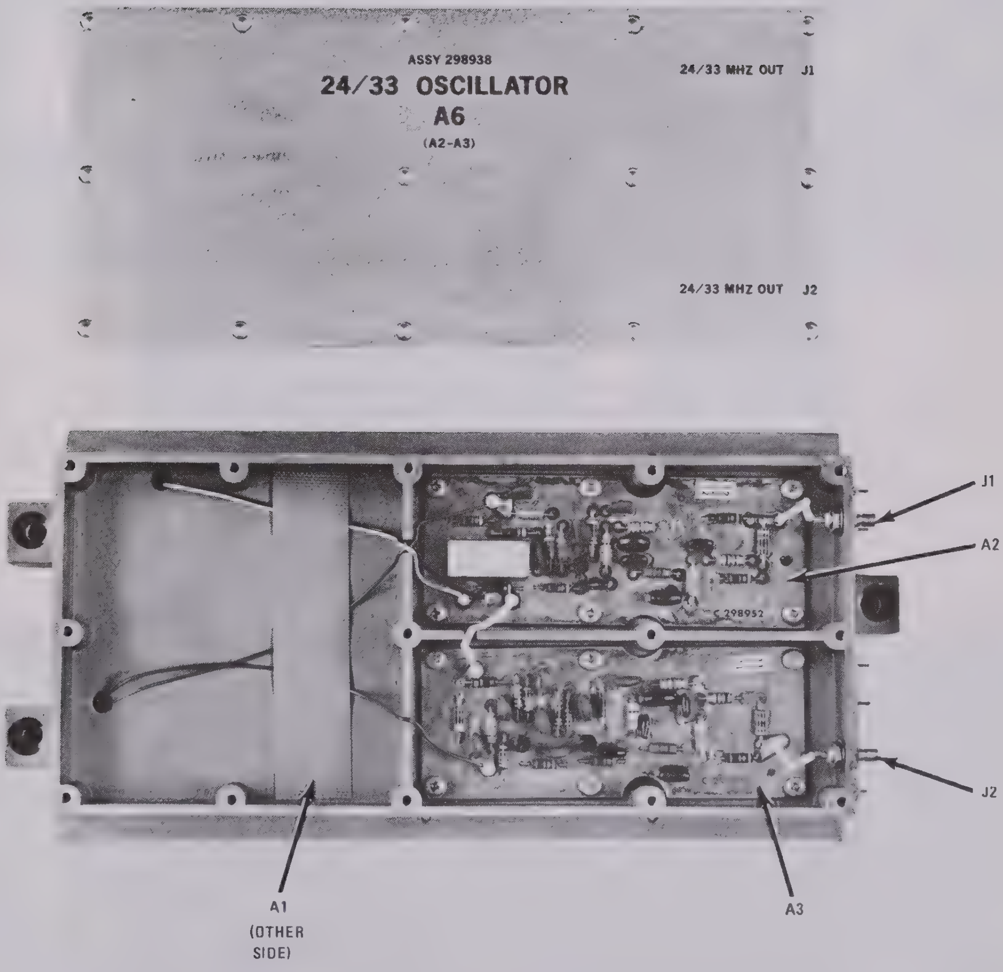

ASSY 298938
24/33 OSCILLATOR
A6
(A2-A3)
24/33 MHZ OUT J1
24/33 MHZ OUT J2
C 298952
J1
A2
J2
A1
(OTHER SIDE)
A3

Figure 6-78. 24/33 MHz Oscillator A6

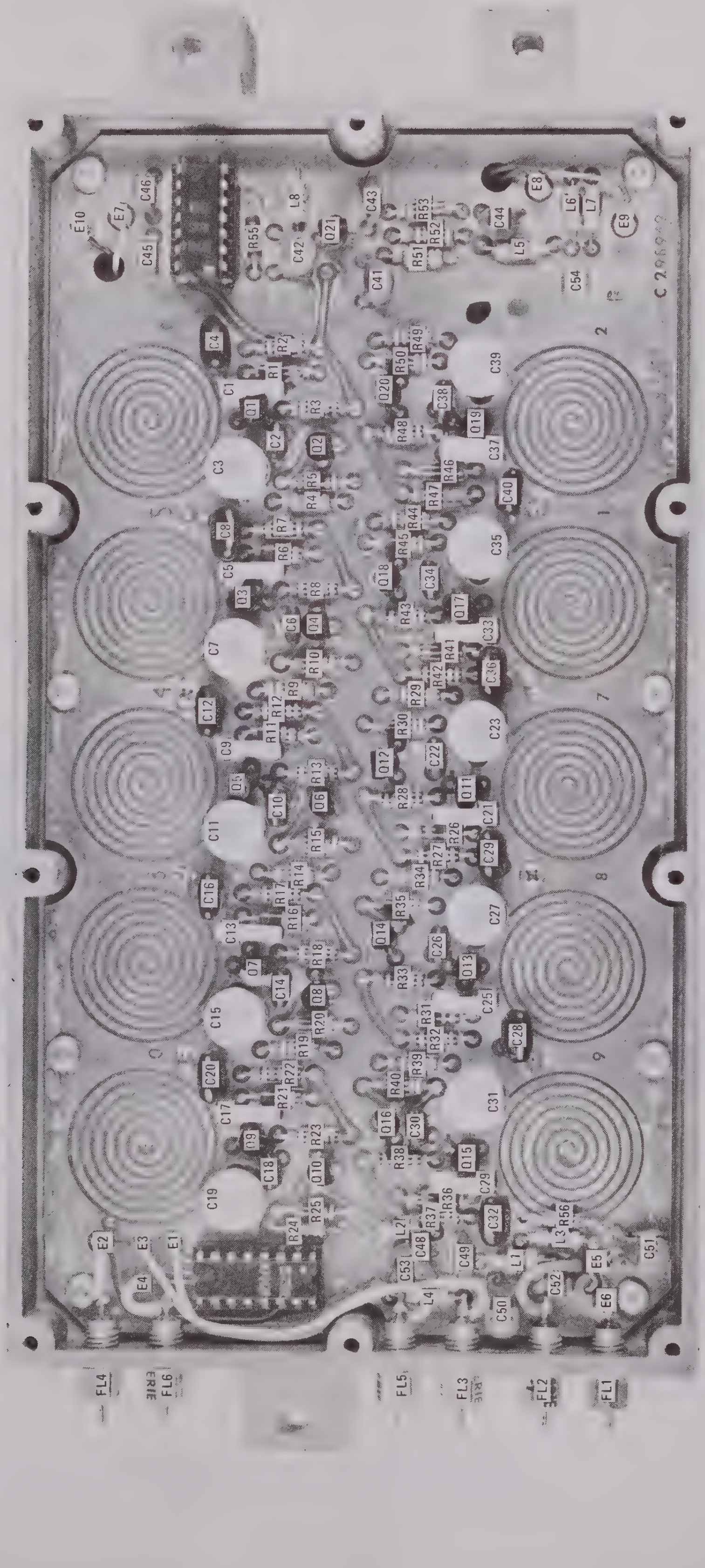

E10
E7
C46
C45
R55
L8
C42
Q21
C43
R53
R52
R51
C44
E8
L6
L7
E9
L5
C54
C41
C4
R2
R1
C1
Q1
R3
C2
Q2
C3
R5
R4
C8
R7
R6
C5
R8
Q3
C6
Q4
C7
R10
R9
C12
R12
R11
C9
Q5
R13
C10
Q6
C11
R15
C16
R17
R14
R16
C13
R18
Q7
C14
Q8
C15
R20
R19
C20
R22
R21
C17
Q9
R23
C18
Q10
C19
R24
R25
E2
E3
E1
E4
R49
R50
Q20
C39
C38
R48
Q19
C37
R46
C40
R47
R44
R45
C35
Q18
C34
R43
Q17
C33
R41
R42
C36
R29
R30
C23
Q12
C22
R28
Q11
C21
R26
R27
C29
R34
R35
C27
Q14
C26
R33
Q13
C25
R31
R32
C28
R39
R40
C31
Q16
C30
R38
Q15
C29
R36
R37
C32
R56
L3
L2
C48
C49
C53
L1
C52
E5
C51
L4
C50
E6
FL4
FL6
FL5
FL3
FL2
FL1

Figure 6-79. 24/33 MHz Oscillator Board A6A1, Component Locations

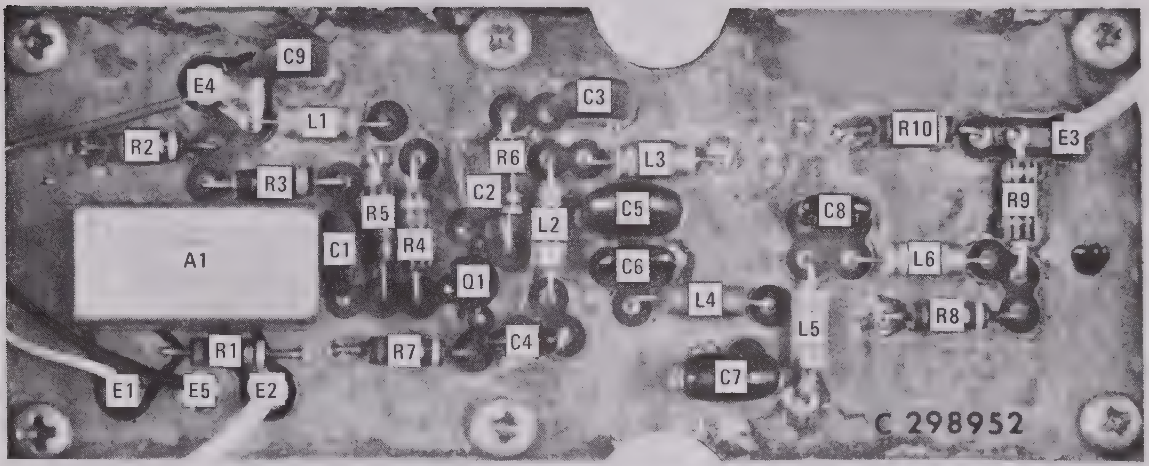

C9
E4
L1
C3
R2
R6
L3
R10
E3
R3
C2
C5
C8
R5
L2
R9
A1
C1
R4
C6
L6
Q1
L4
R8
L5
R1
R7
C4
C7
E1
E5
E2
C 298952

Figure 6-80. Isolator/Divider Board A6A2, Component Locations

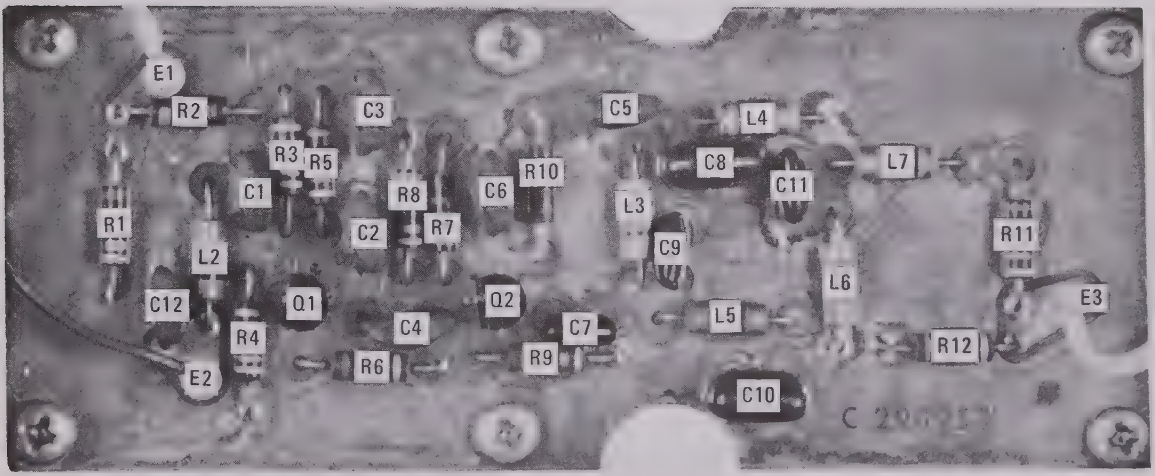

E1
R2
C3
C5
L4
R3
R5
C8
L7
C1
R8
C6
R10
C11
R1
C2
R7
L3
R11
C9
L2
L6
E3
C12
Q1
Q2
L5
R4
C4
C7
R12
R9
E2
R6
C10

Figure 6-81. Isolator/Amplifier Board A6A3, Component Locations

6-218. 366 MHz FILTER CONVERTER OUTPUT BOARD A7A2. (Figures 6-82 and 6-85). The 366-MHz signal from the bandpass filter is further amplified by Q3 and Q4 which form a 20-dB gain amplifier at 366 MHz. The output of this amplifier is at a level of +7 dBm to drive the local oscillator input of mixer M2. The 35/36 MHz from BCD section A4 is applied to a 35/36 MHz bandpass filter consisting of C19 through C22 and L7 through L9. This filter removes the 8.75 to 9 MHz sidebands which appear on the BCD output. This filtered 35/36-MHz signal is mised with the 366-MHz signal in M2 to produce sum and difference frequencies. This signal is applied to 330/331 MHz filter amplifier A9 which selects the difference frequency 330/331 MHz.

6-219. 366 MHz BANDPASS FILTER A7A3. (Figure 6-82). This is an 1/8 wavelength stripline bandpass filter tuned to 366 MHz. The insertion loss is 7 dB. It has a 3-dB bandwidth of approximately 3 MHz and provides greater than 80-dB rejection bandwidth of 58 MHz. This in a combination with the tuned amplifiers provides the filtering required for 100-dB spurious performance.

6-220. 361/370 MHz FILTER AMPLIFIER A8. (Figures 6-86 and 6-87).

6-221. The 361/370 MHz filter takes the output from the mixer on board A10A2 which heterodynes the 337 MHz with the output of the 24 to 33 MHz oscillator producing 361-370 MHz. The upper sideband generated in this mixer is selected by band-pass filter A8A3.

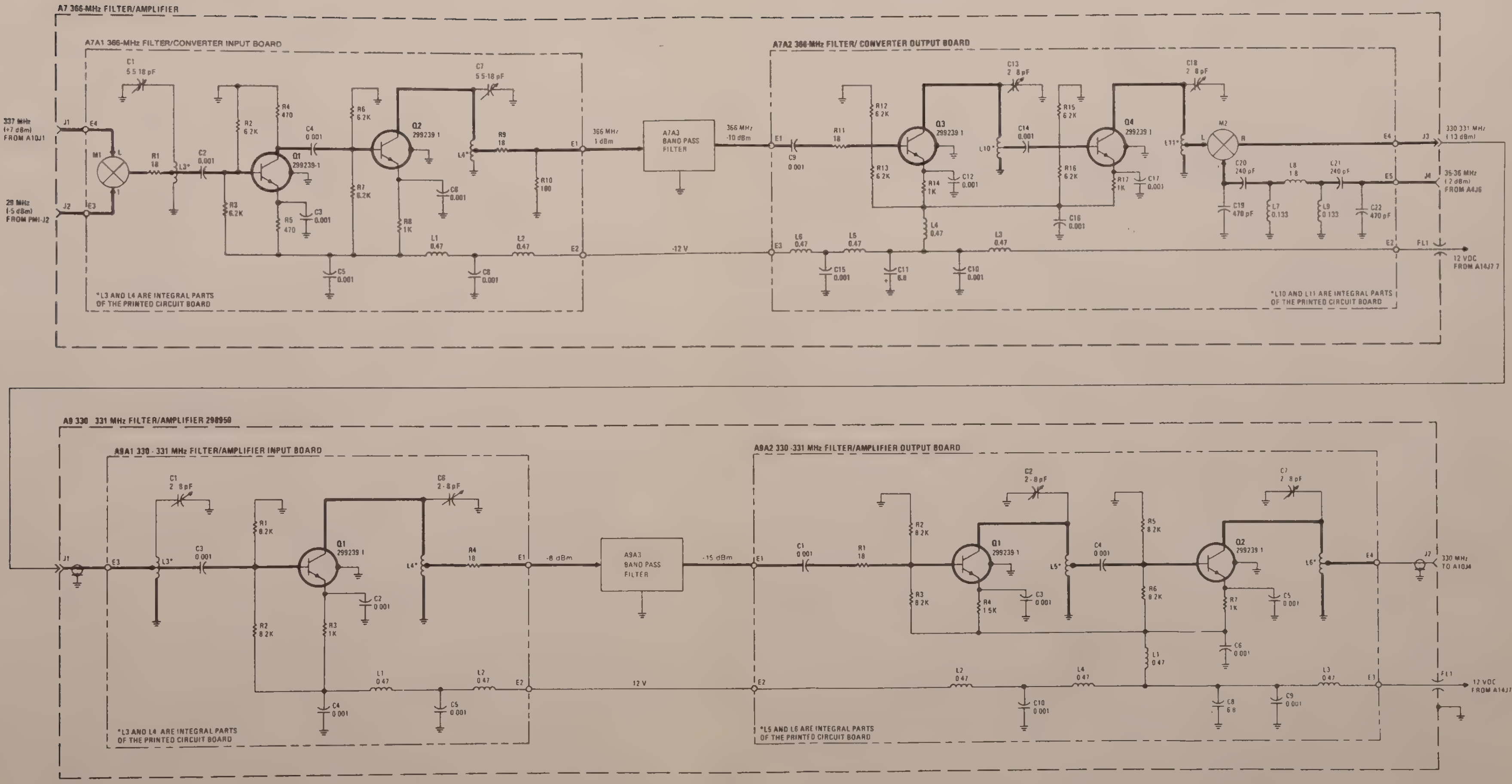
6-222. 361/370 MHz INPUT BOARD A8A1. (Figures 6-86 and 6-88). Input board A8A1 amplifies the input signal by 5 dB. Resistors R1 and R2 form a 3-dB pad to terminate the mixer output. Capacitor C1 and inductor L3 form a 365-MHz resonant circuit to select the correct signal for the amplifier input. Transistor Q1 has a gain of 8 dB at 365 MHz. C5 and L4 tune this amplifier and provide impedance matching at the output.

6-223. 361/370 MHz OUTPUT BOARD A8A2. (Figures 6-86 and 6-89). Output board A8A2 provides 15 dB of gain also tuned to 365 MHz. Amplifier Q1 is tuned by the resonant circuit formed by C4 and L8. The output of Q1 is coupled through C5 into a similar circuit. Q2 is the amplifier; C7 and L9 are the resonant circuits. Inductors L3, L4, and L5 and capacitors C9 and C10 comprise a 400-MHz low-pass filter to further reject the second harmonic of the output signal. Resistors R8 and R9 form a 3-dB pad. The output of this board goes to the RF input of mixer A10A4.

6-224. 361/370 MHz BANDPASS FILTER A8A3. (Figure 6-86). This board is a 7-pole bandpass filter covering the band from 361 to 370 MHz. The insertion loss is approximately 10 dB. This filter, in conjunction with the selectivity of the input and output amplifiers, provides 100-dB rejection at 337 MHz and below.

6-225. 330-331 MHz FILTER AMPLIFIER A9. (Figures 6-82 and 6-90).

6-226. The 330-331 MHz filter amplifier selects the 330- to 331-MHz lower sideband from the mixer in filter converter A7.


A7 366-MHz FILTER/AMPLIFIER
A7A1 366-MHz FILTER/CONVERTER INPUT BOARD
337 MHz (+7 dBm) FROM A10J1
29 MHz (-5 dBm) FROM PMI-J2
J1
J2
E4
E3
M1
L
I
C1 5 5 18 pF
R1 18
L3*
C2 0.001
R2 6 2K
R3 6.2K
R4 470
R5 470
C3 0.001
Q1 299239-1
C4 0 001
R6 6.2K
R7 6.2K
R8 1K
Q2 299239 1
C6 0.001
C5 0.001
L1 0.47
C8 0.001
L2 0.47
C7 5 5-18 pF
L4*
R9 18
R10 180
E1
E2
*L3 AND L4 ARE INTEGRAL PARTS OF THE PRINTED CIRCUIT BOARD
366 MHz 1 dBm
A7A3 BAND PASS FILTER
366 MHz -10 dBm
-12 V
A7A2 366-MHz FILTER/ CONVERTER OUTPUT BOARD
E1
C9 0 001
R11 18
R12 6 2K
R13 6 2K
Q3 299239 1
R14 1K
C12 0.001
C13 2 8 pF
L10*
C14 0.001
R15 6 2K
R16 6 2K
C16 0.001
Q4 299239 1
R17 1K
C17 0.001
C18 2 8 pF
L11*
M2
L
R
C20 240 pF
L8 1 8
C21 240 pF
C19 470 pF
L7 0.133
L9 0 133
C22 470 pF
E4
E5
E2
E3
J3
J4
330 331 MHz (13 dBm)
35-36 MHz (2 dBm) FROM A4J6
FL1
12 VDC FROM A14J7 7
L6 0.47
L5 0.47
L4 0.47
L3 0.47
C15 0.001
C11 6.8
C10 0.001
*L10 AND L11 ARE INTEGRAL PARTS OF THE PRINTED CIRCUIT BOARD
A9 330 331 MHz FILTER/AMPLIFIER 298950
A9A1 330 - 331 MHz FILTER/AMPLIFIER INPUT BOARD
J1
E3
C1 2 8pF
L3*
C3 0 001
R1 8.2K
R2 8 2K
Q1 299239 I
C2 0 001
R3 1K
C6 2-8pF
L4*
R4 18
E1
E2
L1 0 47
L2 0 47
C4 0 001
C5 0 001
*L3 AND L4 ARE INTEGRAL PARTS OF THE PRINTED CIRCUIT BOARD
-8 dBm
A9A3 BAND PASS FILTER
-15 dBm
12 V
A9A2 330 -331 MHz FILTER/AMPLIFIER OUTPUT BOARD
E1
C1 0 001
R1 18
R2 8 2K
R3 8 2K
Q1 299239 1
R4 1 5K
C3 0 001
C2 2-8pF
L5*
C4 0 001
R5 8 2K
R6 8 2K
Q2 299239 1
R7 1K
C5 0 001
C6 0 001
C7 2 8 pF
L6*
E4
J7
330 MHz TO A10J4
E2
L2 0 47
C10 0 001
L4 0 47
L1 0 47
C8 6 8
C9 0 001
L3 0 47
E3
FL1
12 VDC FROM A14J7 7
*L5 AND L6 ARE INTEGRAL PARTS OF THE PRINTED CIRCUIT BOARD

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN µF
INDUCTOR VALUES ARE IN µH

Figure 6-82. 366 MHz Filter Converter A7 and 330/331 Filter Amplifier A9, Schematic Diagram

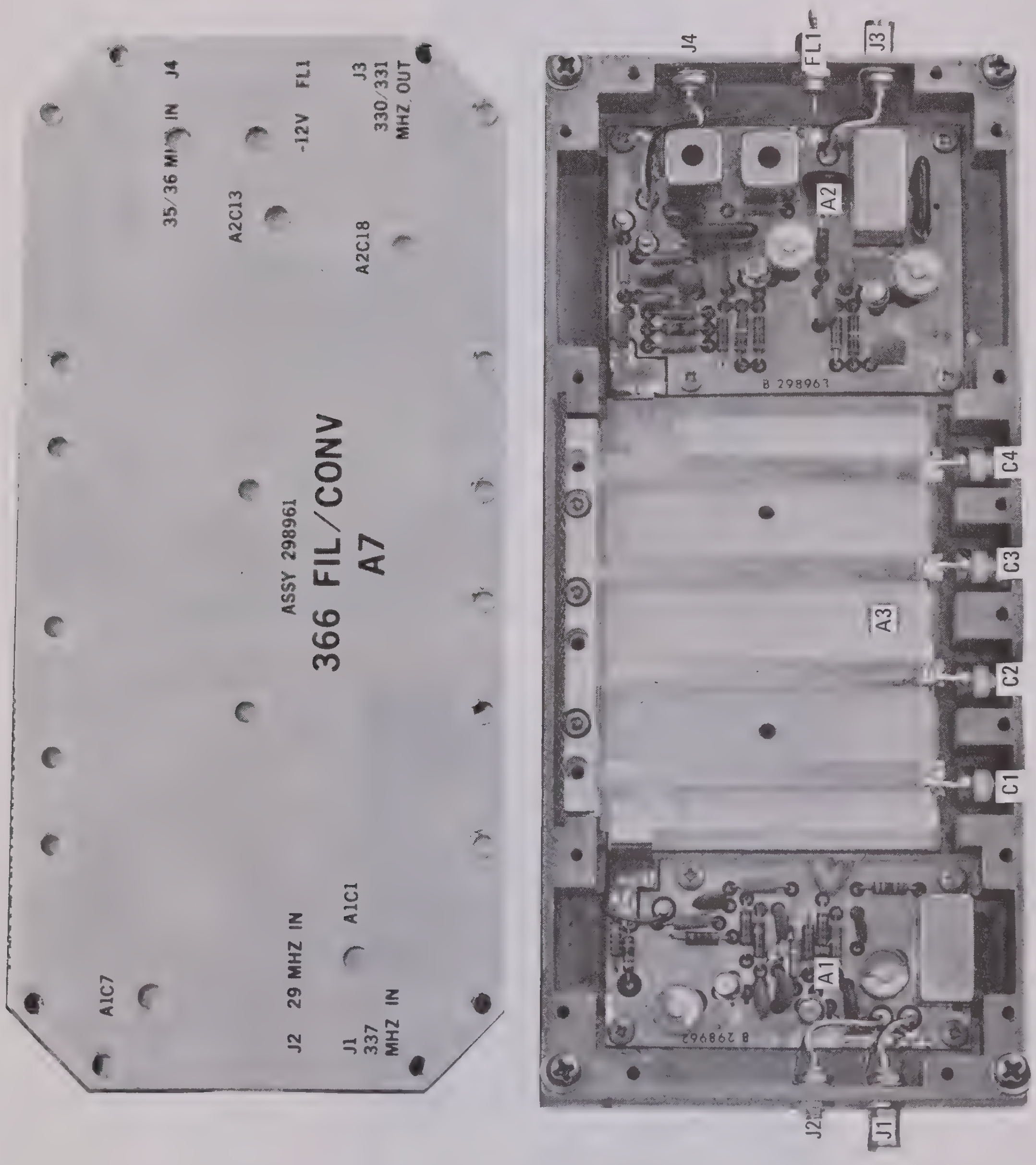

J4
35/36 MHZ IN
FL1
-12V
J3
330/331 MHZ OUT
A2C13
A2C18
ASSY 298961
366 FIL/CONV
A7
A1C1
29 MHZ IN
J2
A1C7
J1
337 MHZ IN
J4
FL1
J3
A2
B 298963
C4
C3
A3
C2
C1
A1
B 298962
J2
J1

Figure 6-83. 366 MHz Filter Converter A7

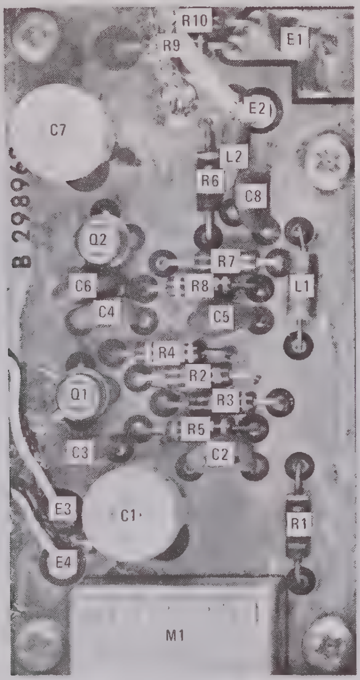

R10
R9
E1
E2
C7
L2
R6
C8
B 29896
Q2
R7
C6
R8
L1
C4
C5
R4
R2
Q1
R3
R5
C3
C2
E3
C1
R1
E4
M1

Figure 6-84. 366 MHz Filter Converter Input Board A7A1, Component Locations

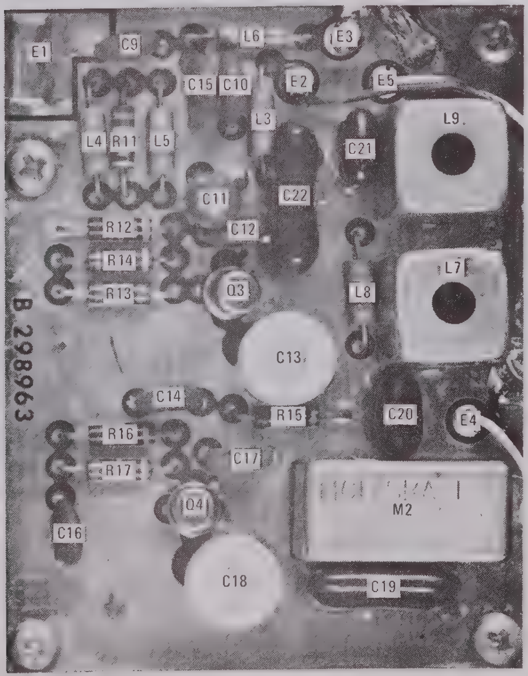
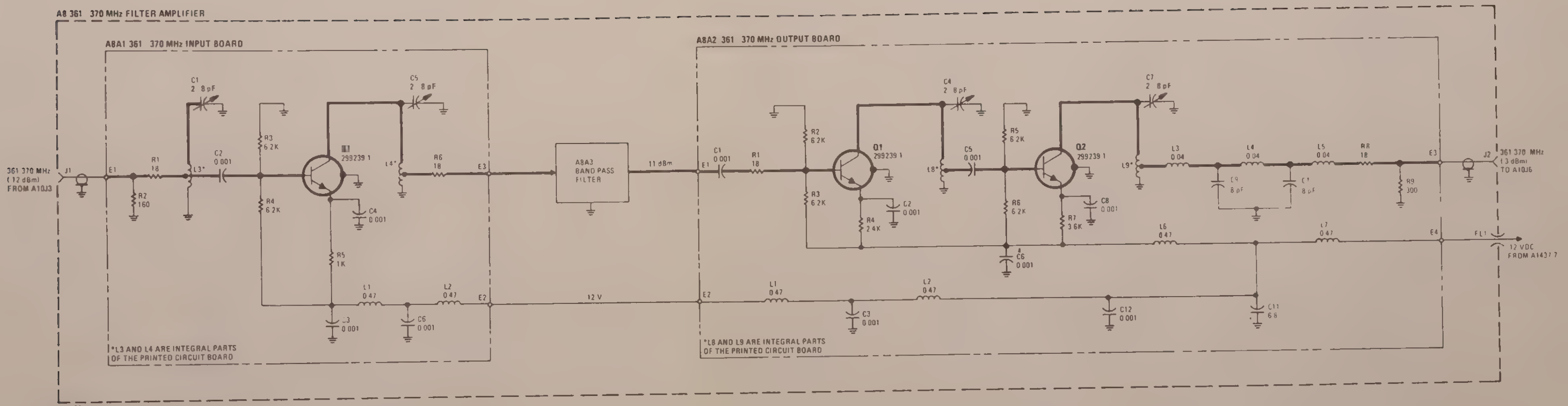

E1
C9
L6
E3
C15
C10
E2
E5
L3
L9
L4
R11
L5
C21
C11
C22
R12
C12
R14
L7
R13
Q3
L8
B 298963
C13
C14
R15
C20
E4
R16
C17
R17
Q4
M2
C16
C18
C19

Figure 6-85. 366 MHz Filter Converter Output Board A7A2, Component Locations


A8 361 370 MHz FILTER AMPLIFIER
A8A1 361 370 MHz INPUT BOARD
361 370 MHz
(12 dBm)
FROM A10J3
J1
E1
R1
18
R2
160
C1
2 8pF
L3*
C2
0 001
R3
6 2K
R4
6 2K
299239 1
C4
0 001
R5
1K
C5
2 8pF
L4*
R6
18
E3
L1
0 47
L2
0 47
E2
C3
0 001
C6
0 001
*L3 AND L4 ARE INTEGRAL PARTS
OF THE PRINTED CIRCUIT BOARD
A8A3
BAND PASS
FILTER
11 dBm
12 V
A8A2 361 370 MHz OUTPUT BOARD
E1
C1
0 001
R1
18
R2
6 2K
R3
6 2K
Q1
299239 1
C2
0 001
R4
2 4K
C4
2 8pF
L8*
C5
0 001
R5
6 2K
R6
6 2K
C6
0 001
Q2
299239 1
C8
0 001
R7
3 6K
C7
2 8pF
L9*
L3
0 04
L4
0 04
L5
0 04
C9
8 pF
C1
8 pF
R8
18
R9
300
E3
J2
361 370 MHz
(3 dBm)
TO A10J6
L6
0 47
L7
0 47
E4
FL1
12 VDC
FROM A14J7 7
E2
L1
0 47
L2
0 47
C3
0 001
C12
0 001
C11
6 8
*L8 AND L9 ARE INTEGRAL PARTS
OF THE PRINTED CIRCUIT BOARD

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-86. 361/370 MHz Filter Amplifier A8, Schematic Diagram

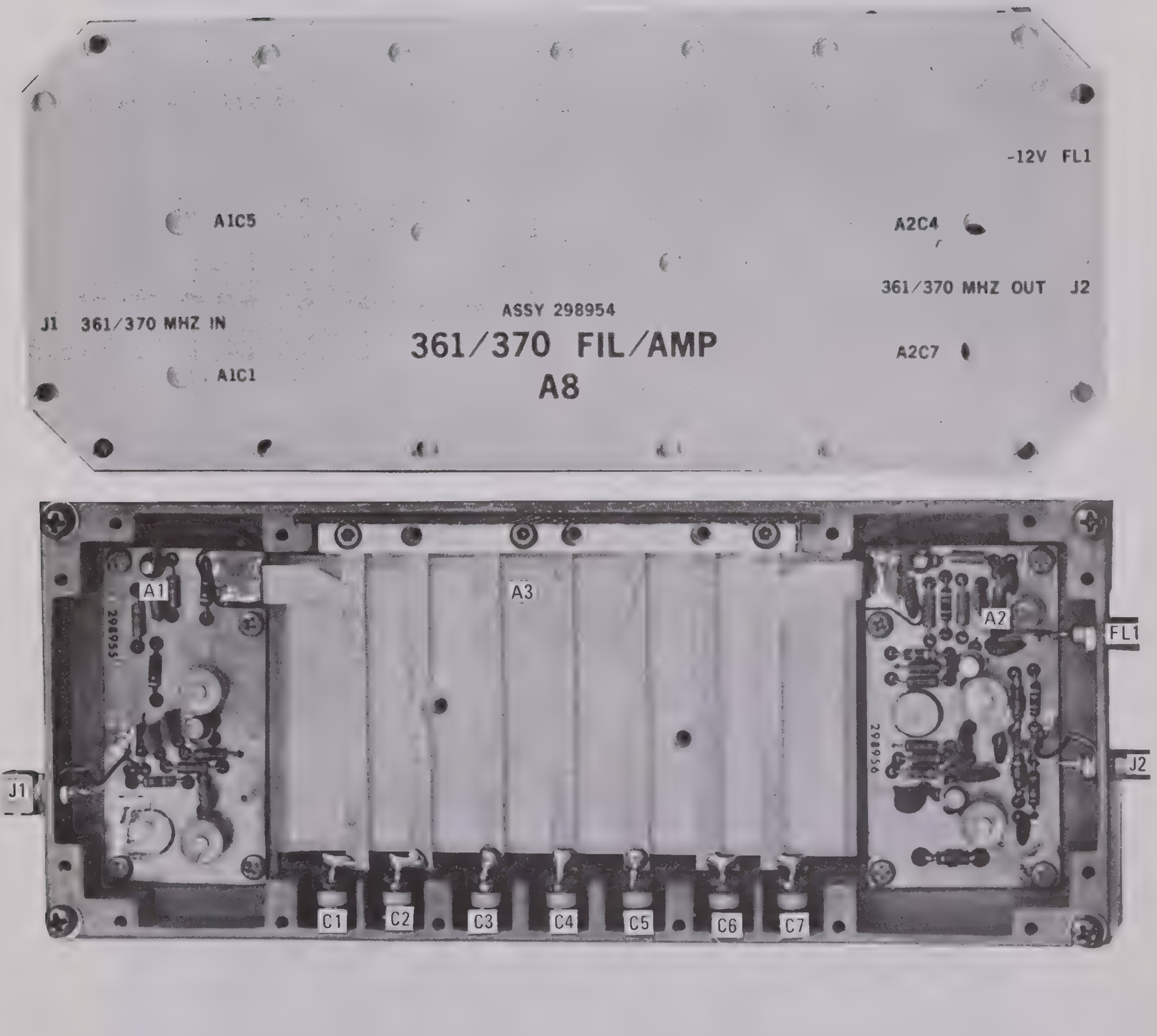

-12V FL1
A1C5
A2C4
361/370 MHZ OUT J2
ASSY 298954
J1 361/370 MHZ IN
361/370 FIL/AMP
A8
A2C7
A1C1
A1
A3
A2
FL1
298955
298956
J1
J2
C1
C2
C3
C4
C5
C6
C7

Figure 6-87. 361/370 MHz Filter Amplifier A8

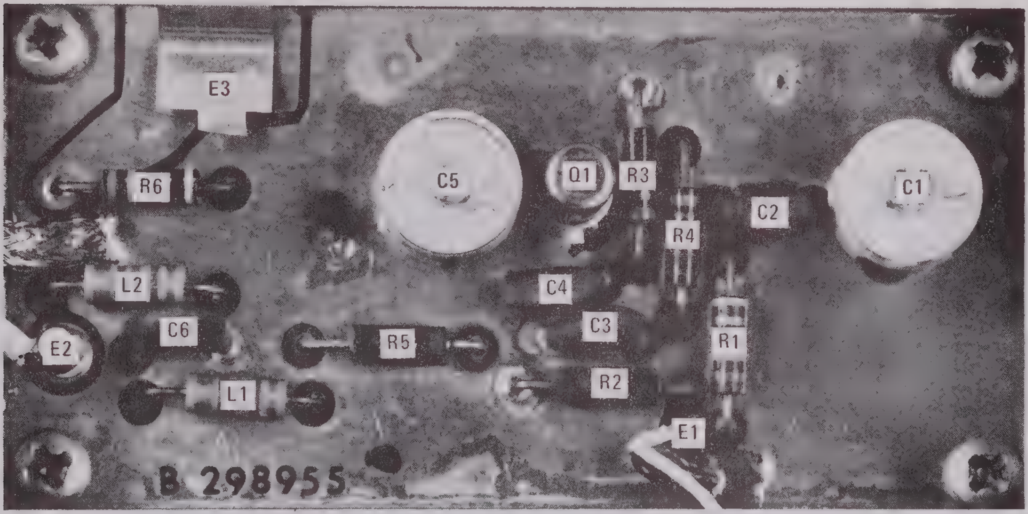

E3
R6
C5
Q1
R3
C1
C2
R4
L2
C4
C3
C6
E2
R5
R1
R2
L1
E1
B 298955

Figure 6-88. 361/370 MHz Filter Amplifier Input Board A8A1, Component Locations

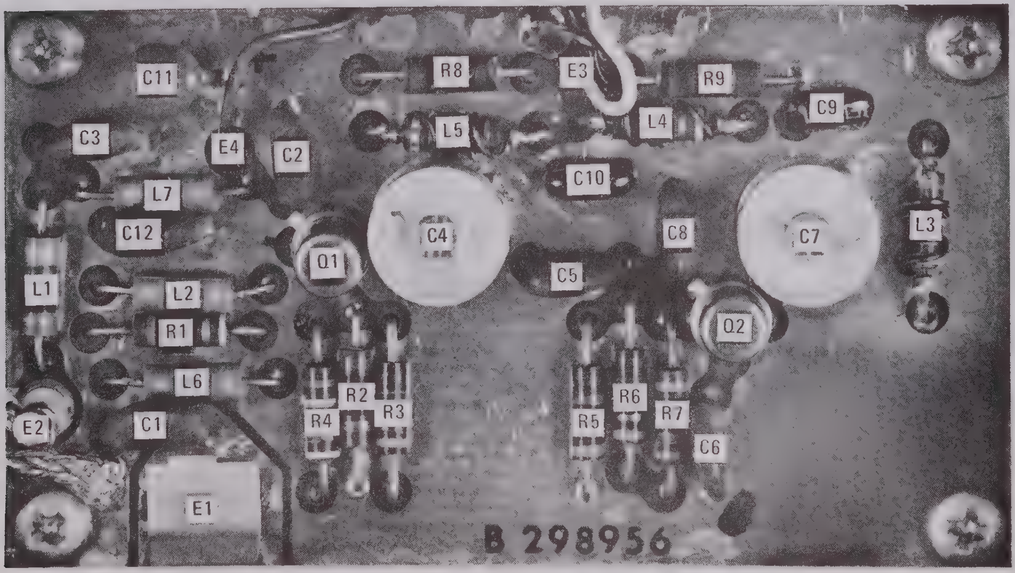

C11
R8
E3
R9
C9
L5
L4
C3
E4
C2
C10
L7
C12
C4
C8
C7
L3
Q1
C5
L1
L2
R1
Q2
L6
R2
R6
R3
R7
R4
R5
E2
C1
C6
E1
B 298956

Figure 6-89. 361/370 MHz Filter Amplifier Output Board A8A2, Component Locations

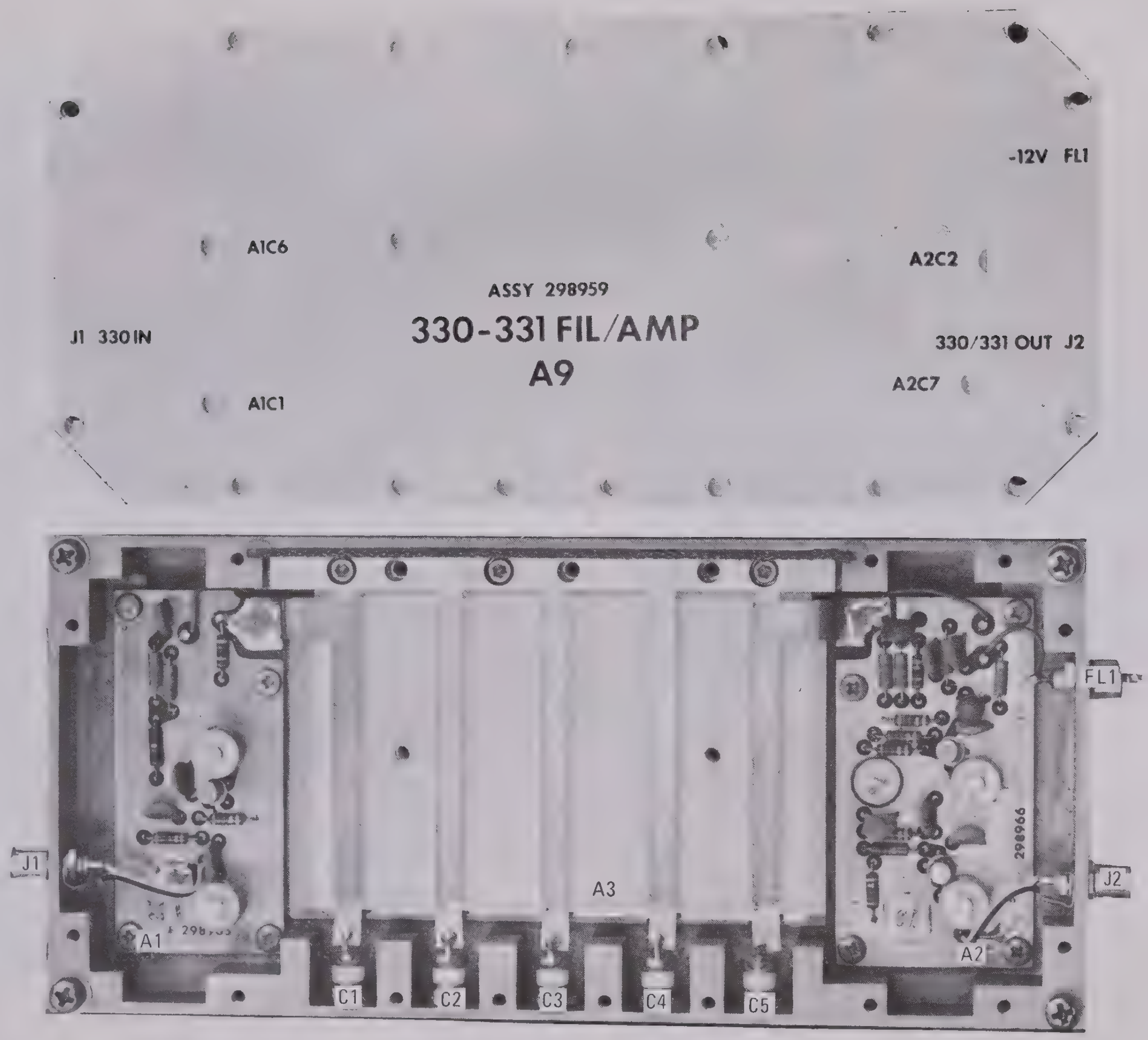

-12V FL1
A1C6
A2C2
ASSY 298959
330-331 FIL/AMP
A9
J1 330 IN
330/331 OUT J2
A1C1
A2C7
FL1
J1
J2
298966
A3
A1
A2
C1
C2
C3
C4
C5

Figure 6-90. 330-331 MHz Filter Amplifier A9

6-227. 330-331 MHz FILTER AMPLIFIER INPUT BOARD A9A1. (Figure 6-91). L3 and C1 form a resonant circuit tuned to 330.5 MHz to select the proper input for transistor amplifier Q1. Q1 is a 10-dB gain amplifier at 330.5 MHz tuned by the resonant circuit L4 and C6. The amplified signal is applied to narrowband bandpass filter A9A3 at 330-331 MHz.

6-228. 330-331 MHz FILTER AMPLIFIER OUTPUT BOARD A9A2. (Figure 6-92). Transistors Q1 and Q2 form a 20-dB gain tuned amplifier at 330.5 MHz.

6-229. 330-331 MHz BANDPASS FILTER A9A3. (Figure 6-82). This unit is a 5-pole stripline bandpass filter having a 3-dB bandwidth of greater than 3 MHz. It provides 90 dB of rejection at 366 MHz so that in combination with the tuned amplifiers the 100-dB spur performance is guaranteed.

6-230. HIGH FREQUENCY SUBASSEMBLY A10. (Figures 6-93 and 6-94).

6-231. The high frequency subassembly houses all the remaining printed circuit boards used in the frequency extender.

6-232. 337 MHz OSCILLATOR/ISOLATOR A10A1. (Figures 6-93 and 6-95). This board contains a 112.3333 MHz crystal oscillator, a times three multiplier, and a four-stage active isolator. Y1 is a 112.3333 MHz crystal in an oscillator circuit formed by Q1 and associated components. Q2 is a 112-MHz amplifier which buffers and amplifies the oscillator output. Q3 and Q4 multiply this signal X3 producing 337 MHz.

6-233. At this point the 337-MHz signal is split two ways to drive two active isolators. One isolator is A10A2; the other is on this board. The isolator consists of four tuned amplifier stages followed by a 400-MHz low-pass filter. Amplifiers Q5-Q8, are tuned by the LC circuits on each collector. The tuning capacitors for the isolator are C22, C26, C30, and C34, respectively. Each amplifier is separated by a resistive attenuator to increase the reverse isolation. Capacitors C36, C38, C39, and inductors L17, L18, and L19 comprise a 400-MHz lowpass filter to reject harmonics of the 337-MHz signal. The output level at A7J1 is +7 dBm.

6-234. 337 MHz ISOLATOR BOARD A10A2. (Figures 6-93 and 6-96). This board contains an active isolator similar to A10A1. There are four, tuned-amplifier stage attenuators. Each stage provides gain in the forward direction and about 20-dB isolation in the reverse direction. The stages are separated by resistive attenuators providing increased isolation. The signal from the 337-MHz oscillator board is applied to the input attenuator formed by R1 and R2. It is amplified by common emitter stage Q1. The collector of Q1 is tuned by L9 and the parallel combination of C4 and C5.

6-235. The output of Q1 goes through three more similar stages: Q2, Q3, and Q4. Each stage provides about 20-dB reverse isolation. The tuning capacitors are C5, C9, C15, and C20. Each one is tuned for a maximum output at the mixer local oscillator input. The output of the last stage feeds a 400-MHz lowpass filter consisting of L6 through L8 and C23 through C25. The output of the filter drives mixer M1. The other input to M1 comes from 24/33 MHz oscillator at A6J1. Mixer M1 combines two signals producing sum and difference frequencies. The upper sideband, 361-370 MHz, is selected by 361-370 MHz bandpass filter A8.

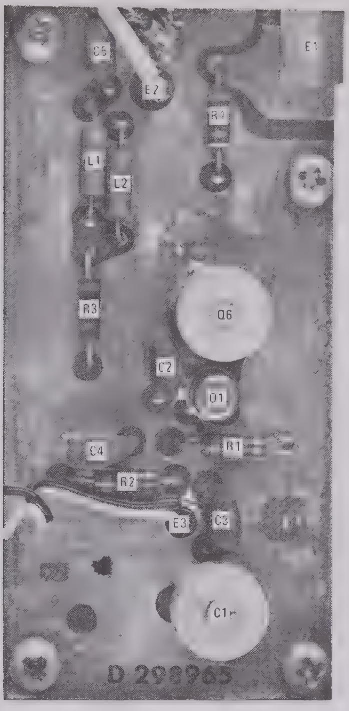

C5
E2
E1
R4
L1
L2
R3
Q6
C2
Q1
C4
R1
R2
E3
C3
C1
D 298965

Figure 6-91. 330-331 MHz Filter Amplifier Input Board A9A1, Component Locations

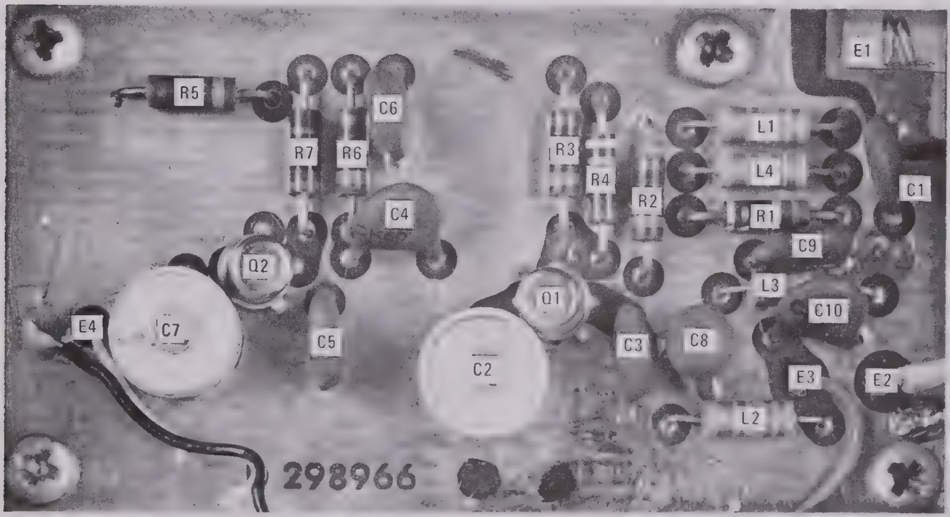

R5
C6
R7
R6
C4
Q2
E4
C7
C5
R3
R4
R2
Q1
C2
C3
C8
E1
L1
L4
C1
R1
C9
L3
C10
E3
E2
L2
298966

Figure 6-92. 330-331 MHz Filter Amplifier Output Board A9A2, Component Locations

6-236. 30-40 MHz BANDPASS FILTER A10A3. (Figures 6-97 and 6-98). The 30-40 MHz bandpass filter selects the lower sideband of the A10A2 mixer output and provides 14-dB gain for the desired signal. It consists of two identical bandpass filters with an amplifier separating the two. The first filter is formed by C1, C2, C3, C5, L1, L2, and L3. Transistor Q1 and associated components comprise the amplifier which drives the output filter formed by C7-C10 and L4-L6. The input and output impedance of this board are 50 ohms.

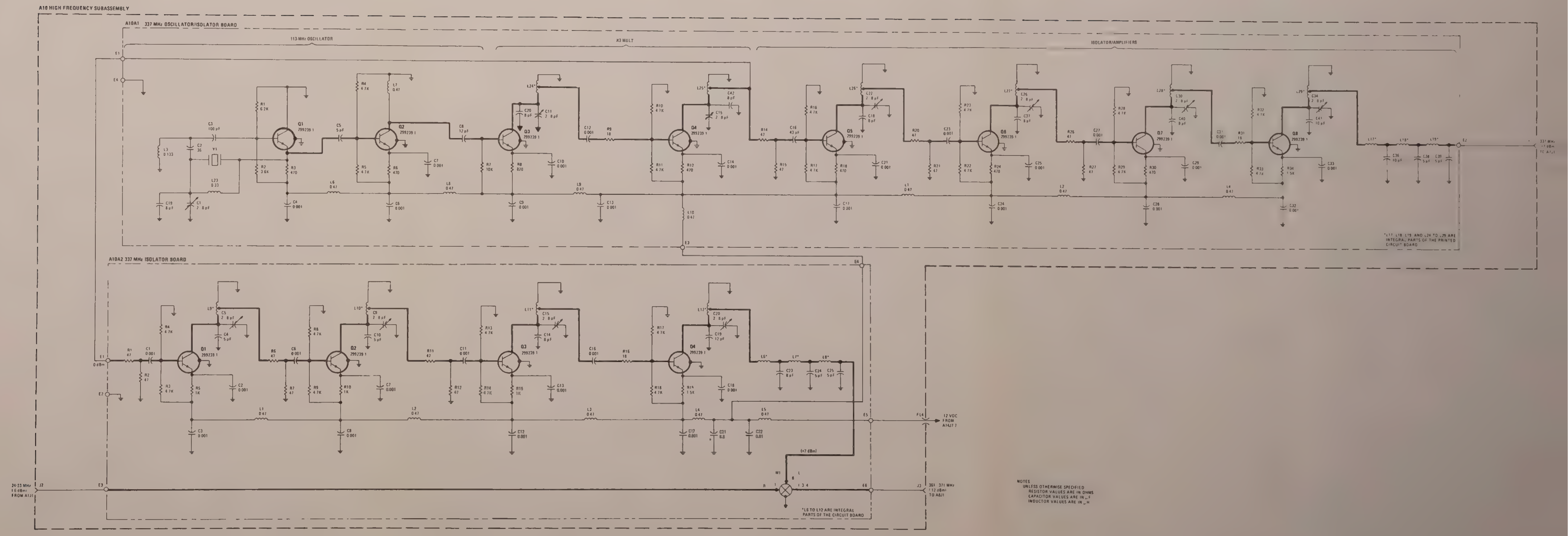
6-237. 330.5 MHz POWER AMPLIFIER A10A4. (Figures 6-97 and 6-99). This unit amplifies the output of 330-331 MHz bandpass filter A9 to a level of +17 dBm, which is the local oscillator input to M1. The output of 361-370 MHz bandpass filter A8 is mixed with the signal in M1, producing the sum and difference frequencies. The 30-40 MHz difference frequency is the desired output of the frequency extender. Board A10A3 amplifies the 30-40 MHz and rejects all other mixer outputs.

6-238. The amplifier portion of this board consists of three stages. The first stage formed by Q1 and associated components is tuned by C9 and L7 providing 10 dB of gain at 330.5 MHz. This feeds 6-dB gain amplifier Q2 which is tuned by C10 and L8 to 330.5 MHz. The third 6-dB gain amplifier, Q3, develops the final power which is tuned by C14 and L9. The harmonics of final signal are removed by the 400-MHz lowpass filter formed by L4, L5, L6, C11, and C12. The signal from the local oscillator drives mixer M1 which mixes with the 361-370 MHz producing 30-40 MHz. The mixer IF output passes through a 6-dB attenuator. The output of the board is applied to A10A3.

6-239. 1-5 MHz COMB GENERATOR A10A5. (Figures 6-100 and 6-101). This board contains a programmable divider and lowpass filters. It generates dc, 1, 2, 3, 4, or 5 MHz depending on the 1-MHz frequency control BCD digit. The 1-MHz digit appears on E1, E2, E3, E4, and A10 FL7, FL8, FL9, FL10, respectively. These logic signals are applied to the Prom U1 which controls the programmable divider consisting of 4 dividers U2, U3, U4 and U5. Table 6-11 shows the truth table for Prom U1.

TABLE 6-11. PROM A10A5 U1 TRUTH TABLE

BCD Digit	Input Code				Divider Function	U1-1	U1-2	U1-3	U1-4	U1-5	U1-6	U1-7	Fout
	E4	E3	E2	E1									
0	0	0	0	0	12	1	0	1	0	0	0	1	5 MHz
1	0	0	0	1	15	1	0	1	0	0	1	1	4 MHz
2	0	0	1	0	20	0	1	1	0	1	1	0	3 MHz
3	0	0	1	1	30	0	1	1	0	1	1	1	2 MHz
4	0	0	0	0	60	0	0	1	1	1	1	1	1 MHz
5	0	1	0	1	—	0	0	0	0	1	1	1	0 MHz
6	0	1	1	0	60	0	0	1	1	1	1	1	1 MHz
7	0	1	1	1	30	0	1	1	0	1	1	1	2 MHz
8	1	0	0	0	20	0	1	1	0	1	1	0	3 MHz
9	1	0	0	1	15	1	0	1	0	0	1	1	4 MHz


A10 HIGH FREQUENCY SUBASSEMBLY
A10A1 337 MHz OSCILLATOR/ISOLATOR BOARD
113 MHz OSCILLATOR
X3 MULT
ISOLATOR/AMPLIFIERS
E1
E4
Y1
Q1 299239 1
Q2 299239 1
Q3 299239 1
Q4 299239 1
Q5 299239 1
Q6 299239 1
Q7 299239 1
Q8 299239 1
L17*
L18*
L19*
C36 10 pF
C38 5 pF
C39 5 pF
E2
337 MHz +7 dBm TO A7J1
*L17, L18, L19 AND L24 TO L29 ARE INTEGRAL PARTS OF THE PRINTED CIRCUIT BOARD
E3
A10A2 337 MHz ISOLATOR BOARD
E1
0 dBm
E2
R1 47
C1 0.001
R2 47
R3 4.7K
R4 4.7K
R5 1K
C3 0.001
L9*
C5 2 8 pF
C4 5 pF
E4
E5
FL4
12 VDC FROM A14J7 7
(+7 dBm)
M1
R
L
I
24-33 MHz (6 dBm) FROM A1J1
J2
E3
E6
J3
361 371 MHz (12 dBm) TO A8J1
*L6 TO L12 ARE INTEGRAL PARTS OF THE CIRCUIT BOARD

NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-93. 337 MHz Oscillator and Isolator Boards A10A1 and A10A2, Schematic Diagram

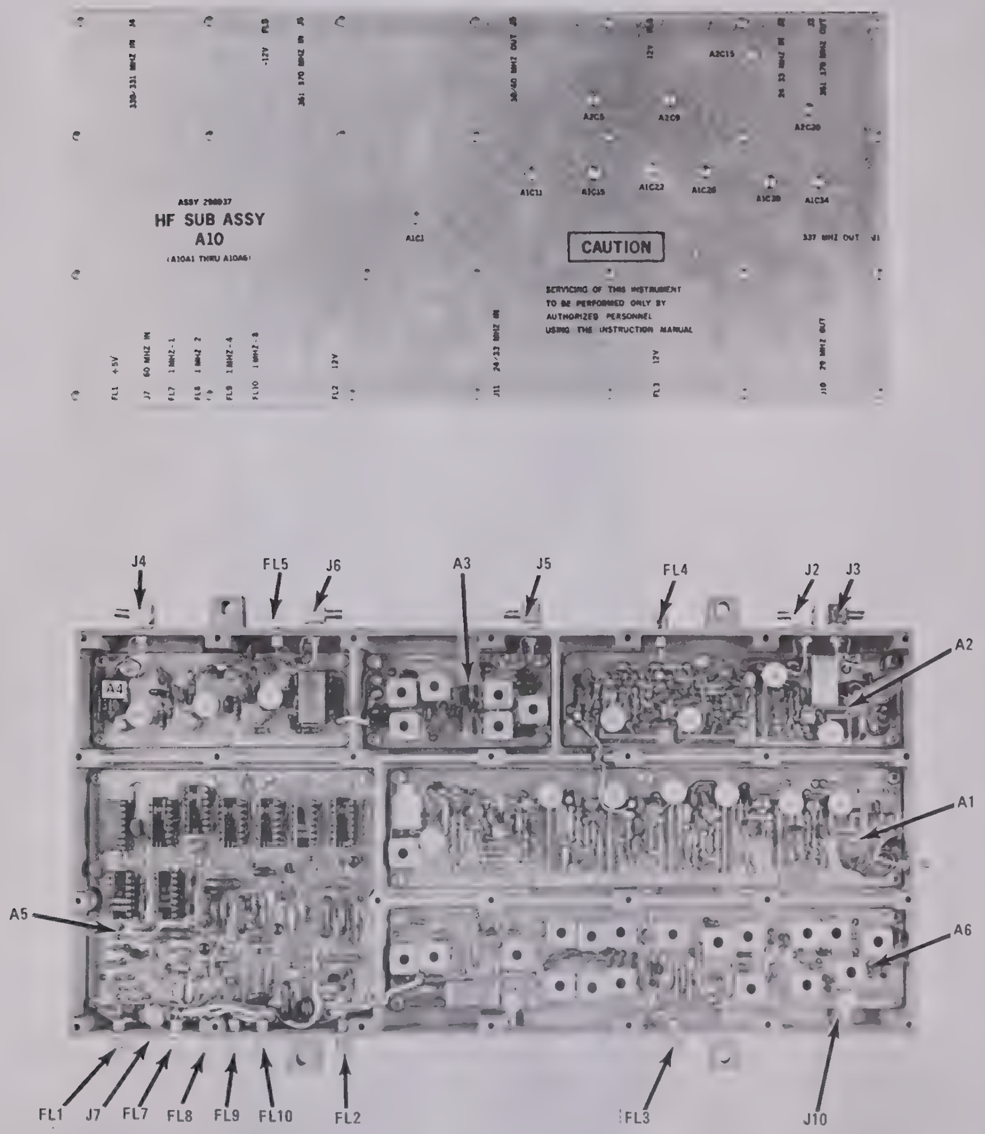

ASSY 290037
HF SUB ASSY
A10
(A10A1 THRU A10A6)
CAUTION
SERVICING OF THIS INSTRUMENT
TO BE PERFORMED ONLY BY
AUTHORIZED PERSONNEL
USING THE INSTRUCTION MANUAL
130-331 MHZ IN J4
-12V FL5
361 370 MHZ IN J6
30/40 MHZ OUT J5
12V
A2C15
A2C5
A2C9
A2C20
A1C11
A1C15
A1C22
A1C26
A1C30
A1C34
A1C1
337 MHZ OUT J1
FL1 +5V
J7 60 MHZ IN
FL7 1 MHZ-1
FL8 1 MHZ 2
FL9 1 MHZ-4
FL10 1 MHZ-8
FL2 12V
J11 24/33 MHZ IN
FL3 12V
J10 29 MHZ OUT
J4
FL5
J6
A3
J5
FL4
J2
J3
A2
A4
A1
A5
A6
FL1
J7
FL7
FL8
FL9
FL10
FL2
FL3
J10

Figure 6-94. High Frequency Subassembly A10

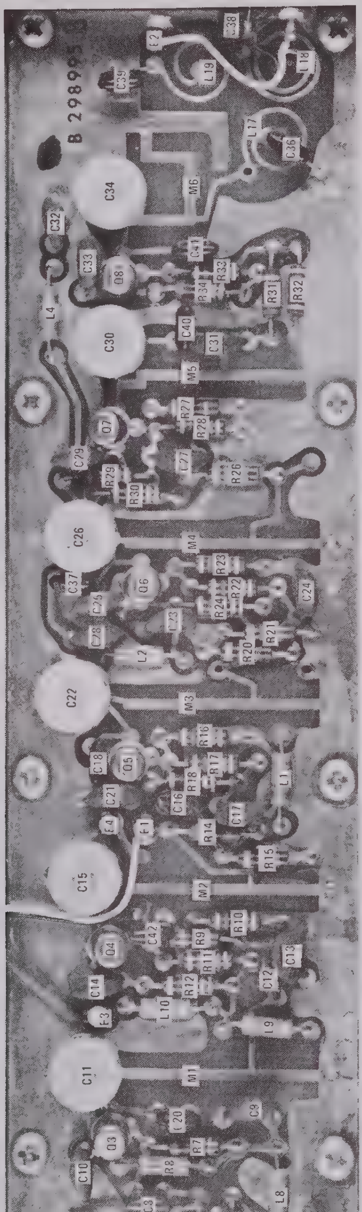

B 298995
C38
E2
L19
L18
C39
L17
C36
M6
C34
C32
C33
Q8
C41
R33
R34
R31
R32
L4
C30
C40
C31
M5
Q7
R27
R28
C29
C27
R29
R26
R30
C26
M4
C37
Q6
R23
C25
R24
R22
C24
L3
C28
R21
R20
L2
C22
M3
R16
C18
Q5
R17
R18
L1
C21
C16
C17
E4
E1
R14
R15
C15
M2
R10
C42
Q4
R9
R11
C13
C14
R12
C12
E3
L10
L9
C11
M1
C20
C9
Q3
R7
C10
R8
L8
L7
C8
Q2
R5
C7
R6
C5
R4
C6
R1
Q1
L6
C3
R2
C4
R3
L23
C2
L19
C1
Y1
L3

Figure 6-95. 337 MHz Oscillator/Isolator Board A10A1, Component Locations

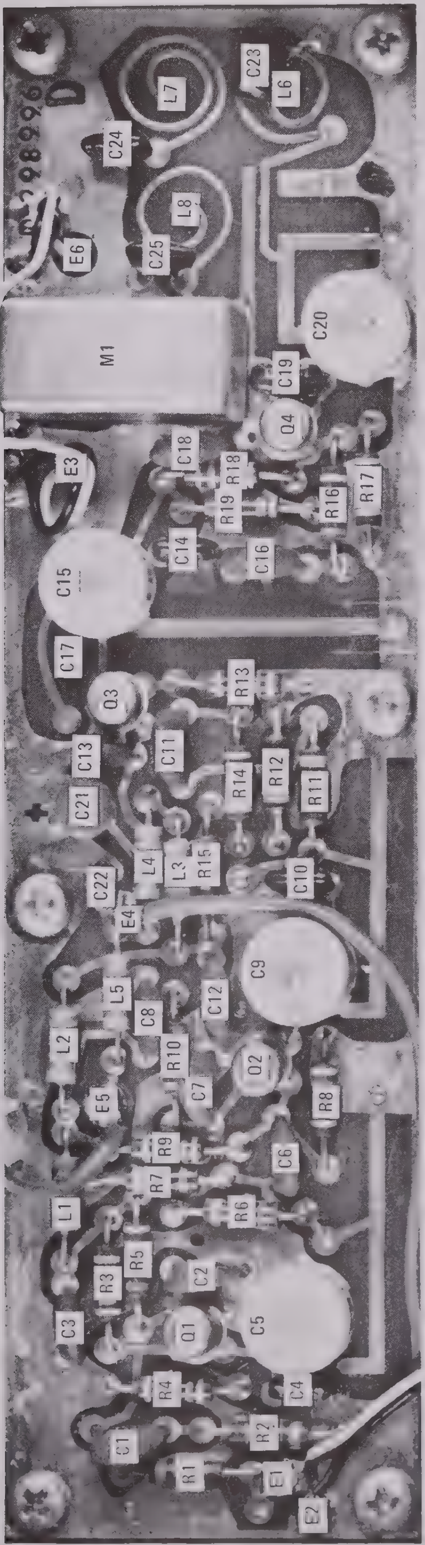

C23
L6
L7
C24
L8
C25
E6
M1
C20
C19
Q4
C18
R18
E3
R19
R16
R17
C14
C16
C15
C17
R13
Q3
C11
C13
R14
R12
R11
C21
L4
L3
R15
C10
C22
E4
C9
L5
C12
C8
L2
R10
Q2
C7
E5
R8
R9
C6
R7
L1
R6
R5
C2
R3
C3
Q1
C5
C4
R4
C1
R2
R1
E1
E2

Figure 6-96. 337 MHz Isolator Board A10A2, Component Locations

6-240. The programmable divider output appears at U1-11. This output is divided by an additional 2 or 4 by U3A, or U3A and U3B, respectively, as required for 1-, 2-, and 3-MHz outputs. The 60 MHz from the fixed frequency generator A5 is applied to driver Q1 which buffers the 60 MHz and drives U1. U1 in combination with U2 and U4 form the programmable divider.

6-241. U1 is a programmable binary counter which is set up to divide the 60-MHz input frequency by 10, 12, or 15 as determined by the BCD digit input control logic. U2 and U4 are used to detect the positive edge of the Q_D, U1 pin 11 of the counter and two counts later outputs a pulse to the parallel entry (PE) input of the counter which is synchronously programmed with the starting count. As indicated in the truth table, the counter output is the first stage of the divide sequence. Some input digits require an additional divide by 2 or 4 which is provided by U3. Each of these flip-flops is controlled by outputs from the control logic.

6-242. The output of the programmable divider appears at U3 pin 6 for 1 MHz, U3 pin 8 for 2 and 3 MHz, and U1 pin 11 for 4 and 5 MHz, depending on which frequency is programmed. The outputs are squarewaves and need filtering before the final output is useable. Transistors Q5, Q6, and Q7 form a single-pole, three-throw FET switch which selects the appropriate lowpass filter output. This output passes through a 3-dB attenuator formed by R22 through R24 and a final lowpass filter consisting of L14 and L15 and C32 through C34. Transistor Q8 is turned on only when the output is required to be a dc level. The output of the comb generator feeds the 29-MHz converter at a level of -8 dBm.

6-243. **29 MHz FILTER CONVERTER A10A6.** (Figures 6-100 and 6-102). This board mixes the dc to 5-MHz output of A10A5 with the 24 to 33 MHz output of A6 producing 29 MHz. The 29 MHz is amplified and filtered to eliminate all signals 1 MHz and more away from 29 MHz.

6-244. The input from A10A5 at E1 passes through the lowpass filter formed by L30, L31, and C44. This reduces high frequency harmonics which may be present on the 1 to 5 MHz signal. This signal mixes with 24 to 33 MHz from A6 in M1. The 29-MHz sideband is selected by the two-pole bandpass filter formed by C1 through C4 and L1 through L3. The 29 MHz is amplified by Q1 which drives an eight pole bandpass filter consisting of C8, C10-C22, and L6-L15. The output of this filter is amplified by Q2 which drives another eight pole bandpass filter. This second eight pole filter is formed by C26, C28-C34, C36-C41, L17-L24, L26, and L27. The output of this board is at a level of -7 dBm and goes to the plug-in interface connector to drive the optional modulator. The output of the modulator, also 29 MHz, goes to the 366 MHz filter amplifier A7.

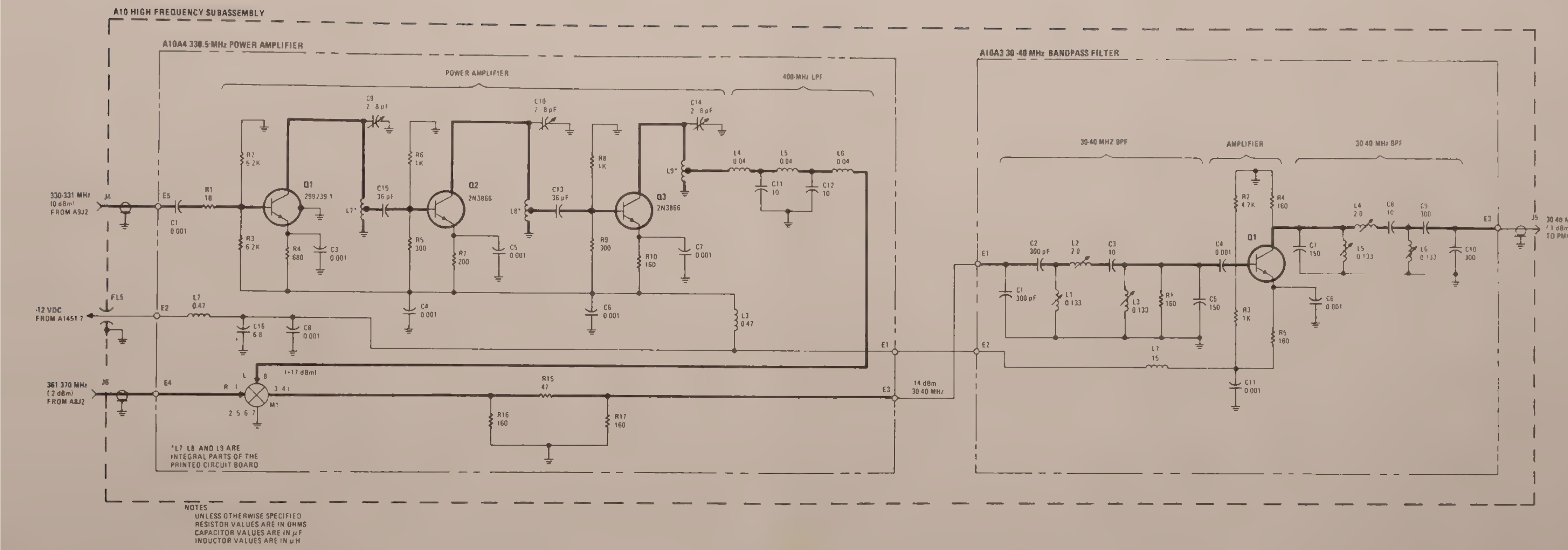

A10 HIGH FREQUENCY SUBASSEMBLY
A10A4 330.5-MHz POWER AMPLIFIER
POWER AMPLIFIER
400-MHz LPF
330-331 MHz
(0 dBm)
FROM A9J2
J4
E5
C1
0 001
R1
18
R2
6 2K
R3
6 2K
Q1
299239 1
R4
680
C3
0 001
C9
2 8 pF
L7*
C15
36 pF
R6
1K
R5
300
Q2
2N3866
R7
200
C5
0 001
C4
0 001
C10
2 8 pF
L8*
C13
36 pF
R8
1K
R9
300
Q3
2N3866
R10
160
C7
0 001
C6
0 001
C14
2 8 pF
L9*
L4
0 04
L5
0.04
L6
0 04
C11
10
C12
10
L3
0 47
-12 VDC
FROM A14S1 7
FL5
E2
L7
0.47
C16
6 8
C8
0 001
E1
(+17 dBm)
361 370 MHz
(2 dBm)
FROM A8J2
J6
E4
L
8
R 1
3 4 1
2 5 6 7
M1
R15
47
R16
160
R17
160
E3
14 dBm
30 40 MHz
*L7 L8 AND L9 ARE
INTEGRAL PARTS OF THE
PRINTED CIRCUIT BOARD
A10A3 30-40 MHz BANDPASS FILTER
30-40 MHZ BPF
AMPLIFIER
30 40 MHz BPF
E1
C2
300 pF
L2
2 0
C3
10
C1
300 pF
L1
0 133
L3
0 133
R1
160
C5
150
C4
0 001
R2
4 7K
R4
160
Q1
R3
1K
R5
160
C6
0 001
C11
0 001
E2
L7
15
L4
2 0
C8
10
C9
300
C7
150
L5
0 133
L6
0 133
C10
300
E3
J5
30 40 MHz
(1 dBm)
TO PM1 J2
NOTES
UNLESS OTHERWISE SPECIFIED
RESISTOR VALUES ARE IN OHMS
CAPACITOR VALUES ARE IN μF
INDUCTOR VALUES ARE IN μH

Figure 6-97. 30-40 MHz Bandpass Filter A10A3 and 330.5 MHz Power Amplifier A10A4, Schematic Diagram

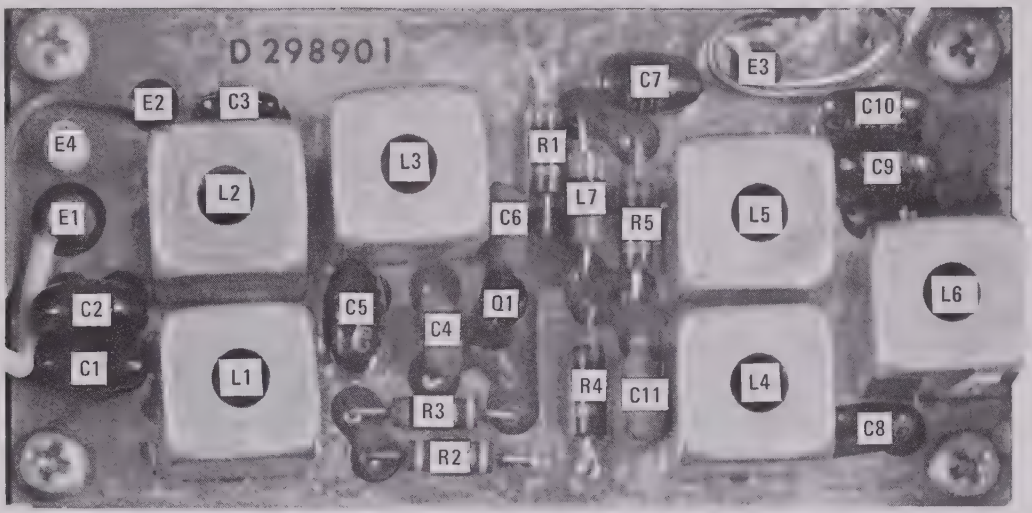

D 298901
E2
C3
E4
L2
L3
R1
C7
E3
C10
C9
L7
R5
L5
E1
C6
L6
C2
C5
Q1
C4
C1
L1
R4
C11
L4
R3
C8
R2

Figure 6-98. 30-40 MHz Bandpass Filter Board A10A3, Component Locations

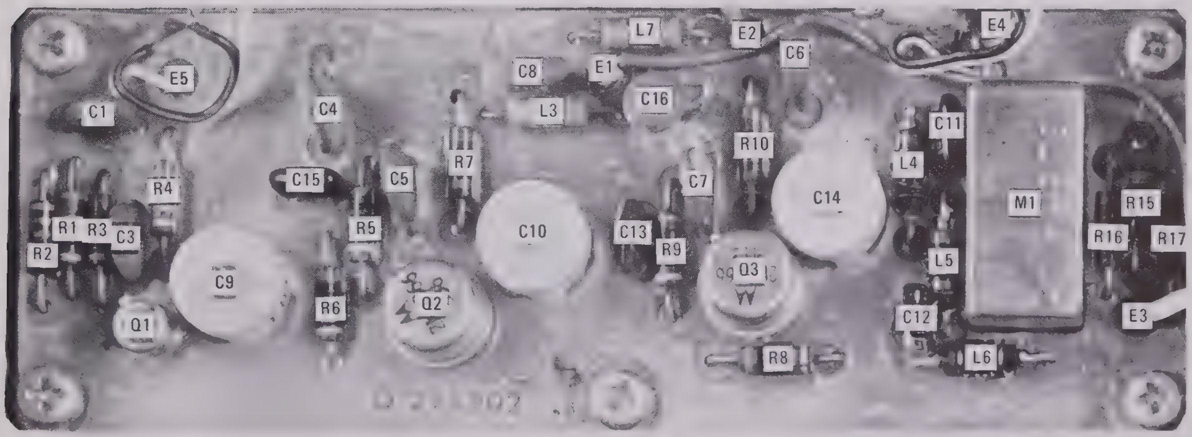
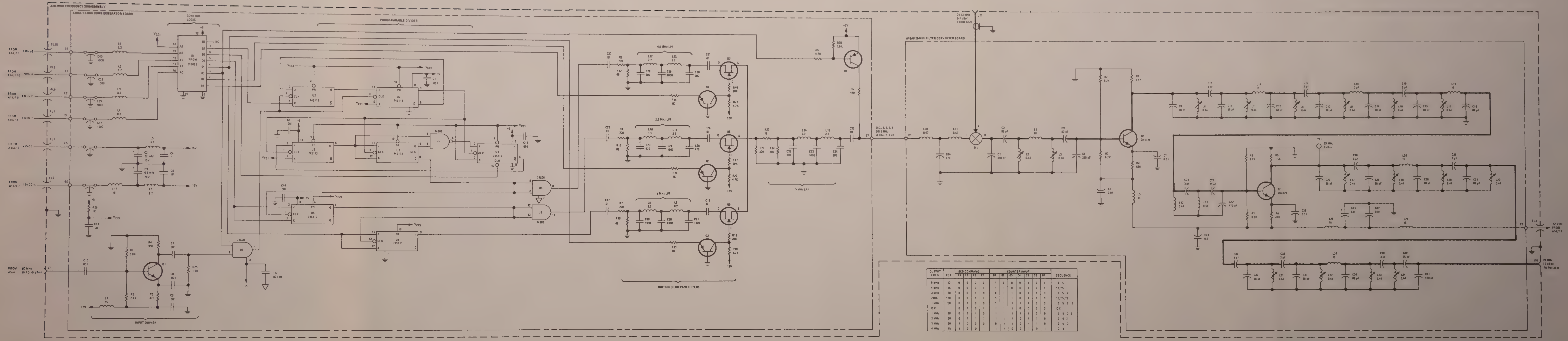

L7
E2
E4
C6
E5
C8
E1
C1
C4
L3
C16
R10
C11
R7
L4
R4
C15
C5
C7
C14
M1
R15
R1
R3
C3
R5
C10
C13
R16
R17
R2
R9
Q3
L5
C9
R6
Q2
C12
E3
Q1
R8
L6

Figure 6-99. 330.5 MHz Power Amplifier Board A10A4, Component Locations


A10 HIGH FREQUENCY SUBASSEMBLY
A10A5 1-5 MHz COMB GENERATOR BOARD
CONTROL LOGIC
PROGRAMMABLE DIVIDER
U1 PROM 293622
FROM A14J7 1 1 MHz 8
FROM A14J7 10 MHz 4
FROM A14J7 9 1 MHz 2
FROM A14J7 8 1 MHz 1
FROM A14J7 6 +5VDC
FROM A14J7 7 12VDC
FROM A3J4 20 MHz (0 TO +5 dBm)
INPUT DRIVER
4.5 MHz LPF
2.2 MHz LPF
1 MHz LPF
5 MHz LPF
SWITCHED LOW PASS FILTERS
D.C., 1, 2, 3, 4 OR 5 MHz
24-33 MHz (+7 dBm) FROM A6J2
A10A6 29-MHz FILTER CONVERTER BOARD
29 MHz 0 dBm
12 VDC FROM A14J7 3
29 MHz (-7 dBm) TO PB8-J3

OUTPUT FREQ	FCT	BCD COMMAND				COUNTER INPUT							SEQUENCE
		E4	E3	E2	E1	D7	D6	D5	D4	D3	D2	D1	
5 MHz	12	0	0	0	0	1	0	0	0	1	0	1	3 4
4 MHz	15	0	0	0	1	1	1	0	0	1	0	1	−3, −5
3 MHz	20	0	0	1	0	0	1	1	0	1	1	0	2 5 2
2MHz	−30	0	0	1	1	1	1	1	0	1	1	0	−3, −5, −2
1 MHz	−60	0	1	0	0	1	1	1	1	1	0	0	3 5 2 2
D C		0	1	0	1	1	1	1	0	0	0	0	D C
1 MHz	60	0	1	1	0	1	1	1	1	1	0	0	3 5 2 2
2 MHz	30	0	1	1	1	1	1	1	0	1	1	0	3 −5 −2
3 MHz	20	1	0	0	0	0	1	1	0	1	1	0	2 5 2
4 MHz	15	1	0	0	1	1	1	0	0	1	0	1	3. 4

Figure 6-100. 1-5 MHz Comb Generator A10A5 and 29 MHz Filter Converter A10A6, Schematic Diagram

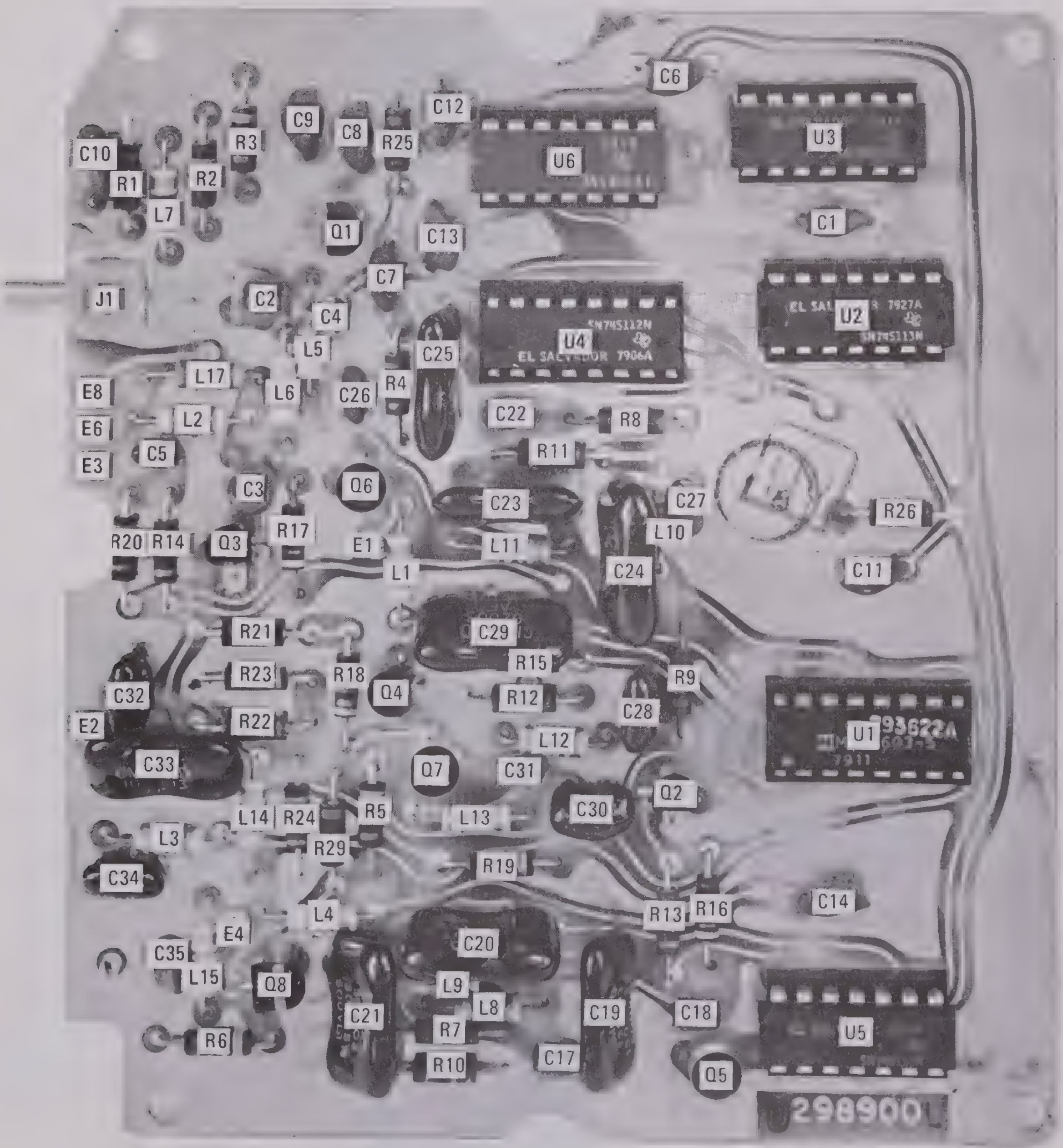

C6
C12
C9
C8
R25
U6
U3
C10
R3
R1
R2
L7
Q1
C13
C1
C7
J1
C2
C4
SN74S112N
U4
EL SALVADOR 7906A
U2
7927A
SN74S113N
L5
C25
L17
E8
L6
C26
R4
C22
R8
E6
L2
R11
C5
E3
Q6
C3
C23
C27
R26
R17
R20
R14
Q3
E1
L11
L10
L1
C24
C11
R21
C29
R15
R9
R23
R18
Q4
R12
C32
C28
U1
293622A
7911
E2
R22
L12
C33
Q7
C31
Q2
L14
R24
R5
L13
C30
L3
R29
R19
C34
R13
R16
C14
L4
E4
C20
C35
L15
Q8
L9
L8
C21
C19
C18
R7
U5
R6
R10
C17
Q5
298900

Figure 6-101. 1-5 MHz Comb Generator A10A5, Component Locations

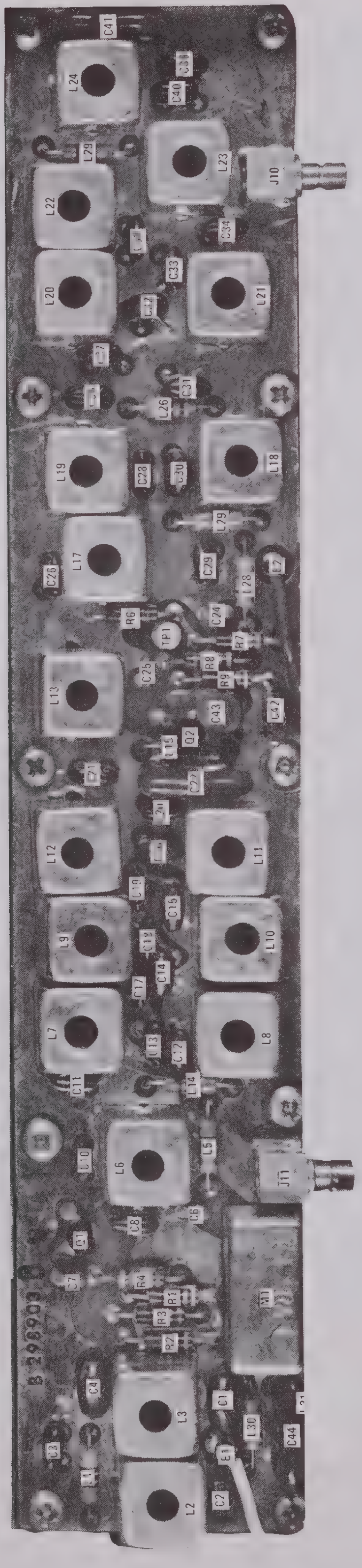

C41
C38
L24
C40
L23
J10
L29
L22
C34
C33
L20
L21
C31
L26
L19
C28
C30
L18
L29
L17
C26
C29
L28
R6
C24
TP1
R7
R8
C25
R9
L13
C43
Q2
L15
C27
L12
L11
C19
C15
L9
C18
L10
C14
C17
L7
L13
C12
L8
C11
L14
L10
L6
L5
J11
C8
C6
Q1
C7
R4
R1
M1
R3
R2
B 296903
C4
C1
L3
L30
C44
C3
E1
L1
C2
L2

Figure 6-102. 29 MHz Filter Converter A10A6, Component Locations

CHAPTER 7

REPLACEABLE PARTS LIST

7-1. INTRODUCTION

7-2. This chapter contains the replaceable parts list for the AILTECH 380K11 Mainframe and 3600E1 Frequency Extender (Figure 7-1). This parts list is presented in reference designation order. The parts list contains the reference designation, AILTECH part number, quantity, description, manufacturers code, and manufacturers part number. Table 7-1 lists manufacturers by manufacturers code.

7-3. The mainframe parts (A1, A2, A4, A5, A11, A12, A14 and A15) are listed on pages 7-4 through 7-39. The frequency extender parts (A6-A10) are listed on pages 7-39 through 7-52.

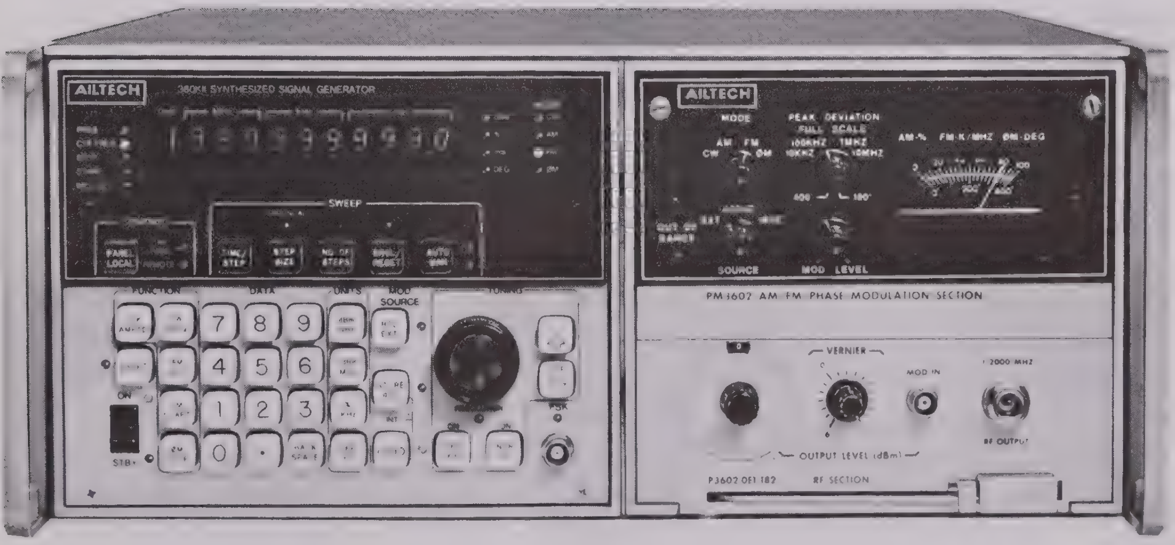

AILTECH
380K11 SYNTHESIZED SIGNAL GENERATOR
SWEEP
FUNCTION
DATA
UNITS
MOD SOURCE
TUNING
7
8
9
4
5
6
1
2
3
0
ON
STBY
AILTECH
MODE
PEAK DEVIATION FULL SCALE
AM FM
CW ØM
100KHZ 1MHZ
10KHZ 10MHZ
AM-% FM-K/MHZ ØM-DEG
SOURCE
MOD LEVEL
PM 3602 AM FM PHASE MODULATION SECTION
VERNIER
MOD IN
2000 MHZ
RF OUTPUT
OUTPUT LEVEL (dBm)
P3602 OE1 182
RF SECTION

Figure 7-1. 380K11 Mainframe and 3600E1 Frequency Extender

TABLE 7-1. FEDERAL SUPPLY CODE MANUFACTURER (FSCM)

Vendor Name	Vendor Code	Vendor Address
Amp	00779	Harrisburg, Pa., 29671
Allen Bradley	01121	Milwaukee, Wisc., 53204
Texas Instruments	01295	Dallas, Texas, 75231
Amphenol	02660	Broadville, Ill., 60152
Motorola	04713	Syracuse, N.Y., 13201
Kemet	05397	Greenville, S.C., 29606
Viking	05574	Chattsworth, Calif., 91311
Prototype Transformer	05853	E. Orange, N.J., 07017
Milross	07047	Southampton, Penn., 18966
Vemaline	08730	Wykoff, N.J.
Thermalloy	13103	Dallas, Texas, 75247
Sensitron	13409	Deer Park, N.Y., 11729
Cornell Dubilier	14655	Newark, N.J., 07105
Mini Ckts. Lab.	15542	Brooklyn, N.Y., 11203
Cablewave Systems	16733	N. Haven, Ct., 06473
Signetics	18324	Sunnyvale, Calif., 94086
Avantek	24539	Santa Clara, Calif., 95051
Hewlett-Packard	28480	Rockaway, N.J., 07886
IMC Magnetics	28875	Rochester, N.H., 03867
Transtector Systems	30992	Monterey Park, Calif., 91754
Stettner Trush	52763	
Sprague	56289	551 Marshall St. North Adams, Ma.
CTC	71279	Milwaukee, Wisc., 53201
Elmenco	72136	Willimantic, Ct., 06226
Fairchild	72314	Mtn. View, Calif., 94040
Erie	72982	Erie, Pa., 16512
Littlefuse	75915	Des Plaines, Ill., 60016
Bourns	80294	Riverside, Calif., 92507
Barry	81860	Broadway, N.Y., 10012
Babcock	82050	161 E. 42nd St., N.Y. New York 10017
Johanson	91293	Boonton, N.J., 07005

TABLE 7-1. FEDERAL SUPPLY CODE MANUFACTURER (FSCM) (Cont)

Vendor Name	Vendor Code	Vendor Address
Augat	91506	Attleboro, Ma., 02703
Dale	91637	Columbus, Ne., 69153
ELCO	91662	Willow Grove, Pa., 19090
Republic Electronics	93985	Paterson, N.J.
Weston	94271	Newark, N.J., 07114
ALCO	95146	N. Andover, Ma., 01845
Vitramon	95275	Bridgeport, Ct., 06601
Microwave Assoc.	96341	Burlington, Ma., 01803
Selectro	98291	Mamaronek, N.J., 10544
Teledyne	98571	Hawthorne, Calif., 90250
Delevan	99800	E. Aurora, N.Y., 14052

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
1	296414-1	1	380 K11 MAIN FRAME	56872	296414-1
BR1	294772-1	2	RECTIFIER BRIDGE 25A	13409	S25A10
BR2	294772-1	2	RECTIFIER BRIDGE 25A	13409	S25A10
C1	29924S-1	2	CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C2	29924S-1				
	296428-1	1	ASSY POWER SUPPLY		
	296425-1	1	RECTIFIER HEATSINK	56872	296425-1
W1	299297-1	1	CABLE ASSY COAX	56872	299297-1
W2	299297-2	1	CABLE ASSY COAX	56872	299297-2
W3	299297-3	1	CABLE ASSY COAX	56872	299297-3
W4	299297-4	1	CABLE ASSY COAX	56872	299297-4
W5	299297-5	1	CABLE ASSY COAX	56872	299297-5
W6	299297-6	1	CABLE ASSY COAX	56872	299297-6
W7	299297-7	1	CABLE ASSY COAX	56872	299297-7
W8	299297-8	1	CABLE ASSY COAX	56872	299297-8
W9	299297-9	1	CABLE ASSY COAX	56872	299297-9
W10	299297-10	1	CABLE ASSY COAX	56872	299297-10
W12	299297-12	1	CABLE ASSY COAX	56872	299297-12
W14	299297-14	1	CABLE ASSY COAX	56872	299297-14
W18	299297-18	1	CABLE ASSY COAX	56872	299297-18
W23	299297-23	1	CABLE ASSY COAX	56872	299297-23
W24	299297-24	1	CABLE ASSY COAX	56872	299297-24
	296409-4	1	CABLE ASSY	56872	296409-4
	296409-1	1	CABLE ASSY	56872	296409-1
	296401-1	1	CABLE ASSY	56872	296401-1
	296403-1	1	CABLE ASSY	56872	294603-1
	296402-1	1	CABLE ASSY	56872	296402-1
	294948-1	1	LINE CORD	05245	80-1245
A1	296389-1	1	ASSY, FRONT PANEL	56872	296389-1
C1	299245-1	1	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
J1	294439	1	CONNECTOR, BNC	02660	74868DG-1094/U
S1	294531	1	SWITCH, 4 PDT		CYK7401-J1-ZQE
W1	296409-3	1	ASSY, CABLE	56872	294609-3
A1A1	296306-1	1	ASSY, KEYBOARD	56872	296306-1
C1	299240-2	1	CAP:22UF 15V	05397	T368B226M015AS
C2	299245-1	16	CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C3	299240-5	2	CAP:68UF 15V	05397	T362C686M015AS
C4	299240-5		CAP:68UF 15V	05397	T362C686M015AS
C5	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C6	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C7	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C8	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C9	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C10	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C11	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C12	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C13	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C14	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C15	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C16	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C17	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C18	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
C19	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8120-050-651-104M
CR1	296248-3	27	DIODE, LIGHT EMITTING	28480	HLMP-1301
CR2	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR3	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR4	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR5	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR6	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR7	299248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR8	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A1A1 CR9	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR10	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR11	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR12	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR13	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR14	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR15	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR16	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR17	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR18	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR19	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR20	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR21	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR22	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR23	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR24	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR25	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR26	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
CR27	296248-3		DIODE, LIGHT EMITTING	28480	HLMP-1301
J1	299934-6	1	HEADER:STRAIGHT 6 PIN	00779	87227-3
J5	299932-2	2	HEADER:STRAIGHT 28 PIN	00779	1-87227-4
J6	299932-2		HEADER:STRAIGHT 28 PIN	00779	1-87227-4
S1	293645-2	6	SWITCH:KEY		DC61-05
S2	293645-2		SWITCH:KEY		DC61-05
S3	293645-2		SWITCH:KEY		DC61-05
S4	293645-2		SWITCH:KEY		DC61-05
S5	293645-2		SWITCH:KEY		DC61-05
S6	293645-2		SWITCH:KEY		DC61-05
S7	293645-1	29	SWITCH:KEY		DC61-03
S8	293645-1		SWITCH:KEY		DC61-03
S9	293645-1		SWITCH:KEY		DC61-03
S10	293645-1		SWITCH:KEY		DC61-03
S11	293645-1		SWITCH:KEY		DC61-03
S12	293645-1		SWITCH:KEY		DC61-03
S13	293645-1		SWITCH:KEY		DC61-03
S14	293645-1		SWITCH:KEY		DC61-03
S15	293645-1		SWITCH:KEY		DC61-03
S16	293645-1		SWITCH:KEY		DC61-03
S17	293645-1		SWITCH:KEY		DC61-03
S18	293645-1		SWITCH:KEY		DC61-03
S19	293645-1		SWITCH:KEY		DC61-03
S20	293645-1		SWITCH:KEY		DC61-03
S21	293645-1		SWITCH:KEY		DC61-03
S22	293645-1		SWITCH:KEY		DC61-03
S23	293645-1		SWITCH:KEY		DC61-03
S24	293645-1		SWITCH:KEY		DC61-03
S25	293645-1		SWITCH:KEY		DC61-03
S26	293645-1		SWITCH:KEY		DC61-03
S27	293645-1		SWITCH:KEY		DC61-03
S28	293645-1		SWITCH:KEY		DC61-03
S29	293645-1		SWITCH:KEY		DC61-03
S30	293645-1		SWITCH:KEY		DC61-03
S31	293645-1		SWITCH:KEY		DC61-03
S32	293645-1		SWITCH:KEY		DC61-03
S33	293645-1		SWITCH:KEY		DC61-03
S34	293645-1		SWITCH:KEY		DC61-03
S35	293645-1		SWITCH:KEY		DC61-03
U1	293212-3	11	INDICATOR:NUMERIC	28480	5082-7302
U2	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U3	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U4	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U5	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U6	293212-3		INDICATOR:NUMERIC	28480	5082-7302

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A1A1 U7	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U8	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U9	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U10	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U11	293212-3		INDICATOR:NUMERIC	28480	5082-7302
U12	293541-19	4	IC:OCTAL LATCH	01295	SN74LS3741J
U13	293691-8	2	IC:RESISTOR NETWORK	01121	316A331
U14	293691-8		IC:RESISTOR NETWORK	01121	316A331
U15	293541-19		IC:OCTAL LATCH	01295	SN74LS2761J
U16	293541-19		IC:OCTAL LATCH	01295	SN74LS3741J
U17	293541-19		IC:OCTAL LATCH	01295	SN74LS3741J
XU1	296449	11	SOCKET:DISPLAY	91506	508-AG4F
XU2	296449		SOCKET:DISPLAY	91506	508-AG4F
XU3	296449		SOCKET:DISPLAY	91506	508-AG4F
XU4	296449		SOCKET:DISPLAY	91506	508-AG4F
XU5	296449		SOCKET:DISPLAY	91506	508-AG4F
XU6	296449		SOCKET:DISPLAY	91506	508-AG4F
XU7	296449		SOCKET:DISPLAY	91506	508-AG4F
XU8	296449		SOCKET:DISPLAY	91506	508-AG4F
XU9	296449		SOCKET:DISPLAY	91506	508-AG4F
XU10	296449		SOCKET:DISPLAY	91506	508-AG4F
XU11	296449		SOCKET:DISPLAY	91506	508-AG4F
A1A2	296308-1	1	ASSY, ENCODER PWB	56872	296308-1
C1	299240-5	1	CAP:FXD CER 68UF 15V	05397	T362C686M015AS
C2	299245-001	15	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C3	299245-007	4	CAP:FXD CER 1.0UF	72982	8131-M058-651-105M
C4	299245-007		CAP:FXD CER 1.0UF	72982	8131-M058-651-105M
C5	299245-007		CAP:FXD CER 1.0UF	72982	8131-M058-651-105M
C6	299245-007		CAP:FXD CER 1.0UF	72982	8131-M058-651-105M
C7	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C8	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C9	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C10	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C11	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C12	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C13	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C14	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C15	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C16	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C17	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C18	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C19	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C20	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
CR1	293240	2	DIODE	72314	IN914
CR2	293240		DIODE	72314	IN914
E1	293306	3	TERMINAL	71279	1019-2-901
E2	293306		TERMINAL	71279	1019-2-901
E3	293306		TERMINAL	71279	1019-2-901
J2	299932	1	HEADER:26 PIN	00779	1-87227-3
J3	294729	1	HEADER:STRAIGHT 8 PIN	00779	87227-4
J4	294730	1	HEADER:RIGHT ANGLE 8 PIN	00779	87230-4
P5	294791	2	CONNECTOR:28 PIN	00779	86418-3
P6	294791		CONNECTOR:28 PIN	00779	86418-3
Q1	295191	3	TRANSISTOR	04713	2N2222A
Q2	295191		TRANSISTOR	04713	2N2222A
Q3	295191		TRANSISTOR	04713	2N2222A
R1	299701-102	3	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R2	299700-103	3	RESISTOR:FXD CAR 10K –+5% 1/8W	01121	RC05GF103J
R3	299700-103		RESISTOR:FXD CAR 10K –+5% 1/8W	01121	RC05GF103J
R4	299700-103		RESISTOR:FXD CAR 10K –+5% 1/8W	01121	RC05GF103J
R5	299701-103	4	RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R6	299701-512	2	RESISTOR:FXD CAR 5.1K –+5% 1/4W	01121	RC07GF512J
R7	299701-103		RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A1A2 R8	299701-103		RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R9	299701-512		RESISTOR:FXD CAR 5.1K -+5% 1/4W	01121	RC07GF512J
R10	299701-103		RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R11	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R12	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
U1	293541-24	2	IC:3 TO 8 DECODER	01295	SN74LS138N
U2	293536	3	IC:DUAL D FLIP FLOP	01295	SN74LS74AN
U3	294038	2	IC:20 KEY ENCODER	01295	MM74C923N
U4	293541-24		IC:3 TO 8 DECODER	01295	SN74LS138N
U5	294038		IC:20 KEY ENCODER	01295	MM74C923N
U6	293540	1	IC:HEX INVERTER	01295	SN74LS04N
U7	293541-001	2	IC:QUAD NO. 2, IN NAND	01295	SN74LS00N
U8	293541-001		IC:QUAD NO. 2, IN NAND	01295	SN74LS00N
U9	294037	1	IC:OCTAL LATCH	01295	SN74S373N
U10	294036	1	IC:HEX 3 STATE BUFFER	01295	SN74LS365N
U11	294494	1	IC:HEX SCHMITT INVERTER	01295	SN74LS14N
U12	294045	1	IC:3 IN AND GATE	01295	SN74LS11N
U13	293536		IC:DUAL D FLIP FLOP	01295	SN74LS74AN
U14	293536		IC:DUAL D FLIP FLOP	01295	SN74LS74AN
A1A3	296424-1	1	ENCODER, OPTICAL	56872	296424-1
A2	298933-2		ASSY, LOWER ORDER BCD	56872	298933-2
A2A1	299204		PUB ASSY, FIXED TUNED BCD (1KHZ)	00752	299204
C1	299243-001	19	CAP:FXD CER DISC .001 UF -+20% 1000WVDC	56289	CK60AW102M
C2	299244-001	26	CAP:FXD CER DISC .01UF -+20+80% 1000WVDC	56289	5HKS-S10
C3	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C4	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C5	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C6	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C7	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C8	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C9	299241-200	6	CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C10	299241-101	1	CAP:FXD DIP MICA 100PF -+5% 300WVDC	72136	DM5-101J
C11	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C12	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C13	299241-430	4	CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C14	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C15	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C16	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C17	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C18	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C19	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C20	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C21	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C22	299241-100	6	CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C23	299241-680	3	CAP:FXD DIP MICA 68PF -+0.5PF 300WVDC	72136	DM5-680J
C24	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C25	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C26	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C27	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C28	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C29	299250-131	3	CAP:FXD DIP MICA 130PF -+5% 500WVDC	14655	CM04F131J03
C30	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C31	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C32	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C33	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C34	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A1 C35	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C36	299244-001		CAP:FXD CER DISC 0.1UF –20+80% 1000WVDC	56289	5HKS-S10
C37	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C38	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C39	299241-120	2	CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-120J
C40	299241-750	1	CAP:FXD DIP MICA 75PF –+5% 300WVDC	72136	DM5-750J
C41	299241-120		CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-120J
C42	299245-001	2	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C43	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C44	299250-241	3	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C45	299250-151	1	CAP:FXD DIP MICA 150PF –+5% 500WVDC	14655	CM04FD151J03
C46	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C47	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C48	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C49	299244-002		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C50	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C51	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C52	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C53	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C54	299241-100		CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C55	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM-680J
C56	299241-100		CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C57	299241-100		CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C58	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C59	299241-100		CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C60	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C61	299241-200		CAP:FXD DIP MICA 20PF –+5% 300WVDC	72136	DM5-200J
C62	299241-270	1	CAP:FXD DIP MICA 27PF –+5% 300WVDC	72136	DM5-270J
C63	299241-131		CAP:FXD DIP MICA 130PF –+5% 500WVDC	14655	CM04FD131J03
C64	299241-200		CAP:FXD DIP MICA 20PF –+5% 300WVDC	72136	DM5-200J
C65	299250-131		CAP:FXD DIP MICA 130PF –+5% 500WVDC	14655	CM04FD131J03
C66	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C67	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C68	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C69	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C70	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C71	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C72	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C73	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C74	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C75	299240-002	1	CAP:FXD DIP TA 22UF –+20% 15WVDC	05397	T368B226M015AS
C76	299240-001	1	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C77	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C78	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C79	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C80	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
CR1	299251-001	8	DIODE:SIGNAL	72314	IN4009
CR2	299251-001		DIODE:SIGNAL	72314	IN4009
CR3	299251-001		DIODE:SIGNAL	72314	IN4009
CR4	299251-001		DIODE:SIGNAL	72314	IN4009
CR5	299251-001		DIODE:SIGNAL	72314	IN4009
CR6	299251-001		DIODE:SIGNAL	72314	IN4009
CR7	299251-001		DIODE:SIGNAL	72314	IN4009
CR8	299251-001		DIODE:SIGNAL	72314	IN4009
E1	293310	8	TERMINAL SOLDER	71279	1597-2-01
L1	299237-026	4	INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L2	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A1 L3	299237-038	11	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L4	299237-032	4	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L5	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L6	299237-022	2	INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L7	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L8	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L9	299237-028	2	INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L10	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L11	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L12	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L13	299237-024	6	INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L14	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L15	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L16	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L17	299237-020	2	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L19	299277-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L20	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L21	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L22	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L23	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L24	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L25	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L26	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L27	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L28	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L29	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L30	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L31	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L32	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L33	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L34	299237-032		INDUCTOR:MOLDED 3.3UH	99800	1025-032
Q1	294172	24	TRANSISTOR:NPN SI	04713	MPS918
Q2	294172		TRANSISTOR:NPN SI	04713	MPS918
Q3	294172		TRANSISTOR:NPN SI	04713	MPS918
Q4	294172		TRANSISTOR:NPN SI	04713	MPS918
Q5	294172		TRANSISTOR:NPN SI	04713	MPS918
Q6	294153	8	TRANSISTOR:PNP SI	04713	2N4126
Q7	294153		TRANSISTOR:PNP SI	04713	2N4126
Q8	294172		TRANSISTOR:NPN SI	04713	MPS918
Q9	294172		TRANSISTOR:NPN SI	04713	MPS918
Q10	294172		TRANSISTOR:NPN SI	04713	MPS918
Q11	294172		TRANSISTOR:NPN SI	04713	MPS918
Q12	294172		TRANSISTOR:NPN SI	04713	MPS918
Q13	294172		TRANSISTOR:NPN SI	04713	MPS918
Q14	294153		TRANSISTOR:PNP SI	04713	2N4126
Q15	294153		TRANSISTOR:PNP SI	04713	2N4126
Q16	294172		TRANSISTOR:NPN SI	04713	MPS918
Q17	294172		TRANSISTOR:NPN SI	04713	MPS918
Q18	294172		TRANSISTOR:NPN SI	04713	MPS918
Q19	294172		TRANSISTOR:NPN SI	04713	MPS918
Q20	294172		TRANSISTOR:NPN SI	04713	MPS918
Q21	294172		TRANSISTOR:NPN SI	04713	MPS918
Q22	294153		TRANSISTOR:PNP SI	04713	2N4126
Q23	294153		TRANSISTOR:PNP SI	04713	2N4126
Q24	294172		TRANSISTOR:NPN SI	04713	MPS918
Q25	294172		TRANSISTOR:NPN SI	04713	MPS918
Q26	294172		TRANSISTOR:NPN SI	04713	MPS918
Q27	294172		TRANSISTOR:NPN SI	04713	MPS918
Q28	294172		TRANSISTOR:NPN SI	04713	MPS918
Q29	294172		TRANSISTOR:NPN SI	04713	MPS918
Q30	294153		TRANSISTOR:PNP SI	04713	2N4126
Q31	294153		TRANSISTOR:PNP SI	04713	2N4126
Q32	294172		TRANSISTOR:NPN SI	04713	MPS918

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A1 Q33	294151		TRANSISTOR:NPN SI	04713	2N4124
Q34	294151		TRANSISTOR:NPN SI	04713	2N4124
Q35	294151		TRANSISTOR:NPN SI	04713	2N4124
R1	299701-470	6	RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R3	299701-102	12	RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R4	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R5	299701-471	5	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R6	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R7	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R8	299701-472	4	RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R9	299701-152	12	RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R10	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R11	299701-103	4	RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R12	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R13	299701-681	6	RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R14	299701-101	2	RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R15	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R17	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R18	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R19	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R21	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R22	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R23	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R24	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R25	299701-103		RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R26	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R27	299701-622	5	RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R28	299701-681		RESISTOR:FXD CAR 600 OHM -+5% 1/4W	01121	RC07GF681J
R29	299701-301	1	RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R30	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R32	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R33	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R34	299702-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R36	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R37	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R38	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R39	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R40	299701-103		RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R41	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R42	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R43	299701-681		RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R44	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R45	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R47	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R48	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R49	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R50	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R51	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R52	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R53	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R54	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R55	299701-103		RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R56	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R57	299701-681		RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R58	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R59	299701-681		RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R60	299701-681		RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R61	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R62	299701-362	2	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R63	299701-391	4	RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
R64	299701-242	2	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R65	299701-391		RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
R66	299701-362		RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A1 R67	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R68	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R69	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R70	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R71	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R72	299701-100		RESISTOR:FXD CAR 10 OHM –+5% 1/4W	01121	RC07GF100J
R73	299701-100		RESISTOR:FXD CAR 10 OHM –+5% 1/4W	01121	RC07GF100J
TP1	293199	1	TEST POINT	00779	2-582118-0
U1	293355	4	IC:LIN MIXER	01295	SN76514N
U2	293350	3	IC:TTL DUAL J-K FF	01295	SN74S113N
U3	293355		IC:LIN MIXER	01295	SN76514N
U4	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
U5	293355		IC:LIN MIXER	01295	SN76514N
U6	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
U7	293355		IC:LIN MIXER	01295	SN76514N
U8	293496	1	IC:TTL DECADE COUNTER	18324	82S90
	294573	8	SOCKET:IC 14 PIN	01295	C931410
A2A2	249214		PWB ASSY, BCD FIXED TUNED 0.1 TO 100 HZ	56872	299214
C1	299243-001	11	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C2	299244-001	26	CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C4	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C5	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C6	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C7	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C8	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C9	299241-200	6	CAP:FXD DIP MICA 20PF –+5% 300WVDC	72136	DM5-200J
C10	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C11	299241-200		CAP:FXD DIP MICA 20PF –+5% 300WVDC	72136	DM5-200J
C12	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C13	299241-430	4	CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C14	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C15	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C16	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C17	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C18	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C19	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C20	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C21	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C22	299241-100	6	CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C23	299241-680	3	CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C24	299241-100		CAP:FXD DIP MICA 10PF –+5PF 300WVDC	72136	DM5-100J
C25	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C26	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C27	299241-200		CAP:FXD DIP DISC 20PF –+5% 300WVDC	72136	DM5-200J
C28	299241-200		CAP:FXD DIP MICA 20PF –+5% 300WVDC	72136	DM5-200J
C29	299250-131	3	CAP:FXD DIP MICA 130PF –+5% 500WVDC	14655	CM04FD141J03
C30	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C31	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C32	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C33	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C34	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C35	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C36	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C37	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C38	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C39	299241-120	2	CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-120J
C40	299241-750	1	CAP:FXD DIP MICA 75PF –5% 300WVDC	72136	DM5-750J
C41	299241-120		CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-120J
C42	299245-001	2	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C43	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C44	299250-241	3	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C45	299250-151	1	CAP:FXD DIP MICA 150PF –+5% 500WVDC	14655	CM04FD151J03

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A2 C46	299250-241		CAP:FXD DIP MICA 240PF -+5% 500WVDC	14655	CM04FD241J03
C47	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C48	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C49	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C50	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C51	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C52	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C53	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C54	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C55	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C56	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C57	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C58	299241-680		CAP:FXD DIP MICA 68PF -+0.5% 300WVDC	72136	DM5-680J
C59	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C60	299245-001		CAP:FXD CER .1UF -+20% 50WVDC	72982	8121-050-651-104M
C61	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C62	299241-270	1	CAP:FXD DIP MICA 27PF -+5% 300WVDC	72136	DM5-270J
C63	299250-131		CAP:FXD DIP MICA 130PF -+5% 500WVDC	14655	CM04FD131J03
C64	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C65	299250-131		CAP:FXD DIP MICA 130PF -+5% 500WVDC	14655	CM04FD131J03
C66	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C75	299240-002	1	CAP:FXD DIP TA 22UF -+20% 15WVDC	05397	T368B226M015AS
C76	299240-001	1	CAP:FXD DIP TA 6.8UF -+20% 35WVDC	05397	T368B685M035AS
C77	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C78	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	5HKS-S10
C79	299250-241		CAP:FXD DIP MICA 240PF -+5% 500WVDC	14655	CM04FD241J03
C80	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	CM5-430J
CR1	299251-001	8	DIODE:SIGNAL	72314	IN4009
CR2	299251-001		DIODE:SIGNAL	72314	IN4009
CR3	299251-001		DIODE:SIGNAL	72314	IN4009
CR4	299251-001		DIODE:SIGNAL	72314	IN4009
CR5	299251-0011		DIODE:SIGNAL	72314	IN4009
CR6	299251-001		DIODE:SIGNAL	72314	IN4009
CR7	299251-001		DIODE:SIGNAL	72314	IN4009
CR8	299251-001		DIODE:SIGNAL	72314	IN4009
E1	293310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-026	4	INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L2	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L3	299237-038	9	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L4	299237-032	4	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L5	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L6	299237-022	4	INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L7	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L8	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L9	299237-028	2	INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L10	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L11	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L12	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L13	299237-024	6	INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L14	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L15	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L16	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L17	299237-020	2	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L19	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L20	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L21	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L22	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L23	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L24	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L25	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L26	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L27	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L28	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A2 L29	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L30	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L33	299237-032		INDUCTOR:FXD MOLDED 3.3UH	98800	1025-032
L34	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
Q1	294172	24	TRANSISTOR:NPN SI	04713	MPS918
Q2	294172		TRANSISTOR:NPN SI	04713	MPS918
Q3	294172		TRANSISTOR:NPN SI	04713	MPS918
Q4	294172		TRANSISTOR:NPN SI	04713	MPS918
Q5	294172		TRANSISTOR:NPN SI	04713	MPS918
Q6	294153	8	TRANSISTOR:PNP SI	04713	2N4126
Q7	294153		TRANSISTOR:PNP SI	04713	2N4126
Q8	294172		TRANSISTOR:NPN SI	04713	MPS918
Q9	294172		TRANSISTOR:NPN SI	04713	MPS918
Q10	294172		TRANSISTOR:NPN SI	04713	MPS918
Q11	294172		TRANSISTOR:NPN SI	04713	MPS918
Q12	294172		TRANSISTOR:NPN SI	04713	MPS918
Q13	294172		TRANSISTOR:NPN SI	04713	MPS918
Q14	294153		TRANSISTOR:PNP SI	04713	2N4126
Q15	294153		TRANSISTOR:PNP SI	04713	2N4126
Q16	294172		TRANSISTOR:NPN SI	04713	MPS91B
Q17	294172		TRANSISTOR:NPN SI	04713	MPS918
Q18	294172		TRANSISTOR:NPN SI	04713	MPS918
Q19	294172		TRANSISTOR:NPN SI	04713	MPS918
Q20	294172		TRANSISTOR:NPN SI	04713	MPS918
Q21	294172		TRANSISTOR:NPN SI	04713	MPS918
Q22	294153		TRANSISTOR:PNP SI	04713	2N4126
Q23	294153		TRANSISTOR:PNP SI	04713	2N4126
Q24	294172		TRANSISTOR:NPN SI	04713	MPS918
Q25	294172		TRANSISTOR:NPN SI	04713	MPS918
Q26	294172		TRANSISTOR:NPN SI	04713	MPS918
Q27	294172		TRANSISTOR:NPN SI	04713	MPS918
Q28	294172		TRANSISTOR:NPN SI	04713	MPS918
Q29	294172		TRANSISTOR:NPN SI	04713	MPS918
Q30	294153		TRANSISTOR:PNP SI	04713	2N4126
Q31	294153		TRANSISTOR:PNP SI	04713	2N4126
Q32	294172		TRANSISTOR:NPN SI	04713	MPS918
Q35	294151		TRANSISTOR:NPN SI	04713	2N4124
R1	299701-470	6	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R3	299701-102	12	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R4	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R5	299701-471	5	RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R6	299701-101	4	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R7	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R8	299701-472	4	RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R9	299701-152	12	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R10	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R11	299701-103	4	RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R12	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R13	299701-681	6	RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R14	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R15	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R17	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R18	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R19	299701-471		RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R21	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R22	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R23	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R24	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R25	299701-103		RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R26	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R27	299701-622	5	RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R28	299701-681		RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R29	299701-301	1	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R30	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R32	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R33	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A2 R34	299701-471		RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R36	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R37	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R38	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R39	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R40	299701-103		RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R41	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R42	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R43	299701-681		RESISTOR:FXD CAR 600 OHM –+5% 1/4W	01121	RC07GF681J
R44	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R45	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R47	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R48	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R49	299701-471		RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R50	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R51	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R52	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R53	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R54	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R55	299701-103		RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R56	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R57	299701-681		RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R58	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF822J
R59	299701-681		RESISTOR:CAR 600 OHM –+5% 1/4W	01121	RC07GF681J
R60	299701-681		RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R61	299701-471		RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R70	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R71	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R72	299701-570		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R73	299701-470		RESISTOR:FXD CAR 47 OHM ±5% 1/4W	01121	RC07GF470J
TP1	293199	1	TEST POINT	00779	2-58118-0
U1	293355	4	IC:LIN MIXER	01295	SN76514N
U2	293350	3	IC:TTL DUAL J-K FF	01295	SN74S113N
U3	293355		IC:LIN MIXER	01295	SN76514N
U4	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
U5	293355		IC:LIN MIXER	01295	SN76514N
U6	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
U7	293355		IC:LIN MIXER	01295	SN76514N
U8	293496	8	IC:TTL DECADE COUNTER	18324	82S90
	294573		SOCKET:IC 14 PIN	01295	C931410
A2A3	299214		SAME AS A2A2		
A2A4	299214		SAME AS A2A2		
A2A5	299225		PWB ASSY, LOWER ORDER BCD JUMPER BD	56872	299225
A2A6	299203		PWB ASSY, BCD FIXED TUNED INTER-CONNECTING	56872	299203
C1	299244-001	8	CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C2	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C3	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C4	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C5	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C6	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C7	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
C8	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	5HKS-S10
J1	299235-001	4	CONNECTOR:RT ANGLE PC BD	98921	700214
J2	299235-001		CONNECTOR:RT ANGLE PC BD	98291	700214
J3	299235-001		CONNECTOR:RT ANGLE PC BD	98291	700214
J4	299235-001	5	CONNECTOR:RT ANGLE PC BD	98291	700214
J6	294406		CONNECTOR:22 PIN DUAL EDGE	91662	00-6007-044-451-018
J7	294406		CONNECTOR:22 PIN DUAL EDGE	91662	00-6007-044-451-018
J8	294406		CONNECTOR:22 PIN DUAL EDGE	91662	00-6007-044-451-018

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A2A6 J9	294406		CONNECTOR:22 PIN DUAL EDGE	91662	00-6007-044-451-018
J10	294406		CONNECTOR:22 PIN DUAL EDGE	91662	00-6007-044-451-018
J11	299932		HEADER:DOUBLE ROW STRAIGHT POST 26 POS	00779	1-87227-3
A2A7	299220		PWB ASSY, BCD DISTRIBUTION BD	56872	299220
	299933	1	HEADER:RT ANGLE 26 PIN	00779	1-87230-3
A2A8	299220		SAME AS A2A7		
A4	298934-1		ASSY, HIGHER ORDER BCD	56872	298934-1
FL1	293609	12	FILTER CONNECTOR	72982	1201-066
FL2	293609		FILTER CONNECTOR	72982	1201-066
FL3	293609		FILTER CONNECTOR	72982	1201-066
FL4	293609		FILTER CONNECTOR	72982	1201-066
FL5	293609		FILTER CONNECTOR	72982	1201-066
FL6	293609		FILTER CONNECTOR	72982	1201-066
FL7	293609		FILTER CONNECTOR	72982	1201-066
FL8	293609		FILTER CONNECTOR	72982	1201-066
FL9	293609		FILTER CONNECTOR	72982	1201-066
FL10	293609		FILTER CONNECTOR	72982	1201-066
FL11	293609		FILTER CONNECTOR	72982	1201-066
FL12	293609		FILTER CONNECTOR	72982	1201-066
A4A1	298904		PWR ASSY, BCD 1	56872	298904
C1	299241-430	2	CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C2	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C3	299241-101	2	CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C4	299250-241	2	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241-J03
C5	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C6	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241-J03
C7	299245-002	6	CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C8	299245-001		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C9	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C10	299250-391	2	CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C11	299250-181	2	CAP:FXD DIP MICA 180PF –+5% 500WVDC	14655	CM04FD181J03
C12	299250-131	2	CAP:FXD DIP MICA 130PF –+5% 500WVDC	14655	CM04FD131J03
C13	299250-131		CAP:FXD DIP MICA 130PF –+5% 500WVDC	14655	CM04FD131J03
C14	299250-181		CAP:FXD DIP MICA 180PF –+5% 500WVDC	14655	CM04F0181J03
C15	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C16	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C17	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C18	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
E1	293310	4	TERMINAL SOLDER	71279	1597-2-01
E2	293306	2	TERMINAL:DISCONNECT	71279	1019-2-901
E3	293306		TERMINAL:DISCONNECT	71279	1019-2-901
E4	293310		TERMINAL SOLDER	71279	1597-2-01
E5	293310		TERMINAL SOLDER	71279	1597-2-01
E6	293310		TERMINAL SOLDER	71279	1597-2-01
J1	299235-001	1	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-008	1	INDUCTOR:FXD MOLDED .33UH	99800	1025-08
L2	231105-003	2	INDUCTOR:VAR .44UH	56872	231105-003
L3	299237-032	2	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L4	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L5	299227-048	2	INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L6	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L7	231105-004	4	INDUCTOR:VAR .9UH	56872	231105-004
L8	299237-038	2	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L9	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L10	299237-044	1	INDUCTOR:FXD MOLDED 10.0 UH	99800	1025-044
L11	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L12	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L13	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L14	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A1 M1	231104-001	1	MIXER	15542	SRA-1
Q1	294172	1	TRANSISTOR	04713	MP5918
R1	299701-301	4	RESISTOR, 300 OHM 1/4W	01121	RC07GF301J
R2	299701-180	2	RESISTOR, 18 OHM 1/4W	01121	RC07GF180J
R3	299701-301		RESISTOR, 300 OHM, 1/4W	01121	RC07GF301J
R4	299701-301		RESISTOR, 300 OHM, 1/4W	01121	RC07GF301J
R5	299701-180		RESISTOR, 18 OHM, 1/4W	01121	RC07GF180J
R6	299701-301		RESISTOR, 300 OHM	01121	RC07GF301J
R7	299701-362	1	RESISTOR, 3.6K, 1/4W	01121	RC07GF362J
R8	299701-242	1	RESISTOR, 2.4K, 1/4W	01121	RC07GF242J
R9	299701-391	2	RESISTOR, 390 OHM, 1/4W	01121	RC07GF391J
R10	299701-391		RESISTOR, 390 OHM, 1/4W	01121	RC07GF391J
R11	299701-471	1	RESISTOR, 470 OHM, 1/4W	01121	RC07GF471J
R12	299701-101	1	RESISTOR, 100 OHM, 1/4W	01121	RC07GF101J
U1	293350	1	IC:TTL DUAL J-K FF	01295	SN74S113N
	293994	1	TRANSIPAD	07047	10105-DAP
	294550	8	SOCKET	00779	1-331677-8
	294573	1	SOCKET, 14 PIN	01295	C931410
A4A2	298905		PWB ASSY, BCD 2	56872	298905
C1	299241-101	6	CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C2	299250-301	3	CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C3	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C4	299250-241	2	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C5	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C6	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C7	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C8	299245-002	6	CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C9	299245-002		CAP:FXD CER .01UF ±20% 50WVDC	72982	8121-M058-651-103M
C10	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C11	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C12	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C13	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C14	299250-151		CAP:FXD DIP MICA 150PF –+5% 500WVDC	14655	CM04FD151J03
C15	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C16	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C17	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C18	299250-151		CAP:FXD DIP MICA 150PF –+5% 500WVDC	14655	CM04FD151J03
C19	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
E1	293305	3	TERMINAL:DISCONNECT	71279	1019-2-901
E2	293310	5	TERMINAL SOLDER	71279	1597-2-01
E3	293305		TERMINAL SOLDER	71279	1019-2-901
E4	293305		TERMINAL SOLDER	71279	1019-2-901
E5	293310		TERMINAL SOLDER	71279	1597-2-01
E6	293310		TERMINAL SOLDER	71279	1597-2-01
E7	293310		TERMINAL SOLDER	71279	1597-2-01
E8	293310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-016	2	INDUCTOR:FXD MOLDED .68UH	99800	1025-016
L2	299237-016		INDUCTOR:FXD MOLDED .68UH	99800	1025-016
L3	231105-001	2	INDUCTOR:VAR .133UH	56872	231105-001
L4	299237-024	1	INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L5	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L6	299237-020	1	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L7	299237-042	3	INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L8	231105-003	4	INDUCTOR:VAR .44UH	56872	231105-003
L9	299237-038	2	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L10	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L11	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L12	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L13	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A2 L14	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L15	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
M1	2331104-001	1	MIXER	15542	SRA-1
Q1	249172	1	TRANSISTOR:NPN SI	04713	MPS918
R1	299701-161	2	RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R2	299701-470	1	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R3	299701-161		RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R4	299701-301	2	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R5	299701-180	1	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R6	299701-301		RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R7	299701-362	1	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R8	299701-242	1	RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R9	299701-391	2	RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R10	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R11	299701-471	1	RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R12	299701-101	1	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
U1	293350	1	IC:TTL DUAL J-K FF	01295	SN74S113N
	294550	8	SOCKET:PIN	00779	1-331677-8
	294573	1	SOCKET:IC 14 PIN	01295	C931410
	293994	1	TRANSIPAD	07047	10105-DAP
A4A3	298906		PWR ASSY, BCD 4	56872	298906
C1	299241-680	2	CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C2	299250-241	1	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C3	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C4	299250-181	4	CAP:FXD DIP MICA 180PF –+5% 500WVDC	14655	CM04FD181J03
C5	299241-101	2	CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C6	299241-750	1	CAP:FXD DIP MICA 75PF –+5% 300WVDC	72136	DM5-750J
C7	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C8	299250-181		CAP:FXD DIP MICA 180PF –+5% 500WVDC	14655	CM04FD181J03
C9	299245-002	6	CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C10	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C11	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C12	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C13	299241-100	1	CAP:FXD CER MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C14	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C15	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C16	299250-301	2	CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C17	299250-391	2	CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C18	299250-181		CAP:FXD DIP MICA 180PF –+5% 500WVDC	14655	CM04FD181J03
C19	299250-181		CAP:FXD DIP MICA 180PF –+5% 500WVDC	14655	CM04FD181J03
C20	299250301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C21	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
E1	293305	4	TERMINAL:DISCONNECT	71279	1019-2-901
E2	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E3	293310	4	TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
E5	293310		TERMINAL SOLDER	71279	1597-2-01
E6	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E7	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E8	2993310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-012	2	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L2	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L3	231105-002	3	INDUCTOR:VAR .27UH	56872	231105-002
L4	299237-032	4	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L5	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L6	231105-002		INDUCTOR:VAR .27UH	56872	231105-002
L7	231105-002		INDUCTOR:VAR .27UH	56872	231105-002
L8	299237-048	2	INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L9	299237-020	1	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L10	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L11	231105-003	4	INDUCTOR:VAR .44UH	56872	231105-003
L12	299237-032	4	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A3 L13	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L14	299237-038	1	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L15	231105-003		INDUCTOR:FXD .44UH	56872	231105-003
L16	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L17	2331105-003		INDUCTOR:VAR .44UH	56872	231105-003
M1	231104-001	1	MIXER	15542	SRA-1
Q1	294172	1	TRANSISTOR:NPN SI	04713	MPS918
R1	299701-101	3	RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R2	299701-470	1	RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R3	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R4	299701-301	2	RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R5	299701-180	1	RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R6	299701-301		RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R7	299701-362	1	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R8	299701-242	1	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R9	299701-391	2	RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
R10	299701-391		RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
R11	299701-471	1	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R12	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
U1	293350	1	IC:TTL DUAL J-K FF	01295	SN74S113N
	294550	8	SOCKET	00779	1-331677-8
	294566	1	SOCKET		014ST-7519
	293394	1	TRANSIPAD	07047	10105-DAP
A4A4	298907		PWB ASSY, BCD 8 NO. 1	56872	298907
C1	249241-101	2	CAP:FXD DIP MICA 100PF -+5% 300WVDC	72136	DM5-101J
C2	299241-101		CAP:FXD DIP MICA 100PF -+5% 300WVDC	72136	DM5-101J
C3	299241-680	2	CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C4	299250-181	2	CAP:FXD DIP MICA 180PF -+5% 500WVDC	14655	CM04FD181J03
C5	299241-470	2	CAP:FXD DIP MICA 47PF -+5% 300WVDC	72136	DM5-470J
C6	299241-470		CAP:FXD DIP MICA 47PF -+5% 300WVDC	72136	DM5-470J
C7	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C8	299280-181		CAP:FXD DIP MICA 180PF -+5% 500WVDC	14655	CM04FD181J03
C9	299245-002	4	CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C10	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C11	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C12	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
E1	293310	7	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
E3	293310		TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
E5	293310		TERMINAL SOLDER	71279	1597-2-01
E6	293310		TERMINAL SOLDER	71279	1597-2-01
E7	293310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-012	1	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L2	231105-002	4	INDUCTOR:VAR .27UH	56872	231105-002
L3	299237-032	2	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L4	231105-002		INDUCTOR:VAR .27UH	00752	231105-002
L5	299237-038	2	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L6	231105-002		INDUCTOR:VAR .27UH	56872	231105-002
L7	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L8	231105-002		INDUCTOR:VAR .27UH	56872	231105-002
L9	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
M1	231104-001	1	MIXER	15542	SRA-1
Q1	294172	1	TRANSISTOR:NPN SI	04713	MPS918
R1	299701-101	2	RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R2	299701-680	1	RESISTOR:FXD CAR 68 OHM -+5% 1/4W	01121	RC07GF680J
R3	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R4	299701-301	2	RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R5	299701-180	1	RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R6	299701-301		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF301J
R7	299701-362	1	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R8	299701-242	1	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R9	299701-391	2	RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A4 R10	299701-391		RESISTOR:FXD CAR 350 OHM –+5% 1/4W	01121	RC07GF391J
	294550	8	SOCKET:PIN	56872	1-331677-8
	293994	1	TRANSIPAD	070	10105-DAP
A4A5	298908		PWB ASSY, BCD 8 NO. 2	007	298908
C1	299250-181	2	CAP:FXD DIP MICA 180PF –+5% 500WVDC	146	CM04FD181J03
C2	299241-680	2	CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C3	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C4	299250-181		CAP:FXD DIP MICA 180PF –+5% 500WVDC	14655	CM04FD181J03
C5	299245-002	12	CAP:FXD CER .01UH –+20% 50WVDC	72982	8121-M058-651-103M
C6	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C7	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C8	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C9	299245-001	4	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-M058-W5R0-104M
C10	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-M058-W5R0-104M
C11	299240-002	1	CAP:FXD DIP TA 22UF –+20% 15WVDC		T268B226M015AS
C12	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-M058-W5R0-104M
C13	299250-471	2	CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C14	299250-132	2	CAP:FXD DIP MICA 1300PF –+5% 500WVDC	14655	CM06FD132J03
C15	299250-331	3	CAP:FXD DIP MICA 330PF –+5% 100WVDC	14655	CM04FA331J03
C16	299241-070	2	CAP:FXD DIP MICA 7PF –+0.5PF 300WVDC	72136	DM5-070D
C17	299250-331		CAP:FXD DIP MICA 330PF –+0.5PF 100WVDC	72136	CM04FA331J03
C18	299250-331		CAP:FXD DIP MICA 330PF –+0.5PF 100WVDC	72136	CM04FA331J03
C19	299241-070		CAP:FXD DIP MICA 7PF –+0.5PF 300WVDC	72136	DM5-070D
C20	299250-471		CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C21	299250-132		CAP:FXD DIP MICA 1300PF –+5% 500WVDC	14655	CM06FD132J03
C22	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C23	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C24	299250-241	3	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C25	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C26	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C27	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C28	299250-391	2	CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C29	299250-151	1	CAP:FXD DIP MICA 150PF –+5% 500WVDC	14655	CM04FD151J03
C30	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C31	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C32	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C33	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C34	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C35	299241-430	2	CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C36	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C37	299240-001	1	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C38	299245-001		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-M058-W5R0-104M
C39	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
E1	293310	2	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	72179	1597-2-01
J1	299235-001	1	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	231105-002	2	INDUCTOR:VAR .27UH	56872	231105-002
L2	231105-002		INDUCTOR:VAR .27UH	56872	231105-002
L3	299237-038	3	INDUCTOR:FXD MOLDED 5.6UH	56872	1025-038
L4	299237-048	7	INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L5	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L6	299237-032	3	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L7	231105-004	5	INDUCTOR:VAR .9UH	56872	231105-004
L8	299237-056	2	INDUCTOR:FXD MOLDED 33UH	99800	1025-046
L9	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L10	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L11	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L12	299237-056		INDUCTOR:FXD MOLDED 33UH	99800	1025-056

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A5 L13	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L14	299237-048		INDUCTOR:VAR MOLDED 15UH	99800	1025-048
L15	231105-001	3	INDUCTOR:VAR .133UH	56872	231105-001
L16	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L17	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L18	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L19	299237-008	1	INDUCTOR:FXD MOLDED .33UH	99800	1025-08
L20	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L21	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L22	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L23	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L24	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L25	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L26	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
Q1	299172	2	TRANSISTOR:NPN SI	04713	MPS918
Q2	294156	1	TRANSISTOR:PNP SI	04713	2N4403
Q3	299239-001	2	TRANSISTOR:NPN SI	04713	2N5179
Q4	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q5	294172		TRANSISTOR:NPN SI	04713	MPS918
R1	299701-362	3	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R2	299701-242	3	RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R3	299701-391	4	RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R4	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R5	299701-681	1	RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R6	299701-102	2	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R7	299701-101	1	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R8	299701-301	1	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R9	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R10	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R11	299701-471	1	RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R12	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R13	299701-622	1	RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R14	299701-152	1	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R15	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R16	299701-201	1	RESISTOR:FXD CAR 200 OHM –+5% 1/4W	01121	RC07GF201J
R17	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R18	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R19	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R20	299701-101	2	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R21	299701-510	1	RESISTOR:FXD CAR 51 OHM –+5% 1/4W	01121	RC07GF510J
R22	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R23	299701-680	1	RESISTOR:FXD CAR 68 OHM –+5% 1/4W	01121	RC07GF680J
U1	293496	1	IC:TTL DECADE COUNTER	18324	82S90
	294573	1	SOCKET:IC 14 PIN	01295	C931410
A4A6	298909		PWB ASSY, BUFFER AMP	56872	298909
C1	299243-001	4	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C2	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299245-002	4	CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C4	299241-820	2	CAP:FXD DIP MICA 82PF –+5% 300WVDC	72136	DM5-820J
C5	299241-101	2	CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C6	299241-560	2	CAP:FXD DIP MICA 56PF –+5% 300WVDC	72136	M5-560J
C7	299250-241	1	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C8	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C9	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C10	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C11	299243-001		CAP:FXD CER DISC .011UF –+20% 1000WVDC	56289	CK60AW102M
C12	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C13	299241-820		CAP:FXD DIP MICA 82PF –+5% 300WVDC	72136	DM5-820J
C14	299241-101		CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C15	299241-560		CAP:FXD DIP MICA 56PF –+5% 300WVDC	72136	DM5-560J
C16	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
E1	29	4	TERMINAL SOLDER	71279	1597-2-01
E2	29		TERMINAL SOLDER	71279	1597-2-01
E3	29		TERMINAL SOLDER	71279	1597-2-01

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A6 E4	29		TERMINAL SOLDER	71279	1597-2-01
E5	293306	1	TERMINAL:DISCONNECT	71279	1019-2-901
J2	299235-001	4	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J3	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J4	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J5	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-038	5	INDUCTOR:FXD MOLDED 5.6UH	98900	1025-038
L2	299237-038		INDUCTOR:FXD MOLDED 5.6UH	98900	1025-038
L3	231105-001	4	INDUCTOR:VAR .133UH	56872	231105-001
L4	299237-034	2	INDUCTOR:FXD MOLDED 3.9UH	98900	1025-034
L5	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L6	299237-036		INDUCTOR:FXD MOLDED 5.6UH	98900	1025-038
L7	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L8	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L9	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L10	299237-034		INDUCTOR:FXD MOLDED 3.9UH	99800	1025-034
L11	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
Q1	299239-001	2	TRANSISTOR:NPN SI	04713	2N5179
Q2	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-010	6	RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R3	299701-362	2	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R4	299701-242	2	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R5	299701-102	2	RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R6	299701-471	2	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R7	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R8	299701-180	2	RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R9	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R10	299701-242		RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R11	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R12	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R14	299701-362		RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R15	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R16	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R17	299701-180		RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R18	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
A4A7	298910		BUS ASSY, 64/65 SWITCH	56872	298910
C1	299245-002	5	CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C2	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C4	299245-001	2	CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C5	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C7	299871-391	1	CAP:FXD CER 290PF -+10% 200WVDC	56289	CK05BX391K
C8	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C9	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C10	299245-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C11	299241-101	1	CAP:FXD DIP MICA 100PF -+5% 300WVDC	72136	DM5-101J
C12	299241-750	1	CAP:FXD DIP MICA 75PF -+5% 300WVDC	72136	DM5-750J
C13	299241-560	1	CAP:FXD DIP MICA 56PF -+5% 300WVDC	72136	DM5-560J
C14	299250-241	1	CAP:FXD DIP MICA 240PF -+5% 500WVDC	14655	CM04FD241J03
CR1	299251-001	2	DIODE:SIGNAL	72314	IN4009
CR2	299251-001		DIODE:SIGNAL	72314	IN4009
E1	293306	8	TERMINAL:DISCONNECT	71279	1019-2-901
E2	293306		TERMINAL:DISCONNECT	71279	1019-2-901
E4	293306		TERMINAL:DISCONNECT	71279	1019-2-901
E5	293306		TERMINAL:DISCONNECT	71279	1019-2-901
E6	293306		TERMINAL:DISCONNECT	71279	1019-2-901
E7	293306		TERMINAL:DISCONNECT	71279	1019-2-901
E8	293306		TERMINAL:DISCONNECT	71279	1019-2-901
E9	293306		TERMINAL:DISCONNECT	71279	1019-2-901
L1	299237-038	4	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L2	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L3	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L4	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L5	231105-001	2	INDUCTOR:VAR .133UH	56872	231105-001

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A7 L6	299237-032	1	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L7	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
Q1	294172	5	TRANSISTOR:NPN SI	04713	MPS918
Q2	294172		TRANSISTOR:NPN SI	04713	MPS918
Q3	294172		TRANSISTOR:NPN SI	04713	MPS918
Q4	294172		TRANSISTOR:NPN SI	04713	MPS918
Q5	294172		TRANSISTOR:NPN SI	04713	MPS918
Q7	294153	2	TRANSISTOR:PNP SI	04713	2N4126
Q8	294153		TRANSISTOR:PNP SI	04713	2N4126
Q9	299239-001	1	TRANSISTOR:NPN SI	04713	2N5179
R1	299701-101	4	RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R2	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R3	299701-102	4	RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R4	299701-681	2	RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R5	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R6	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R7	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R8	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R9	299701-152	3	RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R10	299701-472	1	RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R12	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R13	299701-103	1	RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R14	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R15	299701-242	1	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R16	299701-362	1	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R17	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R18	299701-681		RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
A4A8	298910		SAME AS A4A7		
A4A9	298910		SAME AS A4A7		
A4A10	298910		SAME AS A4A7		
A4A11	299248	35	PWB ASSY, BCD FIXED TUNED 10 KHZ	56872	299248
C1	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C2	299243-001		CAP:FXD CER DISC..001UF -+20% 1000WVDC	56289	CK60AW102M
C3	299241-060	4	CAP:FXD DIP MICA 6PF -+0.5PF 300WVDC	72136	DM5-0600
C4	299241-200	6	CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C5	299241-101	1	CAP:FXD DIP MICA 100PF -+5% 300WVDC	72136	DM5-101J
C6	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C7	299244-001	5	CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C8	299241-430	6	CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C9	299250-241	3	CAP:FXD DIP MICA 240PF -+5% 500WVDC	14655	CM04FD241J03
C10	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C11	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C12	299241-100	6	CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C13	299241-060		CAP:FXD DIP MICA 6PF -+0.5PF 300WVDC	72136	DM5-0600
C14	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C15	299241-680	3	CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C16	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C17	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F01C103Z
C18	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C19	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C20	299250-131	3	CAP:FXD DIP MICA 130PF -+5% 500WVDC	14655	CM04FD131J03
C21	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C22	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C23	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C24	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C25	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C26	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C27	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A11C28	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C29	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C30	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C31	299241-060		CAP:FXD DIP MICA 6PF -+0.5PF 300WVDC	72136	DM5-0600
C32	299241-120	2	CAP:FXD DIP MICA 12PF -+5% 300WVDC	72136	DM5-120J
C33	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C34	299241-750	1	CAP:FXD DIP MICA 75PF -+5% 300WVDC	72136	DM5-750J
C35	299241-120		CAP:FXD DIP MICA 12PF -+5% 300WVDC	72136	DM5-120J
C36	299245-001	2	CAP:FXD CER .1UF -+20% 50WVDC	72982	8121-M058-W5R0-104M
C37	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C38	299250-241		CAP:FXD DIP MICA 240PF -+5% 500WVDC	14655	CM04FD241J03
C39	299250-151	1	CAP:FXD DIP MICA 150PF -+5% 500WVDC	14655	CM04FD151J03
C40	299250-241		CAP:FXD DIP MICA 240PF -+5% 500WVDC	14655	CM04FD241J03
C41	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C42	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C43	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C44	299241-060		CAP:FXD DIP MICA 6PF -+0.5 300WVDC	72136	DM5-0600
C45	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C46	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C47	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C48	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C49	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C50	299241-100		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C51	299245-001		CAP:FXD CER .1UF -+20% 50WVDC	72982	8121-M058-W5R0-104M
C52	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C53	299241-270	1	CAP:FXD DIP MICA 27PF -+5% 300WVDC	72136	DM5-270J
C54	299250-131		CAP:FXD DIP MICA 130PF -+5% 500WVDC	14655	CM04FD131J03
C55	299241-200		CAP:FXD DIP MICA 20PF -+5% 300WVDC	72136	DM5-200J
C56	299250-131		CAP:FXD DIP MICA 130PF -+5% 500WVDC	14655	CM04FD131J03
C57	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C58	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C59	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C60	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C61	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C62	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C63	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C64	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C65	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C66	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C67	299241-070	4	CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C68	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C69	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C70	299241-070		CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C71	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C72	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C73	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C74	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C75	299241-070		CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C76	299240-002	1	CAP:FXD DIP TA 22UF -+20% 15WVDC	05397	T368B226M015AS
C77	299240-001	1	CAP:FXD DIP TA 6.8UF -+20% 35WVDC	05397	T368B685M035AS
C78	299241-070		CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C79	299241-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C80	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C81	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C82	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C83	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C84	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C85	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
CR1	299251-001	8	DIODE:SIGNAL	72314	IN4009
CR2	299251-001		DIODE:SIGNAL	72314	IN4009
CR3	299251-001		DIODE:SIGNAL	72314	IN4009

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A11 CR4	299251-001		DIODE:SIGNAL	72314	IN4009
CR5	299251-001		DIODE:SIGNAL	72314	IN4009
CR6	299251-001		DIODE:SIGNAL	72314	IN4009
CR7	299251-001		DIODE:SIGNAL	72314	IN4009
CR8	299251-001		DIODE:SIGNAL	72314	IN4009
E1	293305	6	TERMINAL:DISCONNECT	71279	1019-2-901
E2	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E3	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E4	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E5	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E6	293305		TERMINAL:DISCONNECT	71279	1019-2-901
J7	299235-001	6	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J8	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J9	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J10	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J11	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J12	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-020	10	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L2	299237-038	18	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L3	299237-026	4	INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L4	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L5	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L6	299237-032	4	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L7	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L8	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L9	299237-036		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L10	299237-022	4	INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L11	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L12	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L13	299237-028	2	INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L14	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L15	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L16	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L17	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L18	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L19	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L20	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L21	299237-024	6	INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L22	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L23	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L24	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L25	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L26	299237-026		INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L27	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L28	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L29	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L30	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L31	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L32	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L33	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L34	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L35	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L36	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L37	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L38	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L39	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L40	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L41	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L42	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L43	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L44	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L45	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L46	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L47	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A11 L48	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
M1	231104-001	4	MIXER	15542	SRA-1
M2	231104-001		MIXER	15542	SRA-1
M3	231104-001		MIXER	15542	SRA-1
M4	231104-001		MIXER	15542	SRA-1
Q1	299239-001	4	TRANSISTOR:NPN SI	04713	2N5179
Q2	294151	7	TRANSISTOR:NPN SI	04713	2N4124
Q3	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q4	299151		TRANSISTOR:NPN SI	04713	2N4124
Q5	294172	20	TRANSISTOR:NPN SI	04713	MPS918
Q6	294172		TRANSISTOR:NPN SI	04713	MPS918
Q7	294172		TRANSISTOR:NPN SI	04713	MPS918
Q8	294172		TRANSISTOR:NPN SI	04713	MPS918
Q9	294153	8	TRANSISTOR:PNP SI	04713	2N4126
Q10	294172		TRANSISTOR:NPN SI	04713	MPS918
Q11	294153		TRANSISTOR:PNP SI	04713	2N4126
Q12	294172		TRANSISTOR:NPN SI	04713	MPS918
Q13	294172		TRANSISTOR:NPN SI	04713	MPS918
Q14	294153		TRANSISTOR:PNP SI	04713	2N4126
Q15	294172		TRANSISTOR:NPN SI	04713	MPS918
Q16	294172		TRANSISTOR:NPN SI	04713	MPS918
Q17	294172		TRANSISTOR:NPN SI	04713	MPS918
Q18	294153		TRANSISTOR:PNP SI	04713	2N4126
Q19	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q20	294151		TRANSISTOR:NPN SI	04713	2N4124
Q21	249239-001		TRANSISTOR:NPN SI	04713	2N5179
Q22	294151		TRANSISTOR:NPN SI	04713	2N4124
Q23	294151		TRANSISTOR:NPN SI	04713	2N4124
Q24	294172		TRANSISTOR:NPN SI	04713	MPS918
Q25	294172		TRANSISTOR:NPN SI	04713	MPS918
Q26	294172		TRANSISTOR:NPN SI	04713	MPS918
Q27	294172		TRANSISTOR:NPN SI	04713	MPS918
Q28	294153		TRANSISTOR:PNP SI	04713	2N4126
Q29	294172		TRANSISTOR:NPN SI	04713	MPS918
Q30	294153		TRANSISTOR:PNP SI	04713	2N4126
Q31	294172		TRANSISTOR:NPN SI	04713	MPS918
Q32	294172		TRANSISTOR:NPN SI	04713	MPS918
Q33	294172		TRANSISTOR:NPN SI	04713	MPS918
Q34	294172		TRANSISTOR:NPN SI	04713	MPS918
Q35	294153		TRANSISTOR:PNP SI	04713	2N4126
Q36	294172		TRANSISTOR:NPN SI	04713	MPS918
Q37	294153		TRANSISTOR:PNP SI	04713	2N4126
Q38	294151		TRANSISTOR:NPN SI	04713	2N4124
Q39	294151		TRANSISTOR:NPN SI	04713	2N4124
R1	299701-622	15	RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R2	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R3	299701-301	4	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301
R4	299701-471	4	RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R5	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R6	299701-681	6	RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R7	299701-101	17	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R8	299701-470	3	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R9	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R10	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R11	299701-301		RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R12	299701-471		RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R13	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R14	299701-681		RESISTOR:FXD CAR 600 OHM –+5% 1/4W	01121	RC07GF681J
R15	299701-680	1	RESISTOR:FXD CAR 68 OHM –+5% 1/4W	01121	RC07GF680J
R16	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R17	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R18	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R19	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R20	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A11 R21	299701-472	6	RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R22	299701-152	14	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R23	299701-102	18	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R24	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R25	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R26	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R27	299701-103	4	RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R28	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R29	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R30	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R31	299701-101		RESISTOR:FXD CAR 100 OHM ±5% 1/4W	01121	RC07GF101J
R32	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R33	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R34	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R35	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R36	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R37	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R38	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R39	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R40	299701-103		RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R41	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R42	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R43	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R44	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R45	299701-301	1	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R46	299701-471		RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R47	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R48	299701-681		RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R49	299701-161	1	RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R50	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R51	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R52	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R53	299701-301		RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R54	299701-471		RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R55	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R56	299701-681		RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R57	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R58	299701-681		RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R59	299701-681		RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R60	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R61	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R62	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R63	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R64	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R65	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R66	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R67	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R68	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R69	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R70	299701-103		RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R71	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R72	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R73	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R74	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R75	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R76	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R77	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R78	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R79	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R80	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R81	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R82	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R83	299701-103		RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R84	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R85	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A4A11 R86	299701-100	3	RESISTOR:FXD CAR 10 OHM –+5% 1/4W	01121	RC07GF100J
R87	299701-100		RESISTOR:FXD CAR 10 OHM –+5% 1/4W	01121	RC07GF100J
R88	299701-100		RESISTOR:FXD CAR 10 OHM –+5% 1/4W	01121	RC07GF100J
R89	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R90	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R91	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R92	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R93	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R94	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R95	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R96	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
U1	293350	3	IC:TTL DUAL J-K FF	01295	SN74S113N
U2	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
U3	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
U4	293496	1	IC:TTL DECADE COUNTER	18324	82S90
	294573	4	SOCKET:IC 14 PIN	01295	C931410
	294550	32	SOCKET:PIN	00779	1-331677-8
	293994	4	TRANSIPAD	07047	10105-DAP
A5	287935-1		ASSY, FIXED FREQUENCY GEN	56872	298935-1
FL1	293609	3	FILTER CONNECTOR	72982	1201-082
FL2	293609		FILTER CONNECTOR	72982	1201-082
FL3	293609		FILTER CONNECTOR	72982	1201-082
R1	299701-180	1	RESISTOR 18 OHM, ±5% 1/4W	00121	RC07GF180J
A5A1	298921		PWB ASSY, FIXED FREQUENCY INPUT	56872	298921
C1	299244-001	8	CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C2	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C3	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C4	299241-270	2	CAP:FXD DIP MICA 27PF –+5% 300WVDC	72136	DM5-270J
C5	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C6	299250-241	1	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C7	299241-270		CAP:FXD DIP MICA 27PF –+5% 300WVDC	72136	DM5-270J
C8	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C9	299250-301	3	CAP:FXD DIP MICA 300PF –+5% 300WVDC	14635	CM04FC301J03
C10	299241-070	1	CAP:FXD DIP MICA 7PF –+0.5PF 300WVDC	72136	DM5-070D
C11	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C12	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C13	299241-120	2	CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-120J
C14	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C15	299241-120		CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-120J
C16	299250-471	2	CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C17	299350-471		CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C18	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C19	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C20	299241-121	2	CAP:FXD DIP MICA 120PF	00752	
C21	293916	1	CAP:VARIABLE	91293	
C22	299241-121		CAP:FXD DIP MICA 120PF	56872	
E1	293306	1	TERMINAL SOLDER	71279	1597-2-01
E2	293305	1	TERMINAL SOLDER	71279	1019-2-901
J1	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-042	2	INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L2	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L3	231105-004	4	INDUCTOR:VAR .9UH	56872	231105-004
L4	299237-056	2	INDUCTOR:FXD MOLDED 33UH	99800	1025-056
L5	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L6	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L7	299237-056		INDUCTOR:FXD MOLDED 33UH	99800	1025-056
L8	231105-004		INDUCTOR:VAR .9UH	56872	231105-004
L9	231105-6	1	INDUCTOR:VARIABLE	99800	1025-048
L10	231105-7	1	INDUCTOR:VARIABLE	99800	1025-048
Q1	294151	2	TRANSISTOR:NPN SI	04713	2N4124
Q2	294151		TRANSISTOR:NPN SI	04713	2N4124
Q3	294157	1	TRANSISTOR:NPN SI	04713	2N3866

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A5A1 R1	299701-470	4	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R2	299701-362	6	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R3	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R4	299701-391	2	RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R5	299701-102	1	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R6	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R7	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R8	299701-681	1	RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R9	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R10	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R11	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R12	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R13	299701-201	1	RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF201J
R14	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R15	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
Y1	298357-001	1	CRYSTAL:110 MHZ	00752	298357-1
	293996	1	TRANSIPAD	07047	10028-DAP
A5A2	298918		PWB ASSY, 10 MHZ OUTPUT AMP	56872	298918
C1	299241-360	4	CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C2	299250-241	2	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C3	299241-360		CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C4	299244-001	6	CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C5	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C6	299241-360		CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	CM5-360J
C7	299241-360		CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C8	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C9	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C10	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C11	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C12	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
E1	293310		TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
J2	299235-001	2	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J3	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-042	4	INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L2	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L3	299237-056	2	INDUCTOR:FXD MOLDED 33UH	99800	1025-056
L4	299237-042		INDUCTOR:FXD MOLDED 6.2UH	99800	1025-042
L5	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L6	299237-056		INDUCTOR:FXD MOLDED 33UH	99800	1025-056
Q1	294151	2	TRANSISTOR:NPN SI	04713	2N4124
Q2	294151		TRANSISTOR:NPN SI	04713	2N4124
R1	299701-362	1	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R2	299701-242	1	RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R3	299701-391	4	RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R4	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R5	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R6	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
A5A3	298919		PWB ASSY, 60 MHZ GENERATOR	56872	298919
C1	299244-001	8	CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C2	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C3	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C4	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C5	299243-001	2	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C6	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C7	299241-560	4	CAP:FXD DIP MICA 56PF –+5% 300WVDC	72136	DM5-460J
C8	299241-010	3	CAP:FXD DIP MICA 1PF –+0.5PF 300WVDC	72136	DM5-010D
C9	299241-560		CAP:FXD DIP MICA 56PF –+5% 300WVDC	72136	DM5-560J
C10	299241-010		CAP:FXD DIP MICA 1PF –+0.5PF 300WVDC	72136	DM5-010D
C11	299241-560		CAP:FXD DIP MICA 56PF –+5% 300WVDC	72136	DM5-560J
C12	299241-010		CAP:FXD DIP MICA 1PF –+0.5PF 300WVDC	72136	DM5-010D
C13	299241-070	2	CAP:FXD DIP MICA 7PF –0.5PF 300WVDC	72136	DM5-070D
C14	299241-560		CAP:FXD DIP MICA 56PF –+5% 300WVDC	72136	DM5-560J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A5A3 C15	299250-391		CAP:FXD DIP MICA 390PF -+5% 100WVDC	14655	CM04FA391J03
C16	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C17	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C18	299245-001	2	CAP:FXD CER .1UF -+20% 50WVDC	72982	8121-M058-W5R0-104M
C19	299241-430	1	CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C20	299241-070		CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C22	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C24	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C25	299245-001		CAP:FXD CER .1UF -+20% 50WVDC	72982	8121-M058-W5R0-104M
C26	299240-001	1	CAP:FXD DIP TA 6.8UF -+20% 35WVDC	05397	T368B685M035AS
E1	293310	3	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
E3	293310		TERMINAL SOLDER	71279	1597-2-01
J4	299235-001	1	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-052	1	INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L2	299237-036	3	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L3	231105-001	4	INDUCTOR:VAR .133UH	56872	231105-001
L4	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L5	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L6	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L7	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L8	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L9	299237-022	2	INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L10	299237-022		INDUCTOR:FXD MOLDED 1.2UH	99800	1025-022
L11	299237-048		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-048
Q1	294151	1	TRANSISTOR:NPN SI	04713	2N4124
Q2	299239-001	2	TRANSISTOR:NPN SI	04713	2N5179
Q3	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-362	2	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R2	299701-242	2	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R3	299701-391	3	RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
R4	299701-391		RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
R7	299701-103	1	RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R8	299701-102	1	RESISTOR:FXD CAR 1000 OHM -+5% 1/4W	01121	RC07GF102J
R9	299701-362		RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R10	299701-242		RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R11	299701-391		RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
R12	299701-471	1	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
A5A4	298917-2		PWB ASSY, 65 MHZ GENERATOR	56872	298917-2
C1	299244-001	6	CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C2	299241-680	2	CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C3	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C4	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C5	299245-001	1	CAP:FXD CER .1UF -+20% 50WVDC	72982	8121-M058-W5R0-104M
C7	299243-001	9	CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C8	299241-030	1	CAP:FXD DIP MICA 3PF -+0.5PF 300WVDC	72136	DM5-030D
C9	293592-001	3	CAP:FXD DIP MICA 0.5PF -+0.5PF 300WVDC	93985	1C.5RC
C10	299241-070	2	CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C11	293592-001		CAP:FXD DIP MICA 0.5PF 300WVDC	93985	1C.5RC
C12	299241-070		CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C13	293592-001		CAP:FXD DIP MICA 0.5PF -+0.5PF 300WVDC	93985	1C.5RC
C15	299241-120	1	CAP:FXD DIP MICA 12PF -+5% 300WVDC	72136	DM5-120J
C16	299241-430	4	CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C17	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C18	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C19	299243-001		CAP:FXD CER DISC .001UH -+20% 1000WVDC	56289	CK60AW102M
C20	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C21	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C22	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C23	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A5A4 C24	299241-101	1	CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C25	299241-750	1	CAP:FXD DIP MICA 75PF –+5% 300WVDC	72136	DM5-750J
C26	299241-020	2	CAP:FXD DIP MICA 2PF –+0.5PF 300WVDC	72136	DM5-020D
C27	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C28	299241-010	2	CAP:FXD DIP MICA 1PF –+0.5PF 300WVDC	72136	DM5-010D
C29	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C30	299241-010		CAP:FXD DIP MICA 1PF –+0.5PF 300WVDC	72136	DM5-010D
C31	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C32	299241-020		CAP:FXD DIP MICA 2PF –+0.5PF 300WVDC	72136	DM5-020D
C33	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C34	299250-241	1	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C35	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C36	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C37	299240-002	1	CAP:FXD DIP TA 22UF –+20% 15WVDC	05397	T368B226M015AS
C39	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C40	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
E1	293305	2	TERMINAL:DISCONNECT	71279	1019-2-901
E2	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E3	2993310	3	TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
E5	2993310		TERMINAL SOLDER	71279	1597-2-01
J7	299235-001	1	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-052	2	INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L2	299237-024	2	INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L3	299237-038	5	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L4	231105-001	9	INDUCTOR:VAR .133UH	00752	231105-001
L5	231105-001		INDUCTOR:VAR .133UH	00752	231105-001
L6	231105-001		INDUCTOR:VAR .133UH	00752	231105-001
L7	231105-001		INDUCTOR:VAR .133UH	00752	231105-001
L8	299237-032	1	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L9	299237-024		INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L10	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L11	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L12	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L13	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L14	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L15	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L16	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L17	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L18	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L19	299237-052		INDUCTOR:FXD MOLDED 22UH	99800	1025-052
Q1	2994172	1	TRANSISTOR:NPN SI	04713	MPS918
Q2	294147	1	TRANSISTOR:NPN SI	04713	2N3866
Q3	249239-001	2	TRANSISTOR:NPN SI	04713	2N5179
Q4	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-362	3	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R2	299701-242	3	RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R3	299701-102	3	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R4	299701-681	1	RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R5	299701-270	1	RESISTOR:FXD CAR 27 OHM –+5% 1/4W	01121	RC07GF270J
R7	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R8	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R9	299701-201	1	RESISTOR:FXD CAR 200 OHM –+5% 1/4W	01121	RC07GF201J
R10	299701-102	3	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R11	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	00121	RC07GF362J
R12	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R13	299701-391	1	RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R17	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R18	299701-301	1	RESISTOR, 300 OHM, ±5% 1/4W	01121	RC07GF301J
R19	299701-471	1	RESISTOR:FXD CAR 470 OHM ±5% 1/8W	01121	RC05GF471J
R20	299700-102	1	RESISTOR:FXD CAR 1K ±5% 1/8W	01121	RC07GF102J
U1	293500-2	1	IC:ECL TYPE D FF	04713	MC1670L

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A5A4 U1	294573	1	SOCKET:IC 14PIN	01295	C931410
	293995	1	TRANSIPAD	07047	10027-DAP
A5A5	298915		PWB ASSY, 64 MHZ GENERATOR	56872	298915
C1	299244-001	7	CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C2	299241-030	1	CAP:FXD DIP MICA 3PF -+0.5PF 300WVDC	72136	DM5-030D
C3	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C5	299241-010	5	CAP:FXD DIP MICA 1PF -+0.5PF 300WVDC	72136	DM5-010D
C6	299243-001	10	CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C7	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F301C103Z
C8	299241-240	1	CAP:FXD DIP MICA 24PF -+5% 300WVDC	72136	DM5-240J
C9	299241-020	3	CAP:FXD DIP MICA 2PF -+0.5PF 300WVDC	72136	DM05-020D
C10	299241-330	3	CAP:FXD DIP MICA 33PF -+5% 300WVDC	72136	DM-330J
C11	299241-330		CAP:FXD DIP MICA 33PF -+5% 300WVDC	72136	DM5-330J
C12	299241-010		CAP:FXD DIP MICA 1PF -+0.5PF 300WVDC	72136	DM5-010D
C13	299241-330		CAP:FXD DIP MICA 33PF -+5% 300WVDC	72136	DM5-330J
C14	299241-010		CAP:FXD DIP MICA 1PF -+0.5PF 300WVDC	72136	DM5-101D
C15	299241-430	6	CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C16	299250-131	1	CAP:FXD DIP MICA 130PF -+5% 500WVDC	14655	CM04FD131J03
C17	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C18	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C19	299241-070	2	CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C20	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C21	299241-070		CAP:FXD DIP MICA 7PF -+0.5PF 300WVDC	72136	DM5-070D
C22	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C23	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C24	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C25	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C26	299241-020		CAP:FXD DIP MICA 2PF -5%PF 300WVDC	72136	DM5-020D
C27	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C28	299241-010		CAP:FXD DIP MICA 1PF -+0.5PF 300WVDC	72136	DM5-010D
C29	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C30	299241-010		CAP:FXD DIP MICA 1PF -+0.5PF 300WVDC	72136	DM5-010D
C31	299241-430		CAP:FXD DIP MICA 43PF -+5% 300WVDC	72136	DM5-430J
C32	299241-020		CAP:FXD DIP MICA 2PF -+0.5PF 300WVDC	72136	DM5-020D
C33	299241-680	1	CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C34	299250-241	1	CAP:FXD DIP MICA 240PF -+5% 500WVDC	14655	CM04FD241J03
C35	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C36	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C37	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C38	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C39	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C40	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
C41	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C42	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F501C103Z
E1	293310	6	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
E3	293310		TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
E5	293310		TERMINAL SOLDER	71279	1597-2-01
E6	293310		TERMINAL SOLDER	71279	1597-2-01
J8	299234-001	1	CONNECTOR:RT ANGLE PC BD	98921	51-353-0000
L1	299237-052	3	INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L2	299237-052		INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L3	299237-032	3	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L4	299237-038	2	INDUCTOR:FXD MOLDED 5.6UH	99800	1024-038
L5	231105-001	10	INDUCTOR:VAR .133UH	56872	231105-001
L6	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L7	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L8	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L9	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L10	299237-052		INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L11	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L12	299237-020	3	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L13	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A5A5 L14	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1024-032
L15	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L16	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L17	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L18	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L19	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L20	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L21	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
Q1	294151	1	TRANSISTOR:NPN SI	04713	2N4124
Q2	299239-001	3	TRANSISTOR:NPN SI	04713	2N5179
Q3	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q4	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-362	3	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R2	299701-242	4	RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R3	299701-301	2	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R4	299701-102	2	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R5	299701-103	1	RESISTOR:FXD CAR 10K –+5% 1/4W	01121	RC07GF103J
R6	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R7	299701-101	1	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R8	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R9	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R10	299701-391	2	RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R11	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
R12	299701-301		RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R13	299701-470	1	RESISTOR:FXD CAR 47 OHM ±5% 1/4W	01121	RC07GF470J
R14	299701-362		RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R15	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R16	299701-681	1	RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R17	299701-242		RESISTOR:FXD CAR 2.4K –+5% 1/4W	01121	RC07GF242J
R22	299701-152	1	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R23	299701-391		RESISTOR:FXD CAR 390 OHM –+5% 1/4W	01121	RC07GF391J
U1	293350	2	IC:TTL DUAL J-K FF	01295	SN74S113N
U2	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
	294573	1	SOCKET:IC 14 PIN	01295	C931410
	294573	1	SOCKET:IC 14 PIN	01295	C931410
A5A6	298926		PWB ASSY, 35/35.5 MHZ GENERATOR	56872	298926
C1	299244-001	6	CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C2	299241-050	3	CAP:FXD DIP MICA 5PF –+0.5PF 300WVDC	72136	DM5-050D
C3	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C4	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56829	C023F501C103Z
C5	299241-430	3	CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C6	299241-050		CAP:FXD DIP MICA 5P5 –+0.5PF 300WVDC	72136	DM5-050D
C7	299243-001	14	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C9	299250-131	1	CAP:FXD DIP MICA 130PF –+5% 500WVDC	14655	CM04FD131J03
C10	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C11	299241-050		CAP:FXD DIP MICA 5PF –+0.5PF 300WVDC	72136	DM5-050D
C12	299243-001		CAP:FXD CER DISC .001UF –+20K 1000WVDC	56289	CK60AW102M
C13	299240-001	2	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C14	299240-001		CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C15	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F501C103Z
C16	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C17	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C18	299244-001		CAP:FXD CER DISC .01UF –20+80% 1000WVDC	56289	C023F401C103Z
C19	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C20	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C21	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C22	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C23	299244-001		CAP:FXD CER DISC .01UF -20+80% 1000WVDC	56289	C023F301C103Z
C24	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C25	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C26	299250-241	1	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C27	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C28	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C29	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C30	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A5A6 C31	299241-040	1	CAP:FXD DIP MICA 4PF -+0.5PF 300WVDC	72136	DM5-040D
C32	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C33	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
CR1	293200		DIODE:SIGNAL	01295	IN645
E1	293310	4	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
E3	293310		TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
E5	293305	1	TERMINAL SOLDER	71279	1019-2-901
J6	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-020	4	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L2	299237-022		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-022
L3	231105-001	2	INDUCTOR:VAR .133UH	56872	231105-001
L4	299237-052	4	INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L5	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L6	299237-038	2	INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L7	299237-052		INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L8	299237-052		INDUCTOR:FXD MOLDED 22UH	99800	1025-052
L9	299237-038		INDUCTOR:FXD MOLDED 5.6UH	99800	1025-038
L10	231105-001		INDUCTOR:VAR .133UH	00752	231105-001
L11	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L12	299237-052		INDUCTOR:FXD MOLDED 22UH	99800	1025-052
Q1	294172	5	TRANSISTOR:NPN SI	04713	MPS918
Q2	294172		TRANSISTOR:NPN SI	04713	MPS918
Q3	294151	2	TRANSISTOR:NPN SI	04713	2N4124
Q4	294172		TRANSISTOR:NPN SI	04713	MPS918
Q5	294172		TRANSISTOR:NPN SI	04713	MPS918
Q6	294172		TRANSISTOR:NPN SI	04713	MPS918
Q7	294151		TRANSISTOR:NPN SI	04713	2N4124
Q8	294153	1	TRANSISTOR:PNP SI	04713	2N4126
R1	299701-622	11	RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R2	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R3	299701-102	4	RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R4	299701-471	3	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R5	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4W	01121	RC07GF622J
R6	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R7	299701-180	2	RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R8	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R9	299701-622	2	RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R10	299701-681	2	RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R11	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R12	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R13	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R14	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4	01121	RC07GF102J
R15	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R16	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R18	299701-471		RESISTOR:FXD CAR 470K -+5% 1/4W	01121	RC07GF471J
R19	299701-101	2	RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R20	299701-101		RESISTOR:FXD CAR 100 OHM -+5% 1/4W	01121	RC07GF101J
R21	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R22	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R23	299701-180		RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R24	299701-622		RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R25	299701-681		RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R26	299701-472	2	RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R27	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R28	299701-161	1	RESISTOR:FXD CAR 160 OHM -+5% 1/4W	01121	RC07GF161J
R29	299701-201	1	RESISTOR:FXD CAR 200 OHM -+5% 1/4W	01121	RC07GF201J
U1	293350	1	IC:TTL DUAL J-K FF	01295	SN74S113N
U2	293496	1	IC:TTL DECADE COUNTER	18324	82S90
	294573		SOCKET:IC 14 PIN	01295	C931410
	294573		SOCKET:IC 14 PIN	01295	C931410

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A11A1	298997-1		PWB ASSY, POWER SUPPLY RECT/FILT	56872	298997-1
C1	293911	4	CAP:FXD AL 2200UF @25 -10%+50%	52763	EG2T/25
C2	293911		CAP:FXD AL 2200UF @25 -10%+50%	52763	EG2T/25
C3	293911		CAP:FXD AL 2200UF @25 -10%+50%	52763	EG2T/25
C4	293911		CAP:FXD AL 2200UF @25 -10%+50%	56763	EG2T/25
C5	293912	4	CAP:FXD AL 2200UF 35/40V -10%+50%	52763	EG2T/40
C6	293912		CAP:FXD AL 2200UF 35/40V -10%+50%	52763	EG2T/40
C7	293912		CAP:FXD AL 2200UF 35/40V -10%+50%	52763	EG2T/40
C8	293912		CAP:FXD AL 2200UF 35/40V -10%+50%	52763	EG2T/40
E1	293050	7	TERMINAL:DISCONNECT	71279	61137-1
F1	294713	2	FUSE: 1AG 32V 7.5AMP	75915	30107-5
F2	294713		FUSE: 1AG 32V 7.5AMP	75915	30107-5
	294930	4	FUSE CLIP PC MOUNT	75915	102068
	294929	4	COMPONENT CLIP	91506	6020-41A
R1		2	RESISTOR: 2K -+5% 1/2W	01121	RC07GF202J
R2			RESISTOR: 2K -+5% 1/2W	01121	RC07GF202J
	294928	4	COMPONENT CLIP	91506	6019-41A
	299258	5	INSULATOR:COMP CLIP	56872	299258
A11A2	298958-1	1	PWB ASSY, +5.2 VOLT REGULATOR	56872	298998-1
C1	293914	3	CAP:FXD AL 200UF -10%+75% 25WVDC	56289	TE1213
C2	293915	1	CAP:FXD CER DISC .02UF -+20% 100WVDC	56289	TGS20
C3	293914		CAP:FXD AL 200UF -10%+75% 25WVDC	56289	TE1213
C4	299250-471	1	CAP:FXD DIP MICA 470PF -+5% 500WVDC	14655	CM06FD471J03
C5	293890	1	CAP:FXD AL 100UF -10%+75% 25WVDC	56289	TE1211
C6	293914		CAP:FXD AL 200UF -10%+75% 25WVDC	56289	TE1213
CR1	293202	2	DIODE RECTIFIER	04713	IN4001
CR2	293202		DIODE RECTIFIER	04713	IN4001
E1	293310	4	TERMINAL SOLDER	71279	1597-2-01
R1	299718	1	RESISTOR:FXD MLD .05 OHM 3% 2W	91637	LVR-2
R2	299701-102	1	RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R3	299702-322	1	RESISTOR:FXD FLM 1K -+5% 1/4W	01121	RN60C1001F
R4	299701-418	1	RESISTOR:FXD FLM 10K -+5% 1/4W	01121	RN60C1002F
R5	299711-359	1	RESISTOR:FXD FLM 2.43K -+1% 1/4W	01121	RN60D2431F
R6	299711-411	1	RESISTOR:FXD FLM 6.04K -+1% 1/4W	01121	RN60D6041F
R7	299701-681	2	RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R8	299701-681		RESISTOR:FXD CAR 680 OHM -+5% 1/4W	01121	RC07GF681J
R9	299701-242	1	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R10	299722	1	RESISTOR:VAR 500 OHM 3/4W	80294	3009P-1-501
R11	299701-391	1	RESISTOR:FXD CAR 390 OHM -+5% 1/4W	01121	RC07GF391J
U1	293502	1	IC:LIN VOLTAGE REGULATOR	04713	MC1723CL
U2	293354	1	IC:LIN OP AMP	72314	UA741TC
	294573	1	IC, 14 PIN	01295	C931410
	294565	1	IC, 8 PIN	01295	C930810
A11A3	299200-1	1	PWB ASSY, -12 VOLT REGULATOR	56872	299200-1
C1	293914	1	CAP:FXD AL 200UF -10%+75% 25WVDC	56289	TE1213
C2	293915	1	CAP:FXD CER DISC .02UF -+20% 100WVDC	56289	TGS20
C3	299250-471	1	CAP:FXD DIP MICA 470PF -+5% 500WVDC	14655	CM06FD471J03
C4	293890	3	CAP:FXD AL 100UF -10%+75% 25WVDC	56289	TE1211
C5	293890		CAP:FXD AL 100UF -10%+75% 25WVDC	56289	TE1211
C6	293890		CAP:FXD AL 100UF -10%+75% 25WVDC	56289	TE1211
CR1	293202	2	DIODE RECTIFIER	04713	IN4001
CR2	293302		DIODE RECTIFIER	04713	IN4001
E1	293310	3	TERMINAL SOLDER	71279	1597-2-01
R1	299718	1	RESISTOR:FXD MLD .05 OHM 3% 2W	91637	LVR-2
R2	299701-102	2	RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R3	299709-322	2	RESISTOR:FXD FLM 1K -+1% 1/4W	01121	RN60C1001F
R4	299709-418	1	RESISTOR:FXD FLM 10K -+1% 1/4W	01121	RN60C1002F
R5	299709-322		RESISTOR:FXD FLM 1K -+1% 1/4W	01121	RN60C1001F
R6	299711-411	1	RESISTOR:FXD FLM 6.0K -+1% 1/4W	01121	RN60D6041F
R7	299701-301	1	RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R8	299701-471	1	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R9	299722	1	RESISTOR:VAR 100 OHM 3/4W	80294	3009P-1-101

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A11A3 R10	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R11	299701-362	1	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R12	299701-180	1	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
U1	293502	1	IC:LIN VOLTAGE REGULATOR	04713	MC1723CL
U2	293354	1	IC:LIN OP AMP	72314	UA741TC
A11A4	298928-1	1	PWB ASSY, POWER SUPPLY MOTHERBD	56872	298928-1
J1	294408-4	3	CONNECTOR:PC EDGE	05574	3VHI0/IJ12
J2	294408-4		CONNECTOR:PC EDGE	05574	3VH10/IJ12
J3	294408-4		CONNECTOR:PC EDGE	05574	3VH10/IJ12
	293079	20	MOD II POST	00779	86091-4
A12	296407-1	1	ASSY REAR PANEL	56872	296407-1
B1	299256-1	1	FAN	28875	MWSZ107FL-7
F1	990019-30	1	FUSE 3 AMP 115V		
	990019-28	1	FUSE 1.5 AMP 230V		
Q1	294175-1	2	TRANSISTOR:	04713	2N6055
Q2	294175-1		TRANSISTOR:	04713	2N6055
T1	299809	1	TRANSFORMER ASSY		
	2994439	1	CONNECTOR, BNC, SWP OUT		UG1094/U-1050
	294695-1	1	LINE RECEPTACLE	05245	6J4
W11	299297011	1	CABLE ASSY COAX		299297-11
W13	299297-13	1	CABLE ASSY COAX		299297-13
W25	299297-25	1	CABLE ASSY COAX		299297-25
	231857	1	CABLE ASSY		231857
A12A1	299261		PWB ASSY, RELAY	56872	299261
E1	293310	4	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
E3	293310		TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
R1	299703-200	1	RESISTOR:FXD CAR 20 OHM –+5% 1/2W	01121	RC32GF200J
A14	299201-1	1	PWB ASSY, LOGIC/POWER DIST	56872	299201-1
C1	293890	2	CAP:FXD AL 100UF -10%+75% 25WVDC	56299	TE1211
C2	293890		CAP:FXD AL 100UF -10%+75% 25WVDC	56289	TE1211
CR1	293203	1	DIODE ZENER 6.2V –+5%	04713	IN753A
CR2	293204	1	DIODE ZENER 15V –+5%	04713	IN30248
CR3	293291	1	DIODE	30992	VZ16DC1
E1	299310	8	TERMINAL SOLDER	71279	1597-2-01
R1	299701-101	4	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R2	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R3	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R4	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R5	299701-681	1	RESISTOR:FXD CAR 600 OHM –+5% 1/4W	01121	RC07GF681J
R6	299701-821	1	RESISTOR:FXD CAR 820 OHM –+5% 1/4W	01121	RC07GF821J
SCR1	294154	2	RECTIFIER, SILICON CONTROLLER	04713	2N4172
SCR2	294154		RECTIFIER, SILICON CONTROLLED	04713	2N4172
	293079		MOD II POST	00779	86091-4
	299932	3	HEADER:DOUBLE F ROW STRAIGHT PDST 26 POS	00779	1-87227-3
	293979	5	STANDOFF	71279	350-1498-10-07
W1	231860	1	FREQUENCY EXTENDER HARNESS ASSY	56872	231860
W15	299297-15	1	CABLE ASSY, COAXIAL	56872	299297-15
W16	299297-16	1	CABLE ASSY, COAXIAL	56872	299297-16
W17	299297-17	1	CABLE ASSY, COAXIAL	56872	299297-17
W19	299297-19	1	CABLE ASSY, COAXIAL	56872	299297-19
W20	299297-20	1	CABLE ASSY, COAXIAL	56872	299297-20
W21	299297-21	1	CABLE ASSY, COAXIAL	56872	299297-21
W22	299297-22	1	CABLE ASSY, COAXIAL	56872	299297-22
A15	296387-1	1	ASSY, MPU	56872	29387-1
W1	296400	1	ASSY CABLE	56872	296400
A15A1	296351-1	1	ASSY MPU PC BOARD	56872	296351-1
C1	299240-5	1	CAP:FXD CER 68UF 15V	05397	T362C686M015AS
C2	299245-1	23	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A15A1 C3	299250-151	1	CAPACITOR, 150PF	14655	CM04FD151J03
C4	299250-391	1	CAPACITOR, 390PF	14655	CM04FA391J03
C5	299240-2	2	CAP:FXD CER 22UF 15V	05397	T368B226M015AS
C6	299240-2	2	CAP:FXD CER 22UF 15V	05397	T368B226M015AS
C7	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C8	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C9	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C10	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C11	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C12	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C13	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C14	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C15	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C16	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C17	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C18	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C19	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C20	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C21	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C22	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C23	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C24	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C25	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C26	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C27	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C28	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
J1	299452-2	1	HEADER:RIGHT ANGLE, 60 PIN	00779	3-87230
J2	299934-5	1	HEADER:STRAIGHT, 40 PIN	00779	2-87227
J3	294213-5	1	HEADER:RIGHT ANGLE, 34 PIN	00779	1-87230-7
L1	299237-44	1	INDUCTOR:FXD MOLDED 10UH	99800	1025-44
R1	299700-103	2	RESISTOR:FXD CAR 10K –+5% 1/8W	01121	RC05GF103J
R2	299701-102	3	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R3	299700-302	2	RESISTOR:FXD CAR 3K –+5% 1/8W	01121	RC05GF302J
R4	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R5	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R6	299700-103		RESISTOR:FXD CAR 10K –+5% 1/8W	01121	RC05GF103J
R7	299700-302		RESISTOR:FXD CAR 3K –+5% 1/8W	01121	RC07GF302J
R8	299700-180	2	RESISTOR:FXD CAR 18 OHM –+5% 1/8W	01121	RC05GF180J
R9	299700-180		RESISTOR:FXD CAR 18 OHM –+5% 1/8W	01121	RC05GF180J
R10	299700-680	1	RESISTOR:FXD CAR 680 OHM –+5% 1/8W	01121	RC05GF680J
U1			NOT USED		
U2	293541-22	1	IC:TRIPLE 3 IN AND GATE	01295	SN74LS11N
U3	296462-1	2	IC:OCTAL TRANSCEIVER	27014	DP8304BN
U4	293438-1	4	IC:QUAD OR GATE	01295	SN74LS32N
U5	294461-3	4	IC:OCTAL LATCH	01295	MM74C374N
U6	296463-1	1	IC:E PROM 4K, X8		
U7	296465-1	2	IC:RAM 1K, X4		P2114L
U8	294461-3		IC:OCTAL LATCH	01295	MM74C374N
U9	296465-1		IC:RAM 1K, X4		P2114L
U10	294461-3		IC:OCTAL LATCH	01295	MM74C374N
U11	294463-2		IC:E PROM 4K, X8		
U12	294035-3		IC:INTER CONT	04713	[illegible]328[illegible]
U13	294461-3		IC:OCTAL LATCH	01295	MM74C374N
U14			NOT USED		
U15	293438-1		IC:QUAD OR GATE	01295	SN74LS32N
U16	296467-1	1	IC:HEX BUFFER	01295	SN74LS365N
U17	294035-5	1	IC:DMA CONTROLLER	04713	MC6844P
U18	296462-1		IC:OCTAL TRANSCEIVER	04713	DP8304BN
U19	293540-1	2	IC:HEX INVERTER	01295	SN74LS04N-00
U20	296464-1	1	IC:MICROPROCESSOR	04713	MC6800P
U21	293540-1		IC:HEX INVERTER	01295	SN74LS04N-00
U22	293541-24	2	IC:3 TO 8 DECODER	01295	SN74LS138N
U23	293633-1	1	IC:J-K FLIP-FLOP	01295	SN74S112N
U24	293541-24		IC:3 TO 8 DECODER	01295	SN74LS138N

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A15A1 U25	293437-1	1	IC:QUAD AND GATE	01295	SN74LS08N
U26	294035-4	1	IC:CLOCK GENERATOR	04713	MC6875P
U27	293438-1		IC:QUAD OR GATE	01295	SN74LS32N
U28	293438-1		IC:QUAD OR GATE	01295	SN74LS32N
U29	296466-1	1	IC:QUAD OR GATE	01295	SN74S32N
XU6	294566-1	4	IC:SOCKET, 24 PIN	01295	C932410
XU7	294574-1	2	IC:SOCKET, 18 PIN	01295	C931810
XU9	294574-1		IC:SOCKET, 18 PIN	01295	C931810
XU11	294566-1		IC:SOCKET, 24 PIN	01295	C932410
XU12	294566-1		IC:SOCKET, 24 PIN	01295	C932410
XU14	294566-1		IC:SOCKET, 24 PIN	01295	C923410
XU17	296468-1	2	IC:SOCKET, 40 PIN	01295	C934010
XU18	294515-1	1	IC:SOCKET, 20 PIN	01295	C932010
XU20	296468-1		IC:SOCKET, 40 PIN	01295	C934010
XU26	294567-1	1	IC:SOCKET, 16 PIN	01295	C931610
	296542-1	1	1 MHZ CLOCK OSCILLATOR, PC BOARD	56872	296542-1
C1	299240-2	1	CAPACITOR, 22UF, 15V	05397	T368B226M015AS
C2	293294-2	2	CAPACITOR, 0.1UF	71590	CY20104P
C3	299240-5	1	CAPACITOR, 68UF, 15V	05397	T362C686M015AS
C4	293294-2		CAPACITOR, 0.1UF	71590	CY20104P
C5	299250-101	1	CAPACITOR, 100PF	14655	CM04FD101J03
CR1	293240-1	1	DIODE		IN914
P1	296454-1	1	SOCKET, ADAPTER	91506	616AG16
R1	299700-102	1	RESISTOR, 1K, 1/8W, 5%	01121	RC05GF102J
R2	299700-394	1	RESISTOR, 390K, 1/8W, 5%	01121	RC05GF394J
R3	299700-180	1	RESISTOR, 18 OHM, 1/8W, 5%	01121	RC05GF180J
U1	296538-1	1	IC, CLOCK	01295	6871B4FC
U2	293536-1	1	IC, FLIP FLOP	01295	74LS74N
U3	293541-25	1	QUAD POS NAND	01295	74LS132
U4	293540-1	1	HEX INVERTER	01295	74LS04
U5	293466-1	1	MONOSTABLE VIBRATOR	01295	74121N
A15A2	296353-1	1	ASSY LATCH PC BOARD	56872	296353-1
C1	296458-1	1	CAP:FXD .01 UF 5% 400V	14655	CMK.01
C2	299245-2	1	CAP:FXD .01UF 20%	72982	8121-M058-651-103M
C3	299250-15	1	CAP:FXD 150PF	14655	CM04FD151J03
C4	299245-1	19	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C5	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C6	299240-5	2	CAP:FXD 68UF 15V	05397	T362C686M015AS
C7	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C8	299240-2	1	CAP:FXD CER 22UF 15V	05397	T368B226M015AS
C9	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C10	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C11	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C12	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C13	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C14	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C15	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C16	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C17	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C18	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C19	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C20	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C21	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C22	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C23	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C24	299240-5		CAP:FXD CER 68UF 15V	05397	T362C686M015AS
C25	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
J1	2994452-2	1	HEADER:RIGHT ANGLE 60 PIN	00779	3-87230-0
J2	299934-4	1	HEADER:STRAIGHT 50 PIN	00779	2-87227-5
J3	294213-2	1	HEADER:RIGHT ANGLE 6 PIN	00779	87230-3
L1	299237-44	2	INDUCTOR:FXD MOLDED 10UH	99800	1025-44
L2	299237-44		INDUCTOR:FXD MOLDED 10UH	99800	1025-44

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A15A2 R1	299700-472	1	RESISTOR:FXD CAR 4.7K –+5% 1/8W	01121	RC07GF472J
R2	299707-383	1	RESISTOR:FXD FLM 4.42K 1% 1/10W	01121	RN55C4421F
R3	299707-534	1	RESISTOR:FXD FLM 143K 1% 1/10W	01121	RN55C1433F
R4	299707-367	1	RESISTOR:FXD FLM 3.01K 1% 1/10W	01121	RN55C3011F
R5	29907-321	1	RESISTOR:FXD FLM 1K 1% 1/10W	01121	RN55C1001F
R6	299745-10	1	RESISTOR:VARIABLE 10K	80294	3009P-1-103
TP1	293310-1	1	TERMINAL SOLDER	71279	1597-2-01
U1	294461-3	12	IC:OCTAL LATCH	04713	MM74C374N
U2	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U3	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U4	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U5	294066-1	1	IC:OPNL AMPL	75915	LF356N
U6	293536-1	1	IC:DUAL D FLIP-FLOP	01295	SN74LS74N-00
U7	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U8	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U9	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U10	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U11	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U12	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U13	293545-1		IC:QUAD NOR GATE	01295	SN74LS02N-00
U14	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U15	294461-3		IC:OCTAL LATCH	04713	MM74C374N
U16	293541-23		IC:OCTAL LATCH	01295	SN74LS373N
U17	294461-2		IC:HEX LATCH	04713	MM74C174N
U18	293691-6	2	IC:RES NTWK 10K	01121	316A103
U19	294461-2		IC:HEX LATCH	04713	MM74C174N
U20	296457-1	1	IC:TIMER		NE555
U21	294461-4	1	IC:HEX INVERTER	04713	MM74C901N
U22	294461-5	1	IC:4-16 DECODER	04713	MM74C154N
U23	294593-2	1	IC:D TO A CONVERTER		AD7520LN
U24	293541-7	1	IC:TRI STATE BUFFER	01295	SN74LS244N
U25	293691-6		IC:RES NTWK 10K	01121	316A103
XJ23	294567-1	1	IC:SOCKET 16 PIN	01295	C931610
A15A3	296355-1	1	ASSY GPIB PC BOARD	56872	296355-1
C1	299240-2	1	CAP:FXD CER 22UF 15V	05397	T368B226M015AS
C2	299245-1	17	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C3	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C4	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C5	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C6	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C7	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C8	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C9	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C10	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C11	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C12	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C13	299245-1		CAP:FXD CER 1.UF –+20% 50WVDC	72982	8121-050-651-104M
C14	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C15	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C16	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C17	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
C18	299245-1		CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-050-651-104M
J1	294452-2	1	HEADER:RIGHT ANGLE 60 PIN	00779	3-87230-0
J2	294453-1	1	CONNECTOR:STRAIGHT 24 PIN	00779	552224-1
S1	296471-1	1	SWITCH:DIP 5 POSITION	81073	76PSB05
S2	296471-2	1	SWITCH:DIP 8 POSITION	81073	76PSB08
U1	296469-1	4	IC:QUAD TRANCEIVER	04713	MC3448A
U2	296469-1		IC:QUAD TRANCEIVER	04713	MC3448A
U3	293540-1		IC:HEX INVERTER	01295	SN74LS04N-00
U4			NOT USED		
U5	296467-1	1	IC:HEX BUFFER	01295	SN74LS365N
U6	293437-1	2	IC:QUAD AND GATE	01295	SN74LS08N
U7	296469-1		IC:QUAD TRANCEIVER	04713	MC3448A

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A15A3 U8	296469-1		IC:QUAD TRANCEIVER	04713	MC3448A
U9	296462-1	1	IC:OCTAL TRANCEIVER	27014	DP8304BN
U10	293545-1	1	IC:QUAD NOR GATE	01295	SN74LS02N-00
U11	293438-1	1	IC:QUAD OR GATE	01295	SN74LS32N
U12	293536-1	1	IC:DUAL D FLIP-FLOP	01295	SN74LS74N-00
U13	296470-1	1	IC:GPIB ADAPTER	04713	MC68488P
U14	293533-1	1	IC:TRIPLE 3 IN NAND	01295	SN74LS10N
U15	293437-1		IC:QUAD AND GATE	01295	SN74LS08N
U16	293691-6	1	IC:RESISTOR NTWK 10K	01121	316A103
U17	293541-7	1	IC:OCTAL BUFFER	01295	SN74LS244N
XJ13	296468-1	1	SOCKET:IC 40 PIN	01295	C934010
A15PS1	296428-1	1	ASSY POWER SUPPLY	56872	296428-1
C1		1	CAPACITOR, 12000UF 20V		
C2		1	CAPACITOR, 220UF, 25V		
C3		1	CAPACITOR, 01UF, 80V		
C4		1	CAPACITOR, 47UF, 10V		
C5		1	CAPACITOR, 22UF, 25V		
CR1		5	DIODES		IN4004
CR2			DIODES		IN4004
CR3			DIODES		IN4004
CR4			DIODES		IN4004
CR5		1	DIODES		IN751A
CR6		1	DIODES		S2006LS3
CR7			DIODE		IN4004
Q1		1	TRANSISTOR		2N5294
Q2		1	TRANSISTOR		2N3055
R1		1	RESISTOR, 47 OHM 1/4W		
R2		1	RESISTOR, 22K, 1/4W		
R3		1	RESISTOR, 1K, 1/4W		
R4		1	SELECT AT TEST		
R5		1	RESISTOR, 0.1 OHM 3W		
R6		2	RESISTOR, 4.7K, 1/4W		
R7			RESISTOR, 4.7K, 1/4W		
R8		1	RESISTOR, VARIABLE, 1K		PT10H
R9	299709-315	1	RESISTOR, 866 OHM, 1/8W 1%		RN60C8660F
R10	299709-368	1	RESISTOR, 3.01K, 1/8W, 1%		RN60D3011F
R11		1	RESISTOR, 220 OHM, 1/4W		
R12		1	RESISTOR, 680 OHM		
T1		1	TRANSFORMER		281-1038
U1		1	INTEGRATED CIRCUIT		723HC
A6	298938		ASSY, 24-33 MHZ OSCILLATOR	56872	298938
FL1	293609	6	FILTER CONNECTOR	72982	1201-066
FL2	293609		FILTER CONNECTOR	72982	1201-066
FL3	293609		FILTER CONNECTOR	72982	1201-066
FL4	293609		FILTER CONNECTOR	72982	1201-066
FL5	293609		FILTER CONNECTOR	72982	1201-066
FL6	293609		FILTER CONNECTOR	72982	1201-066
J1	299233	1	CONNECTOR:ST BULKHEAD	98291	51-045-0000
J2	299233		CONNECTOR:ST BULKHEAD	98291	51-045-0000
A6A1	298949		PWB ASSY, 24-33 OSCILLATOR	56872	298949
C1	299253-001	7	CAP:FXD POR 2.7PF –+0.5PF 500WVDC	95275	VY10CA2R7DER
C2	299241-430	10	CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C3	293593	10	CAP:VAR TRMR 2-8PF	91293	8090
C4	299250-391	10	CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C5	299253-001		CAP:FXD POR 2.7PF –+5% 500WVDC	75275	VY10CA2R7DER
C6	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C7	293593		CAP:VAR TRMR 2-8PF	91293	8090
C8	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C9	299253-001		CAP:FXD POR 2.7PF –+0.5PF 500WVDC	95275	VY10CA2R7DER
C10	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A6A1 C11	293593		CAP:VAR TRMR 2-8PF	91293	8090
C12	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C13	299253-001		CAP:FXD POR 2.7PF –+0.5PF 500WVDC	95275	VY10CA2R7DER
C14	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C15	293593		CAP:VAR TRMR 2-8PF	91293	8090
C16	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C17	299253-002	3	CAP:FXD POR 8.2PF –+5% 500WVDC	95275	VY10A8R2J3
C18	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C19	293593		CAP:VAR TRMR 2-8PF	91293	8090
C20	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C21	299253-001		CAP:FXD POR 2.7PF –+0.5PF 500WVDC	95275	VY10CA2R7DER
C22	299241-430		CAP:FSC DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C23	293593		CAP:VAR TRMR 2-8PF	91293	8090
C24	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C25	299253-001		CAP:FXD POR 2.7PF –+0.5PF 500WVDC	95275	VY10CA2R7DER
C26	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C27	293593		CAP:VAR TRMR 2-8PF	91293	8090
C28	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C29	299253-001		CAP:FXD POR 2.7PF –+0.5PF 500WVDC	95275	VY10CA2R7DER
C30	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C31	293593		CAP:VAR TRMR 2-8PF	91293	8090
C32	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C33	299255-002		CAP:FXD POR 8.2PF –+5% 500WVDC	95275	VY10A8R2JE
C34	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C35	293593		CAP:VAR TRMR 2-8PF	91293	8090
C36	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C37	299253-002		CAP:FXD POR 8.2PF –+5% 500WVDC	95275	VY10A8R2JE
C38	299241-430		CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C39	293593		CAP:VAR TRMS 2-8PF	91293	8090
C40	299250-391		CAP:FXD DIP MICA 390PF –+5% 100WVDC	14655	CM04FA391J03
C41	299243-001	11	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C42	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C43	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C44	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C45	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C46	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C48	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C49	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C50	299240-001	2	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C51	299240-001		CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C52	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C53	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C54	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
E1	293305	6	TERMINAL:DISCONNECT	71279	1019-2-901
E2	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E3	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E4	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E5	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E6	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E7	293301	4	TERMINAL SOLDER	71279	1597-2-01
E8	293310		TERMINAL SOLDER	71279	1597-2-01
E9	293310		TERMINAL SOLDER	71279	1597-2-01
E10	293310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-028	8	INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L2	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L3	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L4	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L5	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L6	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L7	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L8	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
Q1	294172	11	TRANSISTOR:NPN SI	04713	MPS918
Q2	294153	10	TRANSISTOR:PNP SI	04713	2N4126
Q3	294172		TRANSISTOR:NPN SI	04713	MPS918

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A6A1 Q4	294153		TRANSISTOR:PNP SI	04713	2N4126
Q5	294172		TRANSISTOR:NPN SI	04713	MPS918
Q6	294153		TRANSISTOR:PNP SI	04713	2N4126
Q7	294172		TRANSISTOR:NPN SI	04713	MPS918
Q8	294153		TRANSISTOR:PNP SI	04713	2N4126
Q9	294172		TRANSISTOR:NPN SI	04713	MPS918
Q10	294153		TRANSISTOR:PNP SI	04713	2N4126
Q11	294172		TRANSISTOR:NPN SI	04713	MPS918
Q12	294153		TRANSISTOR:PNP SI	04713	2N4126
Q13	294172		TRANSISTOR:NPN SI	04713	MPS918
Q14	294153		TRANSISTOR:PNP SI	04713	2N4126
Q15	294172		TRANSISTOR:NPN SI	04713	MPS918
Q16	294153		TRANSISTOR:PNP SI	04713	2N4126
Q17	294172		TRANSISTOR:NPN SI	04713	MPS918
Q18	294153		TRANSISTOR:PNP SI	04713	2N4126
Q19	294172		TRANSISTOR:NPN SI	04713	MPS918
Q20	294153		TRANSISTOR:PNP SI	04713	2N4126
Q21	294172		TRANSISTOR:NPN SI	04713	MPS918
R1	299701-203	10	RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GR203J
R2	299701-472	13	RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R3	299701-822	19	RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R4	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R5	299701-152	10	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R6	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R7	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R8	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R9	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R10	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R11	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R12	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R13	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R14	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R15	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R16	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R17	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R18	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R19	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R20	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R21	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R22	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R23	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R24	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R25	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R26	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R27	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R28	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R29	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R30	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R31	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R32	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R33	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R34	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R35	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R36	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R37	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R38	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R39	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R40	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R41	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J
R42	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R43	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R44	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R45	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R46	299701-203		RESISTOR:FXD CAR 20K –+5% 1/4W	01121	RC07GF203J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A6A1 R47	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R48	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R49	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R50	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R51	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R52	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R53	299701-301	2	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R55	299701-301		RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R56	299701-470	1	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
U1	299501	1	IC:TTL 4 TO 10 LINE DECODER	01295	SN7442N
U2	2993350	1	IC:TTL DUAL J-K FF	01295	SN74S113N
	294567	1	SOCKET:IC 16 PIN	01295	C931610
	294573	1	SOCKET:IC 14 PIN	01295	C931410
A6A2	298952		PWB ASSY, ISOL/DIVIDER	56872	298952
C1	299243-001	4	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C2	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C4	299241-360	2	CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C5	299250-241	2	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C6	299241-200	1	CAP:FXD DIP MICA 20PF –+5% 300WVDC	72136	DM5-200J
C7	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C8	299241-360		CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C9	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
E1	293310	3	TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
E5	293310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-042	1	INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L2	299237-020	2	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L3	299237-000	2	INDUCTOR:FXD MOLDED .15UH	99800	1025-00
L4	299237-024	1	INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L5	299237-000		INDUCTOR:FXD MOLDED .15UH	99800	1025-00
L6	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
Q1	294172	1	TRANSISTOR:NPN SI	04713	MPS918
R1	299701-680	1	RESISTOR:FXD CAR 69 OHM –+5% 1/4W	01121	RC07GF680J
R2	299701-101	4	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R3	299701-470	1	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R4	299701-472	2	RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R5	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R6	299701-471	1	RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R7	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R8	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R9	299701-390	1	RESISTOR:FXD CAR 39 OHM –+5% 1/4W	01121	RC07GF390J
R10	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
	294550	8	SOCKET:PIN	00779	1-331677-8
	293305	2	TERMINAL	71279	1019-2-901
A6A3	298953		PWB ASSY, ISOL/AMPL	56872	298953
C1	299243-001	7	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C2	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C4	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C5	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C7	299241-360	2	CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C8	299250-241	2	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C9	299241-200	1	CAP:FXD DIP MICA 20PF –+5% 300WVDC	72136	DM5-200J
C10	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C11	299241-360		CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C12	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
E1	293310	2	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
L2	299237-048	1	INDUCTOR:FXD MOLDED 15 UH	99800	1025-048

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A6A3 L3	299237-020	2	INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
L4	299237-000	2	INDUCTOR:FXD MOLDED .15UH	99800	1025-00
L5	299237-024	1	INDUCTOR:FXD MOLDED 1.5UH	99800	1025-024
L6	299237-000		INDUCTOR:FXD MOLDED .15UH	99800	1025-00
L7	299237-020		INDUCTOR:FXD MOLDED 1.00UH	99800	1025-020
Q1	294172	2	TRANSISTOR:NPN SI	04713	MPS918
Q2	294172		TRANSISTOR:NPN SI	04713	MPS918
R1	299701-680	1	RESISTOR:FXD CAR 68 OHM –+5% 1/4W	01121	RC07GF680J
R2	299701-470	1	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R3	299701-472	2	RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R4	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R5	299701-301	2	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R6	299701-101	2	RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R7	299701-622	1	RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R8	299701-362	1	RESISTOR:FXD CAR 3.6K –+5% 1/4W	01121	RC07GF362J
R9	299701-101		RESISTOR:FXD CAR 100 OHM –+5% 1/4W	01121	RC07GF101J
R10	299701-201	1	RESISTOR:FXD CAR 200 OHM –+5% 1/4W	01121	RC07GF201J
R11	299701-180	11	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R12	299701-301		RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
A7	298961		ASSY, 366 FILTER AMP	56872	298961
C1	295374	4	CAPAC: 3-10PF	91293	5201
C2	295374		CAPAC; 3-10PF	91293	5201
C3	295374		CAPAC; 3-10PF	91293	5201
C4	295374		CAPAC: 3-10PF	91293	5201
FL1	293608	1	FILTER CONNECTOR	72982	1201-066
J1	299233	4	CONNECTOR:ST BULKHEAD	98291	51-045-0000
J2	299233		CONNECTOR:ST BULKHEAD	98291	51-045-0000
J3	299233		CONNECTOR:ST BULKHEAD	98291	51-045-0000
J4	299233		CONNECTOR:ST BULKHEAD	98291	51-045-0000
A7A1	298962		PWB ASSY, 366 FILT/CONV INPUT	56872	29862
C1	293746	2	CAP:VAR TRMR CER 5.5, 18PF	72982	538-011-A-5.5-18
C2	299243-001	6	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C4	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C5	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C7	293756		CAP:VAR TRMR CER 5.5, 18PF	72982	538-011-A-5.5-18
C8	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
E2	293310	1	TERMINAL, SOLDER	71279	1597-2-01
L1	299237-12	2	INDUCTOR, .47UH	99800	1025-012
L2	299237-12		INDUCTOR, .47UH	99800	1025-012
M1	231104-1	1	MIXER	15542	SRA-1
Q1	299239-1	2	TRANSISTOR	04713	2N5179
Q2	299239-1		TRANSISTOR	04713	2N5179
R1	299701-180	2	RESISTOR, 18 OHM, 1/4W	01121	RC07GF180J
R2	299701-622	4	RESISTOR, 6.2K, 1/4W	01121	RC07GF622J
R3	299701-622		RESISTOR, 6.2K, 1/4W	01121	RC07GF622J
R4	299701-471	2	RESISTOR, 470 OHM, 1/4W	01121	RC07GF471J
R5	299701-471		RESISTOR, 470 OHM, 1/4W	01121	RC07GF471J
R6	299701-622		RESISTOR, 6.2K, 1/4W	01121	RC07GF622J
R7	299701-622		RESISTOR, 6.2K, 1/4W	01121	RC07GF622J
R8	299701-102	1	RESISTOR, 1K, 1/4W	01121	RC07GF102J
R9	299701-180		RESISTOR, 18 OHM, 1/4W	01121	RC07GF180J
R10	299701-161	1	RESISTOR, 160 OHM, 1/4W	01121	RC07GF161J
	294550	8	SOCKET	00779	1-331677-8
	293994	1	TRANSIPAD	07047	10105-DAP
A7A2	298963		PWB ASSY, 366 FILT/CONV OUTPUT	56872	298963
C9	299243-001	7	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C10	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C11	299240-001	1	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C12	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A7A2 C13	293746	2	CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C14	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C15	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C16	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C17	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C18	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C19	299250-471	2	CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C20	299250-241	2	CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C21	299250-241		CAP:FXD DIP MICA 240PF –+5% 500WVDC	14655	CM04FD241J03
C22	299250-471		CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
E2	293310		TERMINAL SOLDER	71279	1597-2-01
E3	293310		TERMINAL SOLDER	71279	1597-2-01
L3	299237-012	4	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L4	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L5	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L6	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L7	231105-001	2	INDUCTOR:VAR .133UH	00752	231105-001
L8	299237-026	1	INDUCTOR:FXD MOLDED 1.8UH	99800	1025-026
L9	231105-001		INDUCTOR:VAR .133UH	00752	231105-001
M2	231104-002	1	MIXER	21912	MD108
Q8	299239-001	2	TRANSISTOR:NPN SI	04713	2N5179
Q4	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R11	299701-180	1	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R12	299701-622	4	RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R13	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R14	299701-102	2	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R15	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R16	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R17	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
	294550		SOCKET:PIN	00779	1-331677-8
A8	298954		ASSY, 361-370 FILTER AMP	56872	298954
C1	295374	7	CAPAC: 3-10PF	91293	5201
C2	295374		CAPAC: 3-10PF	91293	5201
C3	295374		CAPAC: 3-10PF	91293	5201
C4	295374		CAPAC: 3-10PF	91293	5201
C5	295374		CAPAC; 3-10PF	91293	5201
C6	295374		CAPAC: 3-10PF	91293	5201
C7	295374		CAPAC: 3-10PF	91293	5201
FL1	293608	1	FILTER CONNECTOR 72982	72982	1201-066
J1	299234	1	CONNECTOR:RT ANGLE BULKHEAD	98291	51-047-0000
J2	299233	1	CONNECTOR:ST BULKHEAD	98291	51-045-0000
A8A1	298955		PWB ASSY, 361-370 INPUT	56872	298955
C1	293746	2	CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C2	299243-001	4	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C4	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C5	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
E2	293310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-012	2	INDUCTOR, .47UH	99800	1025-012
L2	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
Q1	299239-001	1	TRANSISTOR:NPN SI	04713	2N5179
R1	299701-180	2	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R2	299701-161	1	RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R3	299701-622	2	RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R4	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R5	299701-102	1	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R6	299701-180		RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
A8A2	298956		PWB ASSY, 361-370 OUTPUT	56872	298956
C1	299243-001	7	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C2	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A8A2 C4	293746	2	CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C5	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C6	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C7	298746		CAP:FXD TRMR CER 2-8PF	72982	538-011-A-2-8
C8	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C9	299241-080	2	CAP:FXD DIP MICA 8PF −+0.5PF 300WVDC	72136	DM5-080D
C10	299241-080		CAP:FXD DIP MICA 8PF −+0.5PF 300WVDC	72136	DM5-080D
C11	299240-001	1	CAP:FXD DIP TA 6.8UF −+20% 35WVDC	05397	T368B685M035AS
C12	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
E2	293310	1	TERMINAL SOLDER	71279	1597-2-01
L1	299237-012	4	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L2	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L3	298899-002	3	INDUCTOR:FXD .04UH	56872	298899-002
L4	298899-002		INDUCTOR:FXD .04UH	56872	298899-002
L5	298899-002		INDUCTOR:FXD .04UH	56872	298899-002
L6	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L7	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
Q1	299239-001	2	TRANSISTOR:NPN SI	04713	2N5179
Q2	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-180	2	RESISTOR:FXD CAR 18 OHM −+5% 1/4W	01121	RC07GF180J
R2	299701-622	4	RESISTOR:FXD CAR 6.2K −+5% 1/4W	01121	RC07GF622J
R3	299701-622		RESISTOR:FXD CAR 6.2K −+5% 1/4W	01121	RC07GF622J
R4	299701-242	1	RESISTOR:FXD CAR 2.4K −+5% 1/4W	01121	RC07GF242J
R5	299701-622		RESISTOR:FXD CAR 6.2K −+5% 1/4W	01121	RC07GF622J
R6	299701-622		RESISTOR:FXD CAR 6.2K −+5% 1/4W	01121	RC07GF622J
R7	299701-362	1	RESISTOR:FXD CAR 3.6K −+5% 1/4W	01121	RC07GF362J
R8	299701-180		RESISTOR:FXD CAR 18 OHM −+5% 1/4W	01121	RC07GF180J
R9	299701-301	1	RESISTOR:FXD CAR 300 OHM −+5% 1/4W	01121	RC07GF301J
A8A3	298957	1	ASSY, 361-370 MHZ CAPACITOR BOARD	56872	298957
A9	298959		ASSY, 330-331 MHS FILTER AMP	56872	298959
C1	285374	5	CAPAC: 3-10PF	91293	5201
C2	295374		CAPAC: 3-10PF	91293	5201
C3	295374		CAPAC: 3-10PF	91293	5201
C4	295374		CAPAC: 3-10PF	91293	5201
C5	295374		CAPAC: 3-10PF	91293	5201
FL1	293608		FILTER CONNECTOR	72982	1201-066
J1	299233	2	CONNECTOR:ST BULKHEAD	98291	51-045-0000
J2	299233		CONNECTOR:ST BULKHEAD	98291	51-045-0000
A9A1	298965		PWB ASSY, 330-331 FILT/AMP INPUT	56872	298965
C1	293746	2	CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C2	299243-001	4	CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C4	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C5	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C6	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
E2	293305	1	TERMINAL:DISCONNECT	71279	1019-2-901
L1	299237-012	2	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L2	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
Q1	299239-1	1	TRANSISTOR	04713	2N5179
R1	299701-822	2	RESISTOR:FXD CAR 8.2K −+5% 1/4W	01121	RC07GF822J
R2	299701-822		RESISTOR:FXD CAR 8.2K −+5% 1/4W	01121	RC07GF822J
R3	299701-102	1	RESISTOR:FXD CAR 1K −+5% 1/4W	01121	RC07GF102J
R4	299701-180	1	RESISTOR:FXD CAR 28 OHM −+5% 1/4W	01121	RC07GF180J
A9A2	298966		PWB ASSY, 330-331 FILT/AMP OUTPUT	56872	298966
C1	299243-001	7	CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C2	293746	2	CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C3	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C4	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C5	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M
C6	299243-001		CAP:FXD CER DISC .001UF −+20% 1000WVDC	56289	CK60AW102M

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A9A2 C7	29376		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C8	299240-001	1	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C9	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C10	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
E2	293305	2	TERMINAL:DISCONNECT	71279	1019-2-901
E3	293305		TERMINAL, SOLDER	71279	1019-2-901
L1	299237-012	4	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L2	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L3	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L4	299236-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
Q1	299239-001	2	TRANSISTOR:NPN SI	04713	2N5179
Q2	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-180	1	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R2	299701-822	4	RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R3	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R4	299701-822	1	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R5	299701-822		RESISTOR:FXD CAR 8.2K -0+5% 1/4W	01121	RC07GF822J
R6	299701-822		RESISTOR:FXD CAR 8.2K –+5% 1/4W	01121	RC07GF822J
R7	299701-102	1	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
A10	298937		ASSY, HIGH FREQ SECTION	56872	298937
FL1	293608	9	FILTER CONNECTOR	72982	1201-066
FL2	293608		FILTER CONNECTOR	72982	1201-066
FL3	293608		FILTER CONNECTOR	72982	1201-066
FL4	293608		FILTER CONNECTOR	72982	1201-066
FL5	293608		FILTER CONNECTOR	72982	1201-066
FL7	293608		FILTER CONNECTOR	72982	1201-066
FL8	293608		FILTER CONNECTOR	72982	1201-066
FL9	293608		FILTER CONNECTOR	72982	1201-066
FL10	299608		FILTER CONNECTOR	72982	1201-066
J1	299233	1	CONNECTOR:ST BULKHEAD	98291	51-045-0000
J2	299234	5	CONNECTOR:RT ANGLE BULKHEAD	98291	51-047-0000
J3	299234		CONNECTOR:RT ANGLE BULKHEAD	98291	51-047-0000
J4	299234		CONNECTOR:RT ANGLE BULKHEAD	98291	51-047-0000
J5	299234		CONNECTOR:RT ANGLE BULKHEAD	98291	51-047-0000
J6	299234		CONNECTOR:RT ANGLE BULKHEAD	98291	51-047-0000
A10A1	298995		PWB ASSY, 337 OSC/ISOL	56872	298995
C1	293746	7	CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C2	299241-360	1	CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C3	299241-101	1	CAP:FXD DIP MICA 100PF –+5% 300WVDC	72136	DM5-101J
C4	299243-001	19	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C5	299241-050	3	CAP:FXD DIP MICA 5PF –+0.5PF 300WVDC	72136	DM5-0500
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C7	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C8	299241-120	1	CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-120J
C9	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C10	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C11	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C12	299243-001		CAP:FXD CER DISC .001UF –+29% 1000WVDC	56289	CK60AW102M
C13	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C14	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C15	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C16	299241-430	1	CAP:FXD DIP MICA 43PF –+5% 300WVDC	72136	DM5-430J
C17	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C18	299241-080	6	CAP:FXD DIP MICA 8PF –+0.5PF 300WVDC	72136	DM5-080D
C19	299241-080		CAP:FXD DIP MICA 8PF –+0.5PF 300WVDC	72136	DM5-080D
C20	299241-080		CAP:FXD DIP MICA 8PF –+0.5PF 300WVDC	72136	DM5-080D
C21	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C22	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C23	299743-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C24	299743-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C25	299743-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C26	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A10A1 C27	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C28	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C29	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C30	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C31	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C32	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C33	299243-001		CAP:FXD CER DISC .001UF -+20% 1000WVDC	56289	CK60AW102M
C34	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C36	299241-100	2	CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC 10	72136	DM5-100J
C37	299241-080		CAP:FXD DIP MICA 8PF -+0.5PF 300WVDC	72136	DM5-080D
C38	299241-050		CAP:FXD DIP MICA 5PF -+0.5PF 300WVDC	72136	DM5-050D
C39	299241-050		CAP:FXD DIP MICA 5PF -+0.5PF 300WVDC	72136	DM5-050D
C40	299241-080		CAP:FXD DIP MICA 8PF -+0.5PF 300WVDC	72136	DM5-080D
C41	299241-00		CAP:FXD DIP MICA 10PF -+0.5PF 300WVDC	72136	DM5-100J
C42	299241-080		CAP:FXD DIP MICA 8PF -+0.5PF 300WVDC	72136	DM5-080D
E1	293310		TERMINAL SOLDER	71279	1597-2-01
E3	293310		TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71297	1597-2-01
L1	299237-012	8	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L2	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L3	299257-001	1	INDUCTOR:FXD .133UH	56872	299257-001
L4	299257-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L6	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L7	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L8	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L9	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L10	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L23	299237-008	1	INDUCTOR:FXD MOLDED .33UH	99800	1025-08
Q1	299239-001	8	TRANSISTOR:NPN SI	04713	2N5179
Q2	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q3	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q4	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q5	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q6	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q7	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q8	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-622	1	RESISTOR:FXD CAR 6.2K -+5% 1/4	01121	RC07GF622J
R2	299701-362	1	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R3	299701-471	6	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R4	299701-472	12	RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R5	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R6	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R7	299701-103		RESISTOR:FXD CAR 10K -+5% 1/4W	01121	RC07GF103J
R8	299701-821	1	RESISTOR:FXD CAR 820 OHM -+5% 1/4W	01121	RC07GF821J
R9	299701-180	1	RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R10	299701-472	2	RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R11	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R12	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R14	299701-470	6	RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R15	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R16	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R17	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R18	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R20	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R21	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R22	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R23	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R24	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R26	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R27	299701-470		RESISTOR:FXD CAR 47 OHM -+5% 1/4W	01121	RC07GF470J
R28	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R29	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R30	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A10A1 R31	299701-180		RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R32	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R33	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R34	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
Y1	298315	1	CRYSTAL:112.333 MHZ	56872	298315
A10A2	298996		PWB ASSY, 337 ISOL	56872	298996
C1	299243-001	12	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C2	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C4	299241-050	4	CAP:FXD DIP MICA 5PF –+0.5PF 300WVDC	72136	CM5-050D
C5	293910	4	CAP:VAR TRMR CER 2-8PF	72982	538-037-A-2-8
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C7	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C8	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C9	293910		CAP:VAR TRMR CER 2-8PF	72982	538-037-A-2-8
C10	299241-050		CAP:FXD DIP MICA 5PF –+0.5PF 300WVDC	72136	DM5-050D
C11	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C12	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C13	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C14	299241-080	2	CAP:FXD DIP MICA 8PF –+0.5PF 300WVDC	72136	CM5-080D
C15	283910		CAP:VAR TRMR CER 2-8PF	72982	538-037-A-2-8
C16	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C17	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C18	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C19	299241-120	1	CAP:FXD DIP MICA 12PF –+5% 300WVDC	72136	DM5-170J
C20	293910		CAP:VAR TRMR CER 2-8PF	72982	538-037-A-2-8
C21	299240-001		CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C22	299245-002	1	CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-03M
C23	299241-080		CAP:FXD DIP MICA 8PF –+0.5PF 300WVDC	72136	DM5-080D
C24	299241-050		CAP:FXD DIP MICA 5PF –+0.5PF 300WVDC	72136	DM5-050D
C25	299241-050		CAP:FXD DIP MICA 5PF –+0.5PF 300WVDC	72136	DM5-050D
E1	293310	3	TERMINAL SOLDER	71279	1597-2-01
E2	293310		TERMINAL SOLDER	71279	1597-2-01
E5	293310		TERMINAL SOLDER	71279	1597-2-01
L1	299237-012	5	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L2	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L3	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L4	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L5	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
M1	231104-001	1	MIXER	15542	SRA-1
Q1	299239-001	4	TRANSISTOR:NPN SI	04713	2N5179
Q2	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q3	299239-001		TRANSISTOR:NPN SI	04713	2N5179
Q4	299239-001		TRANSISTOR:NPN SI	04713	2N5179
R1	299701-470	6	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R2	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R3	299701-472	8	RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R4	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R5	299701-102	3	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R6	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R7	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R8	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R9	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R10	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R11	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R12	299701-470		RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R13	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R14	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R15	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R16	299701-180	1	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R17	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R18	299701-472		RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R19	299701-152	1	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
	294550	8	SOCKET:PIN	00779	1-331677-8

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A10A3	298901		PWB ASSY, 30-40 MHZ 8PF	56872	298901
C1	299250-301	4	CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FD301J03
C2	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FD301J03
C3	299241-120	2	CAP:FXD DIP MICA 12PF –+0.5PF 300WVDC	72136	DM5-120J
C4	299243-001	3	CAP:FXD DIP MICA .001UF –+20% 1000WVDC	56289	CK60AW102M
C5	299250-151	2	CAP:FXD DIP MICA 150PF –+5% 500WVDC	14655	CM04FD151J03
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C7	299250-151		CAP:FXD DIP MICA 150PF –+5% 500WVDC	14655	CM04FD151J03
C8	299241-100		CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C9	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C10	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C11	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
E1	293310		TERMINAL SOLDER	71279	1597-2-01
E4	293310		TERMINAL SOLDER	71279	1597-2-01
L1	231105	4	INDUCTOR:VAR .133UH	56872	231105-001
L2	231105-003	2	INDUCTOR:VAR .44UH	56872	231105-005
L3	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L4	231105-003		INDUCTOR:VAR .44UH	56872	231105-005
L5	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L6	231105-001		INDUCTOR:VAR .133UH	56872	231105-001
L7	299237-048	1	INDUCTOR:FXD MOLDED 15UH	99800	1025-048
Q1	294172	1	TRANSISTOR:NPN SI	04713	MPS918
R1	299701-161	3	RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R2	299701-472	1	RESISTOR:FXD CAR 4.7K –+5% 1/4W	01121	RC07GF472J
R3	299701-102	1	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R4	299701-161		RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R5	299701-161		RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
A10A4	298902		PWB ASSY, 330.5 MHZ POWER AMP	56872	298902
C1	299243-001	7	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C3	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C4	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C5	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C7	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C8	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C9	293746	3	CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C10	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C11	299241-100	2	CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C12	299241-100		CAP:FXD DIP MICA 10PF –+0.5PF 300WVDC	72136	DM5-100J
C13	299241-360	2	CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C14	293746		CAP:VAR TRMR CER 2-8PF	72982	538-011-A-2-8
C15	299241-360		CAP:FXD DIP MICA 36PF –+5% 300WVDC	72136	DM5-360J
C16	299240-001	1	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
E1	293310	1	TERMINAL SOLDER	71279	1597-2-01
E2	293305	2	TERMINAL, SOLDER	71279	1019-2-901
E3	293305		TERMINAL, SOLDER	71279	1019-2-901
L3	299237-012	2	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L4	298899-002	3	INDUCTOR:FXD .04UH	56872	298899-002
L5	298899-002		INDUCTOR:FXD .04UH	56872	298899-002
L6	298899-002		INDUCTOR:FXD .01UH	56872	298899-002
L7	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
M1	231105-003	1	MIXER	15542	SRA-1WH
Q1	299239-001	1	TRANSISTOR:NPN SI	04713	2N5179
Q2	29147		TRANSISTOR:NPN SI	04713	2N3866
Q3	29147		TRANSISTOR:NPN SI	04713	2N3866
R1	299701-180	1	RESISTOR:FXD CAR 18 OHM –+5% 1/4W	01121	RC07GF180J
R2	299701-622	2	RESISTOR:FXD CAR 6.2K –+5%	01121	RC07GF622J
R3	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4W	01121	RC07GF622J
R4	299701-681	1	RESISTOR:FXD CAR 680 OHM –+5% 1/4W	01121	RC07GF681J
R5	299701-301	2	RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R6	299701-102	2	RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R7	299701-201	1	RESISTOR:FXD CAR 200 OHM –+5% 1/4W	01121	RC07GF201J

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A10A4 R8	299701-102		RESISTOR:FXD CAR 1K –+5% 1/4W	01121	RC07GF102J
R9	299701-301		RESISTOR:FXD CAR 300 OHM –+5% 1/4W	01121	RC07GF301J
R10	299701-161	3	RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R15	299701-470	1	RESISTOR:FXD CAR 47 OHM –+5% 1/4W	01121	RC07GF470J
R16	299701-161		RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
R17	299701-161		RESISTOR:FXD CAR 160 OHM –+5% 1/4W	01121	RC07GF161J
	294550	8	SOCKET:PIN	00779	1-331677-8
	293994	1	TRANSIPAD	07047	10105-DAP
	293995	2	TRANSIPAD	07047	10027-DAP
A10A5	298900		PWB ASSY, 1-5 MHZ COMB GENERATOR	56872	298900
C1	299243-001	10	CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C2	299240-002	1	CAP:FXD DIP TA 22UF –+20% –5WVDC	05397	T368B226M015AS
C3	299240-001	1	CAP:FXD DIP TA 6.8UF –+20% 35WVDC	05397	T368B685M035AS
C4	299245-001	1	CAP:FXD CER .1UF –+20% 50WVDC	72982	8121-M058-W5R0-104M
C5	299245-002	8	CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C6	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C7	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C8	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C9	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C10	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C11	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C12	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C13	299243-001		CAP:FXD CER DISC .001UF –+20% 1000WVDC	56289	CK60AW102M
C14	299243-001		CAP:FXD CER DISC .001UF ±20% 1000WVDC	56289	CK60AW102M
C17	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C18	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C19	299250-132	2	CAP:FXD DIP MICA 1300PF –+5% 500WVDC	14655	CM06FD132J03
C20	299250-432	1	CAP:FXD DIP MICA 4300PF –+5% 500WVDC	14655	CM06FD432J03
C21	299250-132		CAP:FXD DIP MICA 1300PF –+5% 500WVDC	14655	CM06FD132J03
C22	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C23	299250-471	2	CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C24	299250-162	1	CAP:FXD DIP MICA 1600PF –+5% 500WVDC	14655	CM06FD162J03
C25	299250-471		CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C26	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C27	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-621-103M
C28	299250-301	4	CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C29	299250-002	2	CAP:FXD DIP MICA 1000PF –+5% 500WVDC	14655	CM06FD102J03
C30	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C31	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C32	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C33	299250-102		CAP:FXD DIP MICA 1000PF –+5% 500WVDC	14655	CM06FD102J03
C34	299250-301		CAP:FXD DIP MICA 300PF –+5% 300WVDC	14655	CM04FC301J03
C35	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
E1	2993305	7	TERMINAL:DISCONNECT	71279	1019-2-901
E2	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E3	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E4	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E5	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E6	293305		TERMINAL:DISCONNECT	71279	1019-2-901
E7	293305		TERMINAL:DISCONNECT	71279	1019-2-901
J1	299234-001	1	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-042	7	INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L2	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L3	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L4	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L5	299237-032	3	INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L6	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L7	299237-048	2	INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L8	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L9	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L10	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L11	299237-032		INDUCTOR:FXD MOLDED 3.3UH	99800	1025-032
L12	299237-028	4	INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028

Reference Designation	AILTECH Part Number	Qty	Description	Mfr. Code	Mfr Part Number
A10A5 L13	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L14	299237-028		INDUCTOR:FXD MOLDED 2.2UH	99800	1025-028
L15	299237-042		INDUCTOR:FXD MOLDED 8.2UH	99800	1025-042
L17	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
Q1	294172	1	TRANSISTOR:NPN SI	04713	MPS918
Q2	294153	4	TRANSISTOR:PNP SI	04713	2N4126
Q3	294153		TRANSISTOR:PNP SI	04713	2N4126
Q4	294153		TRANSISTOR:PNP SI	04713	2N4126
Q5	294172	3	TRANSISTOR:FET	98571	U1897E
Q6	294173		TRANSISTOR:FET	98571	U1897E
Q7	294173		TRANSISTOR:FET	98571	U1897E
Q8	294153		TRANSISTOR:PNP SI	04713	2N4126
R1	299701-362	1	RESISTOR:FXD CAR 3.6K -+5% 1/4W	01121	RC07GF362J
R2	299701-242	1	RESISTOR:FXD CAR 2.4K -+5% 1/4W	01121	RC07GF242J
R3	299701-471	2	RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R4	299701-301	3	RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R5	299701-472	4	RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R6	299701-471		RESISTOR:FXD CAR 470 OHM -+5% 1/4W	01121	RC07GF471J
R7	299701-201	3	RESISTOR:FXD CAR 200 OHM -+5% 1/4W	01121	RC07GF201J
R8	299701-201		RESISTOR:FXD CAR 200 OHM -+5% 1/4W	01121	RC07GF201J
R9	299701-201		RESISTOR:FXD CAR 200 OHM -+5% 1/4W	01121	RC07GF201J
R10	299701-680	3	RESISTOR:FXD CAR 68 OHM -+5% 1/4W	01121	RC07GF680J
R11	299701-680		RESISTOR:FXD CAR 68 OHM -+5% 1/4W	01121	RC07GF680J
R12	299701-680		RESISTOR:FXD CAR 68 OHM -+5% 1/4W	01121	RC07GF680J
R13	299701-102	4	RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R14	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R15	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R16	299701-203	3	RESISTOR:FXD CAR 20K -+5% 1/4W	01121	RC07GF203J
R17	299701-203		RESISTOR:FXD CAR 20K -+5% 1/4W	01121	RC07GF203J
R18	299701-203		RESISTOR:FXD CAR 20K -+5% 1/4W	01121	RC07GF203J
R19	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R20	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R21	299701-472		RESISTOR:FXD CAR 4.7K -+5% 1/4W	01121	RC07GF472J
R22	299701-180		RESISTOR:FXD CAR 18 OHM -+5% 1/4W	01121	RC07GF180J
R23	299701-301		RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R24	299701-301		RESISTOR:FXD CAR 300 OHM -+5% 1/4W	01121	RC07GF301J
R25	299701-152	2	RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
R26	299701-102		RESISTOR:FXD CAR 1K -+5% 1/4W	01121	RC07GF102J
R28	299701-152		RESISTOR:FXD CAR 1.5K -+5% 1/4W	01121	RC07GF152J
U1	293622	1	IC:TTL BINARY COUNTER	72314	93S16DM
U2	293350	3	IC:TTL DUAL J-K FF	01295	SN74S113N
U3	293350		IC:TTL DUAL J-K FF	01295	SN74S113N
U4	293350	1	IC:TTL DUAL J-K FF	01295	SN74S113N
U5	293350	1	IC:TTL DUAL J-K FF	01295	SN74S113N
U6	293634	2	IC:TTL QUAD 2 INPUT NOR GATES	01295	SN7408N
	294567	2	SOCKET, 16 PIN	01295	C931610
	294573	4	SOCKET, 14 PIN	01295	C931410
A10A6	298903		PWB ASSY, 29 MHZ CONVERTER	56872	298903
C1	299250-391	2	CAP:FXD DIP MICA 390PF -+5% 100WVDC	14655	CM04FA391J03
C2	299241-820	2	CAP:FXD DIP MICA 82PF -+5% 300WVDC	72136	DM5-820J
C3	299241-820		CAP:FXD DIP MICA 82PF -+5% 300WVDC	72136	DM5-820J
C4	299250-391		CAP:FXD DIP MICA 390PF -+5% 100WVDC	14655	CM04FA391J03
C6	299245-002	5	CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C7	299245-002		CAP:FXD CER .01UF -+20% 50WVDC	72982	8121-M058-651-103M
C8	299241-680	14	CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C10	299241-030	4	CAP:FXD DIP MICA 3PF -+0.5PF 300WVDC	72136	DM5-030D
C11	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C12	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C13	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C14	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C15	299241-680		CAP:FXD DIP MICA 68PF -+5% 300WVDC	72136	DM5-680J
C16	299241-680		CAP:FXD DIP MICA 67PF -+5% 300WVDC	72136	DM5-680J
C17	299241-020	6	CAP:FXD DIP MICA 2PF -+0.5PF 300WVDC	72136	DM5-020D
C18	299241-020		CAP:FXD DIP MICA 2PF -+0.5PF 300WVDC	72136	DM5-020D

Reference Designation	AILTECH Part Number	Qty	Description	Mfr Code	Mfr Part Number
A10A6 C19	299241-020		CAP:FXD DIP MICA 2PF –+0.5PF 300WVDC	72136	DM5-020D
C20	299241-030		CAP:FXD DIP MICA 3PF –+0.5PF 300WVDC	72136	DM5-030D
C21	299241-750	2	CAP:FXD DIP MICA 75PF –+5% 300WVDC	72136	DM5-750J
C22	299250-471	3	CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C24	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C25	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C26	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C28	299241-030		CAP:FXD DIP MICA 3PF –+0.5PF 300WVDC	72136	DM5-030D
C29	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C30	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C31	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C32	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C33	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C34	299241-680		CAP:FXD DIP MICA 68PF –+5% 300WVDC	72136	DM5-680J
C36	299241-020		CAP:FXD DIP MICA 2PF –+0.5PF 300WVDC	72136	DM5-020D
C37	299241-020		CAP:FXD DIP MICA 2PF –+0.5PF 300WVDC	72136	DM5-020D
C38	299241-020		CAP:FXD DIP MICA 2PF –+0.5PF 300WVDC	72136	DM5-020D
C39	299241-030		CAP:FXD DIP MICA 3PF –+0.5PF 300WVDC	72136	DM5-030D
C40	288241-750		CAP:FXD DIP MICA 75PF –+5% 300WVDC	72136	DM5-750J
C41	299250-471		CAP:FXD DIP MICA 470PF –+5% 500WVDC	14655	CM06FD471J03
C42	299245-002		CAP:FXD CER .01UF –+20% 50WVDC	72982	8121-M058-651-103M
C43	299240-001	1	CAP:FXD DIP TA 6.8PF –+20% 35WVDC	05397	T368B685M035AS
C44	299250-471		CAP:FXD DIP MICA 470PF 5% 500WVDC	14655	CM06FD471J03
E1	293310		TERMINAL SOLDER	71279	1597-2-01
J10	299235-001	2	CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
J11	299235-001		CONNECTOR:RT ANGLE PC BD	98291	51-353-0000
L1	299237-044	1	INDUCTOR:FXD MOLDED 10. OUH	99800	1025-044
L2	231105-003	18	INDUCTOR:VAR .44UH	56872	231105-003
L3	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L5	299237-048	7	INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L6	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L7	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L8	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L9	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L10	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L11	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L12	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L13	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L14	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L15	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L17	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L18	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L19	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L20	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L21	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L22	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L23	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L24	231105-003		INDUCTOR:VAR .44UH	56872	231105-003
L26	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L27	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L28	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L29	299237-048		INDUCTOR:FXD MOLDED 15UH	99800	1025-048
L30	299237-012	2	INDUCTOR:FXD MOLDED .47UH	99800	1025-012
L31	299237-012		INDUCTOR:FXD MOLDED .47UH	99800	1025-012
M1	231104-001	1	MIXER	15542	SRA-1
Q1	294151	2	TRANSISTOR:NPN SI	04713	2N4124
Q2	294151		TRANSISTOR:NPN SI 1.5K	04713	2N4124
R1	299701-152	2	RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
R2	299701-622	4	RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R3	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R4	299701-681	1	RESISTOR:FXD CAR 680 OHM –+5% 1/4	01121	RC07GF681J
R6	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R7	299701-622		RESISTOR:FXD CAR 6.2K –+5% 1/4	01121	RC07GF622J
R8	299701-471	1	RESISTOR:FXD CAR 470 OHM –+5% 1/4W	01121	RC07GF471J
R9	299701-152		RESISTOR:FXD CAR 1.5K –+5% 1/4W	01121	RC07GF152J
	294550	8	SOCKET	00779	1-331677-8
	293994	1	TRANSIPAD	07047	10105-DAP

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